TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP179D

Measurement Instruments
Logic IC Testers / Memory Testers
Board Testers / Scanners

The TOSHIBA TLP179D Mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP179D consists of a GaAs infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

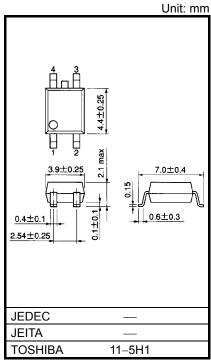
Its characteristics include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measurement instruments.

Features

• 4 pin SOP (2.54SOP4) : 2.1 mm high, 2.54 mm pitch

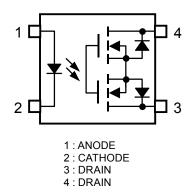
• 1-Form-A

Peak OFF-State Voltage : 200 V (min)
 Trigger LED Current : 3 mA (max)
 ON-State Current : 50 mA (max)
 ON-State Resistance : 50 Ω (max)
 Output Capacitance : 20 pF (max)
 Isolation Voltage : 1500 Vrms (min)

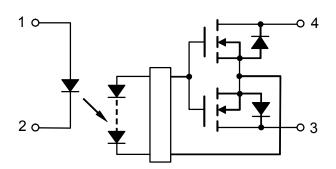


Weight: 0.1 g (typ.)

Pin Configuration (top view)



Schematic



Absolute Maximum Ratings (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT
	Forward Current	lF	50	mA
E	Forward Current Derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
۳	Reverse Voltage	V _R	5	V
	Junction Temperature	Tj	125	°C
~	OFF-State Output Terminal Voltage	V _{OFF}	200	V
CTO	ON-State Current	I _{ON}	50	mA
ETECTOR	ON-State Current Derating (Ta ≥ 25°C)	Δl _{ON} /°C	-0.5	mA/°C
	Junction Temperature	Tj	125	°C
Stora	ge Temperature Range	T _{stg}	-55 to 125	°C
Oper	ating Temperature Range	T _{opr}	-40 to 85	°C
Lead	Soldering Temperature (10 s)	T _{sol}	260	°C
Isolat	ion Voltage (AC, 1 minute, R.H.≤ 60%) (Note1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Device considered a two-terminal device : LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	Min	Тур.	Max	UNIT
Supply Voltage	V_{DD}	_	_	160	V
Forward Current	lF	5	7.5	15	mA
ON-State Current	I _{ON}	_	_	50	mA
Operating Temperature	T _{opr}	-20	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
	Forward Voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
DETECTOR	OFF-State Current	l _{OFF}	V _{OFF} = 160 V	_	_	1	nA
	Capacitance	C _{OFF}	V = 0, f = 1 MHz		15	20	pF

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Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Trigger LED Current	I _{FT}	I _{ON} = 50 mA	_	1	3	mA
Return LED Current	I _{FC}	I _{OFF} = 100 μA	0.1	_	_	mA
ON-State Resistance	R _{ON}	$I_{ON} = 50 \text{ mA}, I_F = 5 \text{ mA}$	_	40	50	Ω

Isolation Characteristics (Ta = 25°C)

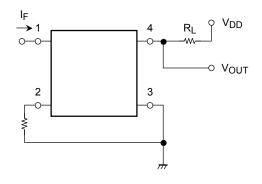
CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Capacitance Input to Output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation Voltage	BV_S	BV _S AC, 1 second (in oil)	_	3000	_	VIIIIS
		DC, 1 minute (in oil)	_	3000	_	Vdc

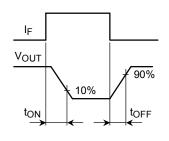
Switching Characteristics (Ta = 25°C)

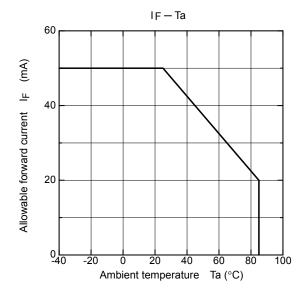
CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Turn-on Time	t _{ON}	$R_L = 200 \Omega$ (Note2)	_	0.03	0.5	ms
Turn-off Time	tOFF	$V_{DD} = 10 \text{ V}, I_F = 5 \text{ mA}$	_	0.07	0.2	1115

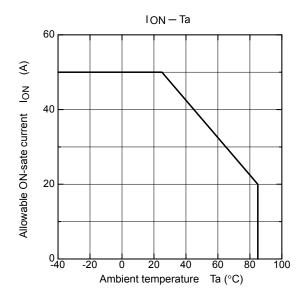
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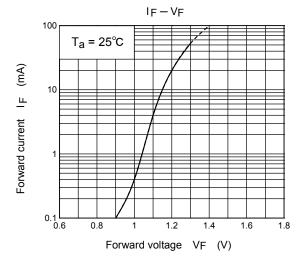
Note2 : Switching Time Test Circuit

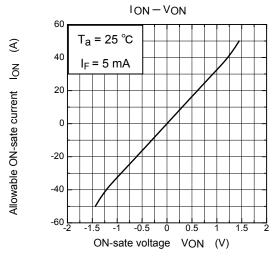


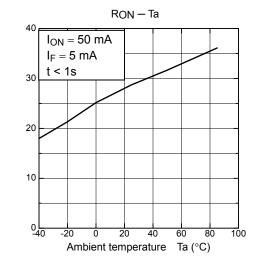




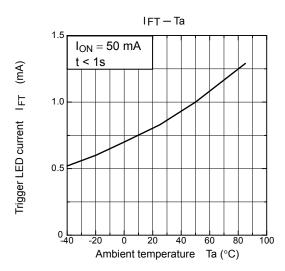


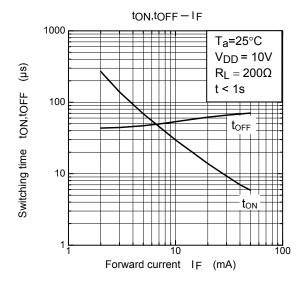


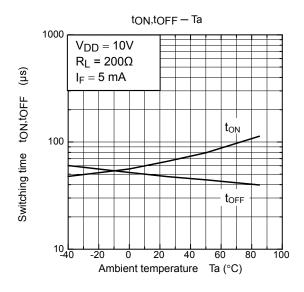


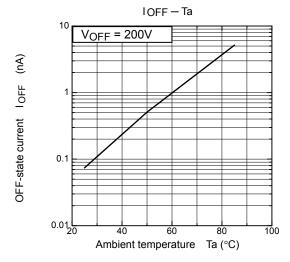


ON-sate resistance R_{ON} (Ω)









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