

VS-ETU3006SHM3, VS-ETU3006-1HM3

Vishay Semiconductors

Ultrafast Rectifier, 30 A FRED Pt®





VS-ETU3006SHM3

Base cathode

2

1 3

N/C

Anode

D²PAK



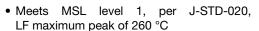
N/C

TO-262

PRODUCT SUMMARY				
Package	TO-263AB (D ² PAK), TO-262AA			
I _{F(AV)}	30 A			
V_{R}	600 V			
V _F at I _F	2 V			
t _{rr} (typ.)	30 ns			
T_J max.	175 °C			
Diode variation	Single die			

FEATURES

- Low forward voltage drop
- · Ultrafast recovery time
- 175 °C operating junction temperature
- · Low leakage current
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test



 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912









DESCRIPTION/APPLICATIONS

Ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 113 °C	30	^
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	200	A
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	
Forward valtage	V	I _F = 30 A	-	1.4	2.0	V
Forward voltage V _F	I _F = 30 A, T _J = 150 °C	-	1.15	1.35		
Devene leekene euwent		$V_R = V_R$ rated	-	0.02	30	
Reverse leakage current I _R		T _J = 150 °C, V _R = V _R rated	-	30	250	μΑ
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	30	45	
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	45	-	ns A nC
		T _J = 125 °C		-	100	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	5.6	-	
		T _J = 125 °C		-	10	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	127	-	
		T _J = 125 °C]	-	580	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	0.95	1.4	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
vveigni			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Madina da ta		Case style D ² PAK modified	ETU3006SH			
Marking device		Case style TO-262	ETU3006-1H			

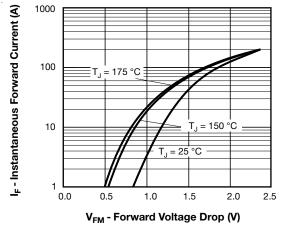


Fig. 1 - Typical Forward Voltage Drop Characteristics

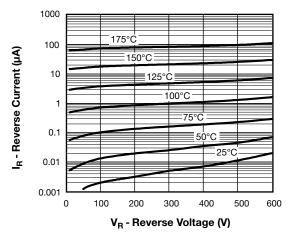


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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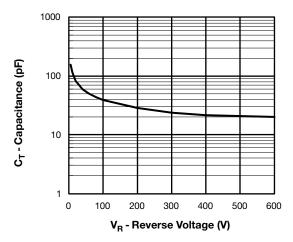


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

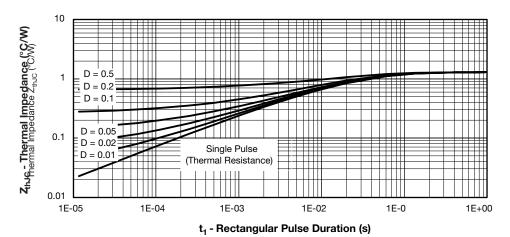


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

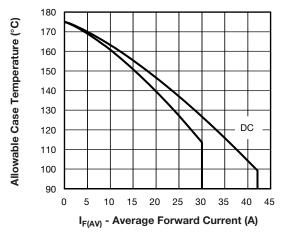


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

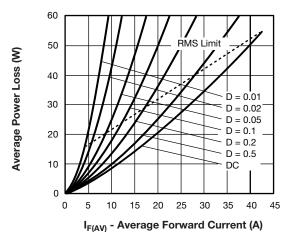


Fig. 6 - Forward Power Loss Characteristics

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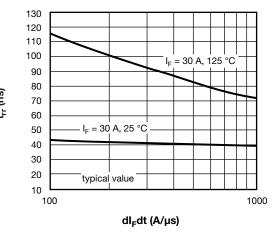


Fig. 7 - Typical Reverse Recovery vs. dl_F/dt

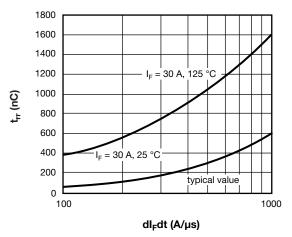


Fig. 8 - Typical Stored Charge vs. dl_F/dt

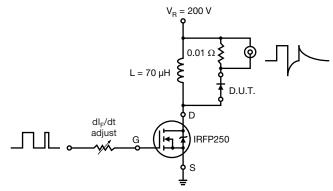
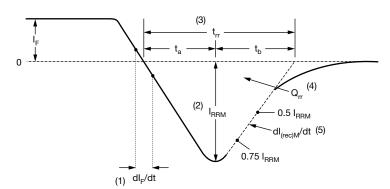


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

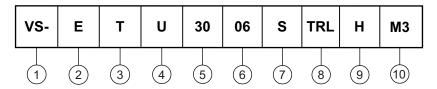
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Circuit configuration
E = Single diode

3 - T = TO-220

U = Ultrafast recovery time

5 - Current code (30 = 30 A)

6 - Voltage code (06 = 600 V)

7 - • S = D²PAK

• -1 = TO-262

8 - • None = Tube

- • TRL = Tape and reel (left oriented, for D²PAK package)

- • TRR = Tape and reel (right oriented, for D²PAK package)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETU3006SHM3	50	1000	Antistatic plastic tube		
VS-ETU3006-1HM3	50	1000	Antistatic plastic tube		
VS-ETU3006STRRHM3	800	800	13" diameter reel		
VS-ETU3006STRLHM3	800	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046		
Dimensions	TO-262AA	www.vishay.com/doc?95419		
Dort marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032		



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