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June 2004 Revised June 2004

FXL5T244

Low Voltage Dual Supply 5-Bit Signal Translator with Configurable Voltage Supplies and Signal Levels and 3-STATE Outputs

General Description

The FXL5T244 is a configurable dual-voltage-supply translator designed for one-way (unidirectional) voltage translation of signals between two voltage levels. The device allows translation between voltages as high as 3.6V to as low as 1.1V. A Inputs and the $\overline{\text{OE}}$ Control Pin track the V_{CCI} level, and Y Outputs track the V_{CCO} level. Both inputs and outputs are designed to accept supply voltage levels from 1.1V to 3.6V. This allows for unidirectional voltage translation over a variety of voltage levels: 1.2V, 1.5V, 1.8V, 2.5V, and 3.3V.

The device remains in 3-STATE until both $V_{CC}s$ reach active levels allowing either V_{CC} to be powered-up first. The device also contains power down control circuits that place the device in 3-STATE if either V_{CC} is removed. The \overline{OE} input, when HIGH, disables the outputs by placing them in 3-STATE condition.

Features

- One-way (unidirectional) translation between any 2 levels from 1.1V to 3.6V
- \blacksquare Fully configurable, inputs and outputs track respective V_{CC} levels
- Non-preferential power-up sequencing; either V_{CC} may be powered-up first
- Outputs remain in 3-STATE until active V_{CC} level is reached
- Outputs switch to 3-STATE if either V_{CC} is at GND
- Power-off protection
- Control input (OE) level is referenced to V_{CCI} voltage
- Packaged in 14-terminal DQFN (2.5mm x 3.0mm) package
- ESD protection exceeds:
 - 4kV HBM ESD (per JESD22-A114 & Mil Std 883e 3015.7)
 - 8kV HBM I/O to GND ESD (per JESD22-A114 & Mil Std 883e 3015.7)
 - 1kV CDM ESD (per ESD STM 5.3)
 - 200V MM ESD (per JESD22-A115 & ESD STM5.2)

Ordering Code:

Order Number	Package Number	Package Description
FXL5T244BQX		14-Terminal Depopulated Quad Very-Thin Flat Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 3.0mm

Terminal Descriptions

Terminal Names	Description
ŌĒ	Output Enable Input
A _n	Data Inputs
Y _n	3-STATE Outputs
V _{CCI}	Inputs Power Supply
V _{cco}	Outputs Power Supply
GND	Ground

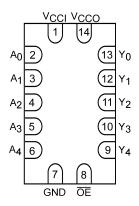
Truth Table

Inp	Outputs	
OE	A _n	Y _n
L	L	L
L	Н	Н
Н	X	3-STATE
H = HIGH Voltage Level		

H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care

Connection Diagram

Terminal Assignments for DQFN



(Top View)

Terminal Assignment

Terminal Number	Terminal Name
1	V _{CCI}
2	A ₀
3	A ₁
4	A ₂
5	A ₃ A ₄
6	A ₄
7	GND
8	ŌĒ
9	Y ₄
10	Y ₃
11	Y ₂
12	Y ₁
13	Y ₀
14	V _{cco}

Power-Up/Power-Down Sequencing

FXL translators offer an advantage in that either V_{CC} may be powered up first. This benefit derives from the chip design. When either V_{CC} is at 0 volts, outputs are in a HIGH-Impedance state. The control input, \overline{OE} , is designed to track the V_{CCI} supply. A pull-up resistor tying \overline{OE} to V_{CCI} should be used to ensure that bus contention, excessive currents, or oscillations do not occur during power-up/power-down. The size of the pull-up resistor is based upon the current-sinking capability of the \overline{OE} driver.

The recommended power-up sequence is the following:

- 1. Apply power to either V_{CC} .
- Apply power to the OE input (Logic HIGH for A-to-B operation; Logic LOW for B-to-A operation) and to the respective data inputs (A Port or B Port). This may occur at the same time as Step 1.
- 3. Apply power to other V_{CC}.
- 4. Drive the $\overline{\text{OE}}$ input LOW to enable the device.

The recommended power-down sequence is the following:

- 1. Drive $\overline{\text{OE}}$ input HIGH to disable the device.
- 2. Remove power from either V_{CC} .
- 3. Remove power from other V_{CC} .

0.0V to 3.6V

Absolute Maximum Ratings(Note 1)

Recommended Operating Conditions (Note 3)

Supply Voltage

-0.5V to +4.6V V_{CCI}

-0.5V to +4.6V V_{CCO} DC Input Voltage (V_I) -0.5V to +4.6V

Output Voltage (V_O) (Note 2)

Outputs 3-STATE

Outputs Active -0.5V to $V_{CCO} + 0.5V$ DC Input Diode Current (I $_{IK}$) $V_I < 0V$ -50 mA

DC Output Diode Current (IOK)

 $V_O < 0V$ -50 mA $V_{O} > V_{CC}$ +50 mA

DC Output Source/Sink Current

 (I_{OH}/I_{OL})

 $\operatorname{DC}\operatorname{V}_{\operatorname{CC}}$ or Ground Current per

Supply Pin (I_{CC} or GND) ±100 mA $-65^{\circ}C$ to $+150^{\circ}C$

Storage Temperature Range (T_{STG})

Power Supply Operating (V_{CCI} or V_{CCO}) 1.1V to 3.6V

Input Voltage

-0.5V to +4.6V

-50 mA / +50 mA

0.0V to 3.6V A_n

Control Inputs (OE)

Output Current in I_{OH}/I_{OL}

 $V_{CCO} = 3.0V \text{ to } 3.6V$ ±24 mA

 $V_{CCO} = 2.3V$ to 2.7V±18 mA

 $V_{CCO} = 1.65V$ to 1.95V ±6 mA $V_{CCO} = 1.4V \text{ to } 1.65V$ ±2 mA

 $V_{CCO} = 1.1V \text{ to } 1.4V$ ±0.5 mA -40°C to +85°C

Free Air Operating Temperature (T_A) Minimum Input Edge Rate $(\Delta V/\Delta t)$

 $V_{CCA/B} = 1.1V$ to 3.6V10 ns/V

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: In Absolute Maximum Rating must be observed. Note 3: All unused inputs must be held at V_{CCI} or GND.

DC Electrical Characteristics

Symbol	Parameter	Conditions	V _{CCI} (V)	V _{CCO} (V)	Min	Max	Units
V _{IH}	High Level Input Voltage		2.7 - 3.6	. ,	2.0		
			2.3 - 2.7		1.6		
			1.65 - 2.3	1.1 - 3.6	0.65 x V _{CCI}		V
			1.4 - 1.65		0.65 x V _{CCI}		
			1.1 - 1.4		0.9 x V _{CCI}		
V _{IL}	Low Level Input Voltage		2.7 - 3.6			0.8	
			2.3 - 2.7			0.7	
			1.65 - 2.3	1.1 - 3.6		0.35 x V _{CCI}	V
			1.4 - 1.65			0.35 x V _{CCI}	
			1.1 - 1.4			0.1 x V _{CCI}	
V _{OH}	High Level Output Voltage	$I_{OH} = -100 \mu A$	1.1 - 3.6	1.1 - 3.6	V _{CC0} - 0.2		
		$I_{OH} = -12 \text{ mA}$	2.7	2.7	2.2		
		$I_{OH} = -18 \text{ mA}$	3.0	3.0	2.4		
		$I_{OH} = -24 \text{ mA}$	3.0	3.0	2.2		
		$I_{OH} = -6 \text{ mA}$	2.3	2.3	2.0		V
		$I_{OH} = -12 \text{ mA}$	2.3	2.3	1.8		•
		$I_{OH} = -18 \text{ mA}$	2.3	2.3	1.7		
		$I_{OH} = -6 \text{ mA}$	1.65	1.65	1.25		
		$I_{OH} = -2 \text{ mA}$	1.4	1.4	1.05		
		$I_{OH} = -0.5 \text{ mA}$	1.1	1.1	0.75 x V _{CC0}		

DC Electrical Characteristics (Continued)

Symbol	Parameter	Conditions	V _{CCI} (V)	v _{cco}	Min	Max	Units
V _{OL}	Low Level Output Voltage	$I_{OL} = 100\mu A$	1.1 - 3.6	1.1- 3.6		0.2	
		I _{OL} = 12 mA	2.7	2.7		0.4	
		I _{OL} = 18 mA	3.0	3.0		0.4	
		I _{OL} = 24 mA	3.0	3.0		0.55	
		I _{OL} = 12 mA	2.3	2.3		0.4	V
		I _{OL} = 18 mA	2.3	2.3		0.6	
		I _{OL} = 6 mA	1.65	1.65		0.3	
		I _{OL} = 2 mA	1.4	1.4		0.35	
		$I_{OL} = 0.5 \text{ mA}$	1.1	1.1		0.3 x V _{CCI}	
I _I	Input Leakage Current	$V_I = V_{CCI}$ or GND	1.1 - 3.6	3.6		±1.0	μΑ
I _{OFF}	Power Off Leakage Current	A_n , $\overline{OE} = 0V$ to 3.6V	0	3.6		±10.0	μА
		$Y_n = 0V \text{ to } 3.6V$	3.6	0		±10.0	μА
I _{OZ}	3-STATE Output Leakage	OE = V _{IH}	3.6	3.6		±10.0	
(Note 4)	$0 \le V_O \le 3.6V$	OE = Don't Care	0	3.6		+10.0	μΑ
	$V_I = V_{IH}$ or V_{IL}	OE = Don't Care	3.6	0		+10.0	
I _{CCI/O} (Note 4)	Quiescent Supply Current	$V_I = V_{CCI}$ or GND; $I_O = 0$	1.1 - 3.6	1.1 - 3.6		20.0	μА
I _{CCZ} (Note 5)	Quiescent Supply Current	$V_I = V_{CCI}$ or GND; $I_O = 0$	1.1 - 3.6	1.1 - 3.6		20.0	μΑ
I _{CCI}	Quiescent Supply Current	$V_I = V_{CCI}$ or GND; $I_O = 0$	0	1.1 - 3.6		-10.0	μΑ
		$V_I = V_{CCI}$ or GND; $I_O = 0$	1.1 - 3.6	0		10.0	μΑ
I _{CCO}	Quiescent Supply Current	$V_I = V_{CCO}$ or GND; $I_O = 0$	1.1 - 3.6	0		-10.0	μΑ
		$V_I = V_{CCO}$ or GND; $I_O = 0$	0	1.1 - 3.6		10.0	μΑ
ΔI _{CCI/O}	Increase in I _{CC} per Input;	V _{IH} = 3.0	3.6	3.6		500	μΑ
	Other Inputs at V _{CC} or GND						

Note 4: Don't Care = Any valid logic level.

Note 5: Reflects current per supply, $\rm V_{CCI}$ or $\rm V_{CCO}.$

AC Electrical Characteristics $v_{\text{CCI}} = 3.0 v \ \text{to} \ 3.6 \text{V}$

	Parameter	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$										
Symbol		V _{CCO} = 3.0V to 3.6V		V _{CCO} = 2.3V to 2.7V		V _{CCO} = 1.65V to 1.95V		V _{CCO} = 1.4V to 1.6V		V _{CCO} = 1.1V to 1.3V		Units
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	†
t _{PLH} , t _{PHL}	Propagation Delay A to Y	0.2	3.5	0.3	3.9	0.7	5.4	0.8	6.8	1.4	22.0	ns
t _{PZH} , t _{PZL}	Output Enable OE to Y	0.5	4.0	0.7	4.4	1.0	5.9	1.0	6.4	1.5	17.0	ns
t _{PHZ} , t _{PLZ}	Output Disable OE to Y	0.2	3.8	0.2	4.0	0.7	4.8	1.5	6.2	2.0	17.0	ns

AC Electrical Characteristics $v_{\text{CCI}} = 2.3 \text{V to } 2.7 \text{V}$

Symbol	Parameter	$T_A = -40^{\circ}C$ to $+85^{\circ}C$										
		V _{CCO} = 3.0V to 3.6V		V _{CCO} = 2.3V to 2.7V		V _{CCO} = 1.65V to 1.95V		V _{CCO} = 1.4V to 1.6V		V _{CCO} = 1.1V to 1.3V		Units
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	1
t _{PLH} , t _{PHL}	Propagation Delay A toY	0.2	3.8	0.4	4.2	0.5	5.6	0.8	6.9	1.4	22.0	ns
t _{PZH} , t _{PZL}	Output Enable OE to Y	0.6	4.2	0.8	4.6	1.0	6.0	1.0	6.8	1.5	17.0	ns
t_{PHZ},t_{PLZ}	Output Disable OE to Y	0.2	4.1	0.2	4.3	0.7	4.8	1.5	6.7	2.0	17.0	ns

AC Electrical Characteristics $v_{\text{CCI}} = 1.65 \text{V to } 1.95 \text{V}$

Symbol	Parameter	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$										
		V _{CCO} = 3.0V to 3.6V			V _{CCO} = 2.3V to 2.7V		V _{CCO} = 1.65V to 1.95V		V _{CCO} = 1.4V to 1.6V		V _{CCO} = 1.1V to 1.3V	
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Ì
t _{PLH} , t _{PHL}	Propagation Delay A to Y	0.3	4.0	0.5	4.5	0.8	5.7	0.9	7.1	1.5	22.0	ns
t _{PZH} , t _{PZL}	Output Enable OE to Y	0.6	5.2	0.8	5.4	1.2	6.9	1.2	7.2	1.5	18.0	ns
t _{PHZ} , t _{PLZ}	Output Disable OE to Y	0.2	5.1	0.2	4.0	0.8	5.2	1.5	7.0	2.0	17.0	ns

AC Electrical Characteristics $v_{\text{CCI}} = 1.4 v \text{ to } 1.6 v$

Symbol	Parameter	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$										
		V _{CCO} = 3.0V to 3.6V		V _{CCO} = 2.3V to 2.7V		V _{CCO} = 1.65V to 1.95V		V _{CCO} = 1.4V to 1.6V		V _{CCO} = 1.1V to 1.3V		Units
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	1
t _{PLH} , t _{PHL}	Propagation Delay A to Y	0.5	4.3	0.5	4.8	1.0	6.0	1.0	7.3	1.5	22.0	ns
t _{PZH} , t _{PZL}	Output Enable OE to Y	1.1	7.5	1.1	7.6	1.3	7.7	1.4	7.9	2.0	20.0	ns
t _{PHZ} , t _{PLZ}	Output Disable OE to Y	0.4	6.1	0.4	6.2	0.9	6.2	1.5	7.5	2.0	18.0	ns

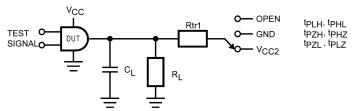
AC Electrical Characteristics $v_{\text{CCI}} = 1.1 \text{V to } 1.3 \text{V}$

Symbol	Parameter	T _A = -40°C to +85°C										
		V _{CCO} = 3.0V to 3.6V			V _{CCO} = 2.3V to 2.7V		V _{CCO} = 1.65V to 1.95V		co = o 1.6V	V _{CCO} = 1.1V to 1.3V		Units
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Ī
t _{PLH} , t _{PHL}	Propagation Delay A to Y	0.8	13.0	1.0	7.0	1.2	8.0	1.3	9.5	2.0	24.0	ns
t _{PZH} , t _{PZL}	Output Enable OE to Y	1.0	12.0	1.0	9.0	2.0	10.0	2.0	11.0	2.0	24.0	ns
t _{PHZ} , t _{PLZ}	Output Disable OE to Y	1.0	15.0	0.7	7.0	1.0	8.0	2.0	10.0	2.0	20.0	ns

Capacitance

Symbol	Parameter	Conditions	$T_A = +25^{\circ}C$	Units
Cymbol	r arameter	Conditions	Typical	Onits
C _{IN}	Input Capacitance An Control Pin (OE)	$V_{CCI} = V_{CCO} = 3.3V$, $V_I = 0V$ or V_{CCI}	4.0	pF
C _{OUT}	Output Capacitance Y _n	$V_{CCI} = V_{CCO} = 3.3V$, $V_I = 0V$ or V_{CCI}	5.0	pF
C _{PD}	Power Dissipation Capacitance	$V_{CCI} = V_{CCO} = 3.3V$, $V_I = 0V$ or V_{CCI} , $F = 10$ MHz	20.0	pF

AC Loading and Waveforms

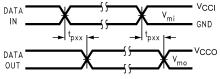


TEST	SWITCH OPEN			
t _{PLH} , t _{PHL}				
t _{PLZ} , t _{PZL}	V_{CCO} x 2 at V_{CCO} = 3.3 ± 0.3V, 2.5V ± 0.2V, 1.8V ± 0.15V, 1.5V ± 0.1V, 1.2V ± 0.1V			
t _{PHZ} , t _{PZH}	GND			

FIGURE 1. AC Test Circuit

AC Load Table

V _{cco}	V _{CCO} C _L		Rtr1	
1.2V ± 0.1V	15 pF	2 kΩ	2 kΩ	
1.5V ± 0.1V	15 pF	2 kΩ	2 kΩ	
1.8V ± 0.15V	15 pF	2 kΩ	2 kΩ	
2.5V ± 0.2V	15 pF	2 kΩ	2 kΩ	
$3.3V \pm 0.3V$	15 pF	2 kΩ	2 kΩ	



OUTPUT CONTROL Vmi GND GND CONTROL Vmo S Vput Vol

Note: Input $t_R = t_F = 2.0 \text{ ns}$, 10% to 90%

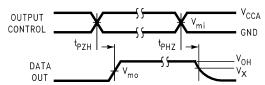
Input $\rm t_R = t_F = 2.5 ns,~10\%$ to 90%, @ $\rm V_I = 3.0 V$ to 3.6V only

FIGURE 2. Waveform for Inverting and Non-Inverting Functions

Note: Input $t_R = t_F = 2.0 \text{ ns}$, 10% to 90%

Input $t_{\mbox{\scriptsize R}}=t_{\mbox{\scriptsize F}}=2.5 \mbox{ns},\, 10\%$ to 90%, @ $V_{\mbox{\scriptsize I}}=3.0 \mbox{\scriptsize V}$ to 3.6V only

FIGURE 3. 3-STATE Output Low Enable and Disable Times for Low Voltage Logic



Note: Input $t_R = t_F = 2.0$ ns, 10% to 90%

Input $t_{R}=t_{F}=2.5 ns,\,10\%$ to 90%, @ $V_{I}=3.0 V$ to 3.6V only

FIGURE 4. 3-STATE Output High Enable and Disable Times for Low Voltage Logic

Symbol	V _{CC}				
	$3.3V \pm 0.3V$	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V
V _{mi}	V _{CCI} /2	V _{CCI} /2	V _{CCI} /2	V _{CCI} /2	V _{CCI} /2
V _{mo}	V _{CCO} /2	V _{CCO} /2	V _{CCO} /2	V _{CCO} /2	V _{CCO} /2
V _X	V _{OH} – 0.3V	V _{OH} – 0.15V	V _{OH} – 0.15V	V _{OH} – 0.1V	V _{OH} – 0.1V
V _Y	V _{OL} + 0.3V	V _{OL} + 0.15V	V _{OL} + 0.15V	V _{OL} + 01V	V _{OL} + 01V

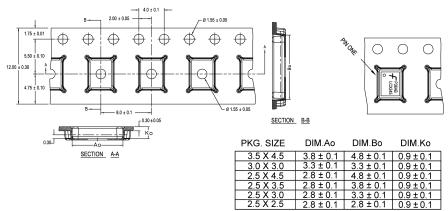
Note: For V_{mi} : $V_{CCI} = V_{CCA}$ for Control Pins T/\overline{R} and \overline{OE} , or $V_{CCA}/2$

Tape and Reel Specification

Tape Format for DQFN

Package	Tape	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
BQX	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)



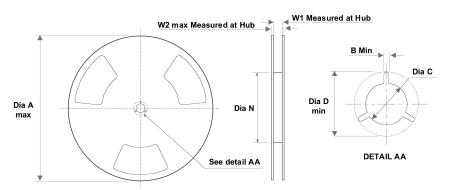
DIMENSIONS ARE IN MILLIMETERS

NOTES: unless otherwise specified

- 1. Cummulative pitch for feeding holes and cavities (chip pockets) not to exceed 0.008[0.20] over 10 pitch span.

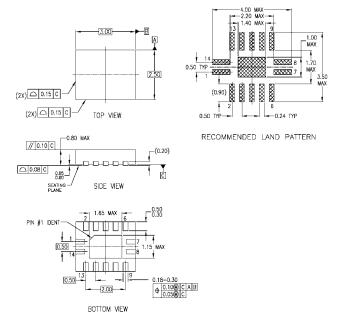
- 2. Smallest allowable bending radius.
 3. Thru hole inside cavity is centered within cavity.
 4. Tolerance is ±0.002[0.05] for these dimensions on all 12mm tapes.
 5. Ao and Bo measured on a plane 0.120[0.30] above the bottom of the pocket.
- 6. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
 7. Pocket position relative to sprocket hole measured as true position of pocket. Not pocket hole.
- 8. Controlling dimension is millimeter. Diemension in inches rounded.

REEL DIMENSIONS inches (millimeters)



Tape Size	Α	В	С	D	N	W1	W2
12 mm	13.0	0.059	0.512	0.795	2.165	0.488	0.724
	(330.0)	(1.50)	(13.00)	(20.20)	(55.00)	(12.4)	(18.4)

Physical Dimensions inches (millimeters) unless otherwise noted



NOTES:

- A. CONFORMS TO JEDEC REGISTRATION MO-241, VARIATION AA
 B. DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994

MLP014ArevA

14-Terminal Depopulated Quad Very-Thin Flat Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 3.0mm Package Number MLP014A

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