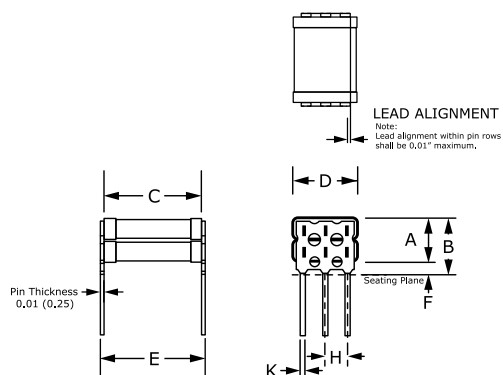


KPS-MCC Indust C0G HT200C, Ceramic, 0.6 uF, 10%, 100 VDC, C0G



Dimensions

D	6.35mm +/-0.635mm
L	6.35mm MIN
H	2.54mm NOM
F	1.397mm +/-0.25mm
A	5.3mm MAX
B	7.078mm MAX
C	6.35mm +/-0.635mm
E	7.62mm MAX
K	0.5mm NOM

Packaging Specifications

Packaging:	Waffle, Box
Packaging Quantity:	50

General Information

Series:	KPS-MCC Indust C0G HT200C
Style:	Leaded Stacked Chip
Description:	Low ESR, Stacked Ceramic Chips
Features:	200C, Low ESR, High Thermal Stability, Bulk Capacitance
RoHS:	No
Prop 65:	WARNING: Cancer and reproductive harm - www.p65warnings.ca.gov .
REACH:	SVHC (Pb – CAS 7439-92-1)
Termination:	Silver
Lead:	Straight Leads
Notes:	Number of chips in this stack: 2

Specifications

Capacitance:	0.6 uF
Capacitance Tolerance:	10%
Voltage DC:	100 VDC
Dielectric Withstanding Voltage:	250 VDC
Temperature Range:	-55/+200°C
Temperature Coefficient:	C0G
Dissipation Factor:	0.10% 1kHz 25C
Aging Rate:	0% Loss/Decade Hour
Insulation Resistance:	1.67 GOhms