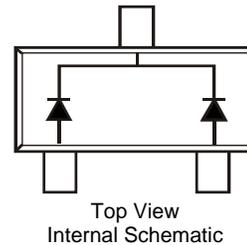


## Features

- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automated Insertion
- For General Purpose Switching Applications
- High Conductance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Notes 3 & 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.008 grams (approximate)

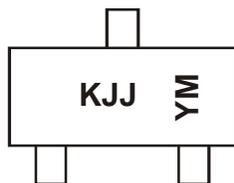


## Ordering Information (Note 5)

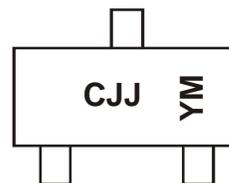
Part Number	Qualification	Case	Packaging
BAV70-7-F	Commercial	SOT23	3,000/Tape & Reel
BAV70-13-F	Commercial	SOT23	10,000/Tape & Reel
BAV70Q-7-F	Automotive	SOT23	3,000/Tape & Reel
BAV70Q-13-F	Automotive	SOT23	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Product manufactured with Date Code 9W (week 39, 2009) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 9W are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.
  5. For packaging details, go to our website at <http://www.diodes.com>.

## Marking Information



K = SAT (Shanghai Assembly / Test site)  
 JJ = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Z = 2012)  
 M = Month (ex: 9 = September)



C = CAT (Chengdu Assembly / Test site)  
 JJ = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Z = 2012)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2000	2001	.....	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016
Code	L	M	.....	U	V	W	X	Y	Z	A	B	C	D

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Non-Repetitive Peak Reverse Voltage	V <sub>RM</sub>	100	V	
Peak Repetitive Reverse Voltage	V <sub>R(RM)</sub>	75	V	
Working Peak Reverse Voltage	V <sub>R(WM)</sub>			
DC Blocking Voltage	V <sub>R</sub>			
RMS Reverse Voltage	V <sub>R(RMS)</sub>	53	V	
Forward Continuous Current (Note 6)	I <sub>FM</sub>	300	mA	
Average Rectified Output Current (Note 6)	I <sub>O</sub>	150	mA	
Repetitive Peak Forward Current	I <sub>FRM</sub>	450	mA	
Non-Repetitive Peak Forward Surge Current	I <sub>FSM</sub>	@ t = 1.0μs	2.0	A
		@ t = 1.0s	1.0	

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P <sub>D</sub>	350	mW
Thermal Resistance Junction to Ambient Air (Note 6)	R <sub>θJA</sub>	357	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V <sub>(BR)R</sub>	75	—	V	I <sub>R</sub> = 2.5μA
Forward Voltage	V <sub>F</sub>	—	0.715 0.855 1.0 1.25	V	I <sub>F</sub> = 1.0mA I <sub>F</sub> = 10mA I <sub>F</sub> = 50mA I <sub>F</sub> = 150mA
Reverse Current (Note 7)	I <sub>R</sub>	—	2.5 50 30 25	μA μA μA nA	V <sub>R</sub> = 75V V <sub>R</sub> = 75V, T <sub>J</sub> = 150°C V <sub>R</sub> = 25V, T <sub>J</sub> = 150°C V <sub>R</sub> = 20V
Total Capacitance	C <sub>T</sub>	—	2.0	pF	V <sub>R</sub> = 0, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	4.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA, I <sub>tr</sub> = 0.1 x I <sub>R</sub> , R <sub>L</sub> = 100Ω

Notes: 6. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com>.  
7. Short duration pulse test used to minimize self-heating effect.

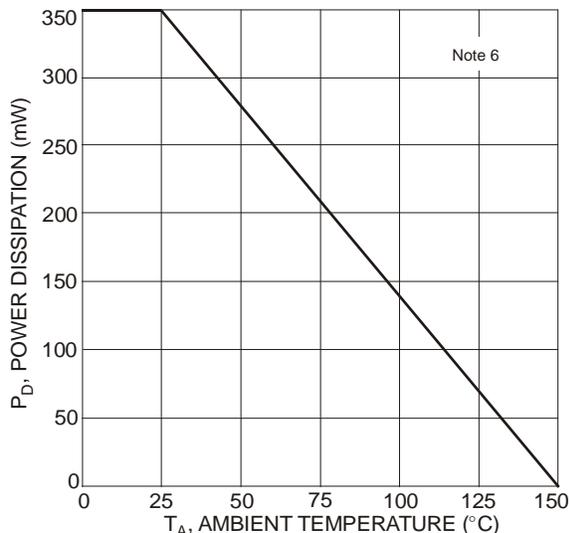


Fig. 1 Power Derating Curve, Total Package

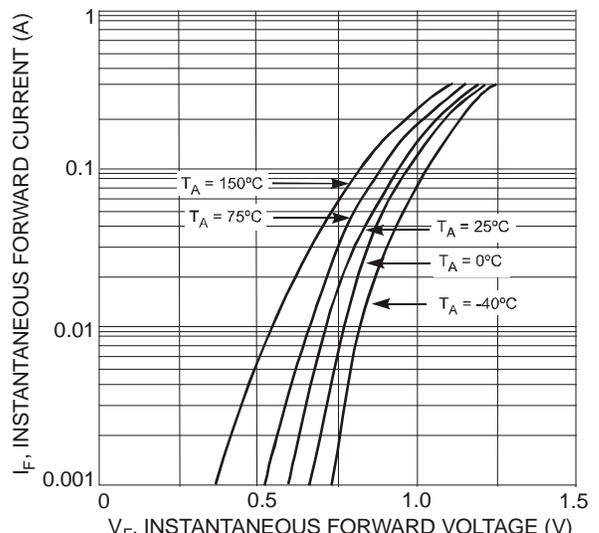


Fig. 2 Typical Forward Characteristics, Per Element

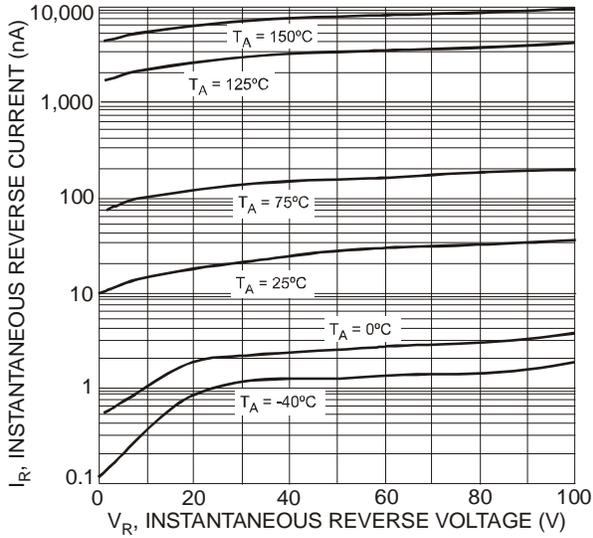


Fig. 3 Typical Reverse Characteristics, Per Element

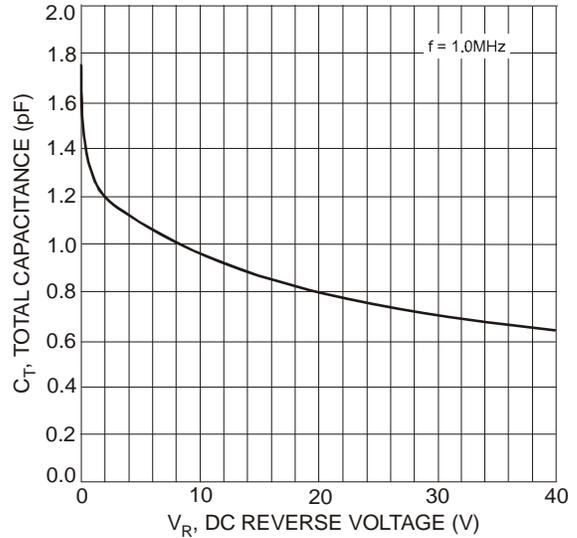
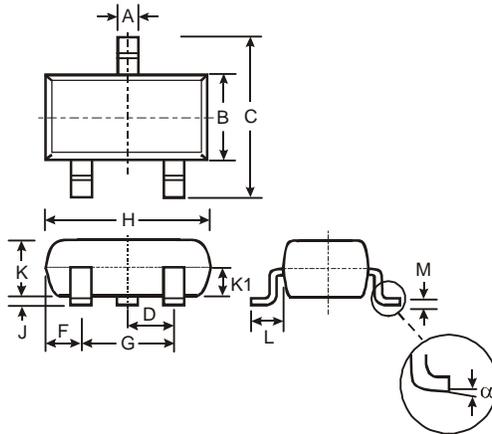


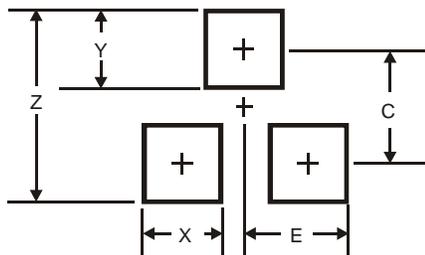
Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

### Package Outline Dimensions



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
$\alpha$	0°	8°	-
All Dimensions in mm			

### Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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