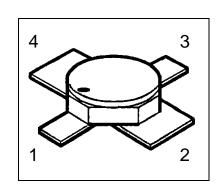


HiRel NPN Silicon Germanium RF Transistor

- HiRel Discrete and Microwave Semiconductor
- High gain low noise RF transistor
- High maximum stable gain: G_{ms} 24dB at 1.8 GHz
- Noise figure F = 0.8 dB at 1.8 GHz
 Noise figure F = 1.1 dB at 6 GHz
- Hermetically sealed microwave package

ESCC Detail Spec. No.: 5611/009



ESD: Electrostatic discharge sensitive device, observe handling precautions!

Туре	Marking	Pin Co	Pin Configuration			Package
		1	2	3	4	
BFY640-04	-	С	Е	В	Е	Micro-X

Maximum Ratings

Parameter	Symbol	Values	Unit	
Collector-emitter voltage $T_a > 0 \text{ °C}$ $T_a \le 0 \text{ °C}$	V _{CEO}	4.0 3.7	V	
Collector-base voltage	V _{CBO}	13	V	
Emitter-base voltage	V _{EBO}	1.2	V	
Collector current 1)	Ic	50	mA	
Base current	I _B	3	mA	
Junction temperature	T_j	175	°C	
Operating temperature range	T _{op}	-65+175	°C	
Storage temperature range	T _{stg}	-65+175	°C	
Thermal Resistance	·		•	
Junction-soldering point 2)	R _{th JS}	325	K/W	



Notes.:

- 1) For T_S ≤ 110°C. For T_S > 110 °C derating is required.
 2) T_S is measured on the emitter lead at the soldering point to the pcb.

Electrical Characteristics

at T_A=25°C; unless otherwise specified

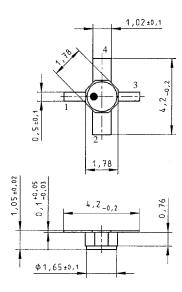
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-base cutoff current	I _{CBO}	-	-	10	μΑ
$V_{CB} = 5 \text{ V}, I_E = 0$					
Collector-emitter cutoff current 1)	I _{CEX}	-	-	200	μΑ
$V_{CE}=4.0~V,~I_{B}=0.1~\mu A$					
Emitter-base cuttoff current	I _{EBO}	-	-	5	μΑ
$V_{EB} = 1.2 \text{ V}, I_{C} = 0$					
DC current gain	h _{FE}	135	180	250	-
$I_C = 30$ mA, $V_{CE} = 3$ V					
AC Characteristics		•	•		•
Collector-base capacitance	ССВ	-	0.07	-	pF
$V_{CB} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Collector-emitter capacitance	C _{CE}	-	0.45	-	pF
$V_{CE} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Emitter-base capacitance	C _{EB}	-	0.6	-	pF
$V_{EB} = 0.5V$, $V_{CB} = vcb = 0$, $f = 1 MHz$					
Noise Figure ($Z_S = Z_{sopt}$)	F				dB
$I_C = 5 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$ $I_C = 5 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$		-	0.8 1.1	-	
Insertion power gain ($Z_S = Z_L = 50 \Omega$)	S _{21e} ²				dB
$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$	1-2161	_	22.5	_	
$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$		-	12.5	-	
Power gain ($Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$)	G _{ms} ²⁾				dB
$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$		-	24	-	
Power gain ($Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$)	G _{ma} ²⁾				dB
$I_C = 30$ mA, $V_{CE} = 3$ V, $f = 6.0$ GHz		-	14	-	

Notes.:
1) This Test assures V(BR)CE0 > 4.0V



2)
$$G_{ma} = \left| \frac{S21}{S12} \right| (k - \sqrt{k^2 - 1}), \quad G_{ms} = \left| \frac{S21}{S12} \right|$$

Micro-X Package



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