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Kind regards,

Team Nexperia



PNP resistor-equipped transistor;  $R1 = 4.7 k\Omega$ , R2 = openRev. 2 — 4 May 2012Product data

Product data sheet

#### 1. **Product profile**

#### **1.1 General description**

PNP Resistor-Equipped Transistor (RET) in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTC143TMB.

### 1.2 Features and benefits

- 100 mA output current capability
- Reduces component count
- Built-in bias resistors
- Reduces pick and place costs

### **1.3 Applications**

- Low-current peripheral driver
- Control of IC inputs

- Simplifies circuit design
- AEC-Q101 qualified
- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Replaces general-purpose transistors in digital applications
- Mobile applications

### 1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-50	V
lo	output current		-	-	-100	mA
R1	bias resistor 1 (input)	T <sub>amb</sub> = 25 °C	3.3	4.7	6.1	kΩ



PNP resistor-equipped transistor;  $R1 = 4.7 \text{ k}\Omega$ , R2 = open

# 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		
2	G	GND (emitter)		3
3	0	output (collector)	2 Transparent top view SOT883B (DFN1006B-3)	1 2 sym009

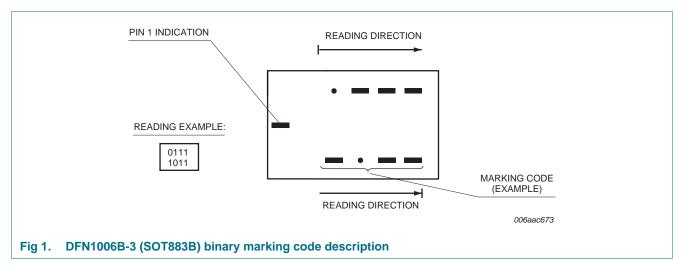
# 3. Ordering information

Table 3. Ordering information								
Type number	Package							
	Name	Description	Version					
PDTA143TMB	DFN1006B-3	Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B					

# 4. Marking

Table 4.	Marking o	odes
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Type number	Marking code
PDTA143TMB	0010 1000



PNP resistor-equipped transistor; R1 = 4.7 k $\Omega$ , R2 = open

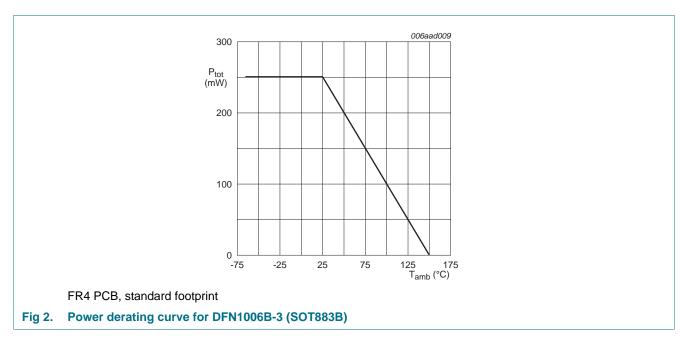
# 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
Ι <sub>Ο</sub>	output current			-	-100	mA
I <sub>CM</sub>	peak collector current	pulsed; t <sub>p</sub> ≤ 1 ms		-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	<u>[1]</u>	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

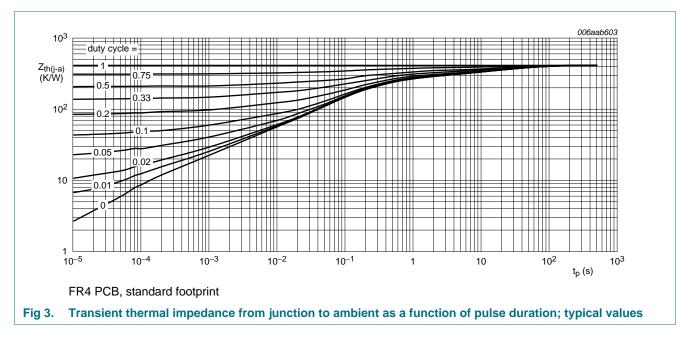


PNP resistor-equipped transistor;  $R1 = 4.7 \text{ k}\Omega$ , R2 = open

# 6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u>	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



# 7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB}$ = -50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
I <sub>CEO</sub>	collector-emitter cut-off	$V_{CE}$ = -30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-1	μA
	current	$V_{CE}$ = -30 V; $I_B$ = 0 A; $T_j$ = 150 °C	-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB}$ = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	200	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C} = -5 \text{ mA}; I_{B} = -0.25 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	-150	mV
R1	bias resistor 1 (input)	T <sub>amb</sub> = 25 °C	3.3	4.7	6.1	kΩ
C <sub>C</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	3	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; [1] T <sub>amb</sub> = 25 °C	-	180	-	MHz

[1] Characteristics of built-in transistor.

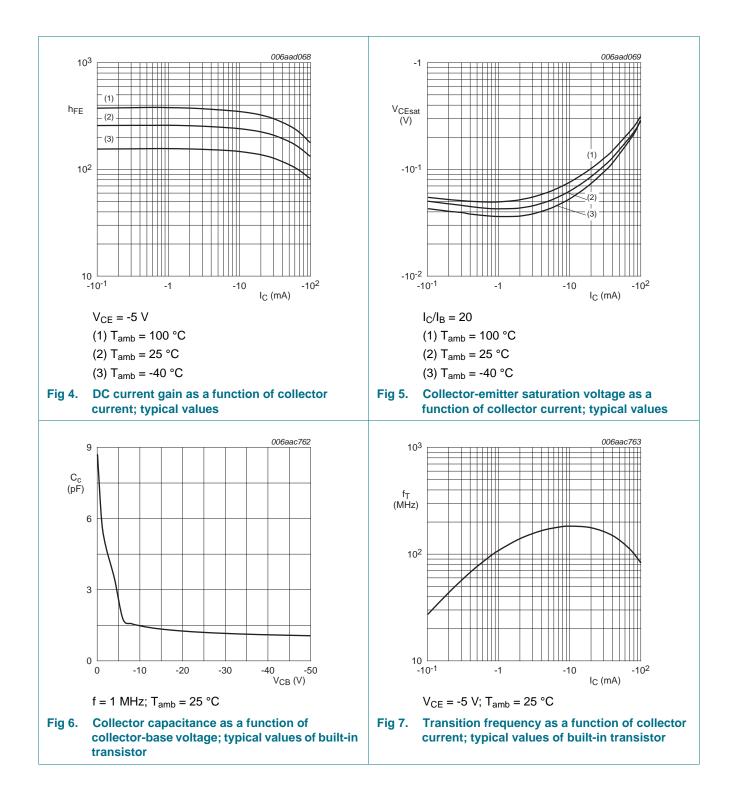
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PDTA143TMB

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# PDTA143TMB

#### PNP resistor-equipped transistor; $R1 = 4.7 \text{ k}\Omega$ , R2 = open



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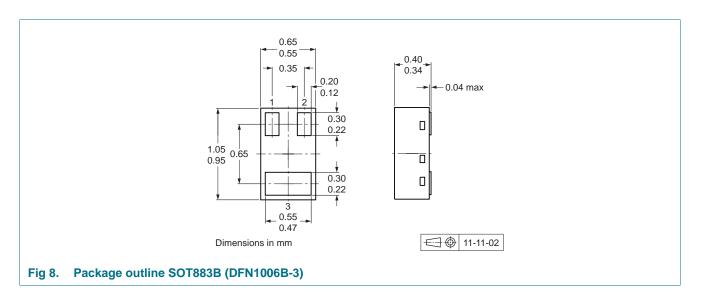
**PNP** resistor-equipped transistor;  $R1 = 4.7 \text{ k}\Omega$ , R2 = open

## 8. Test information

#### 8.1 Quality information

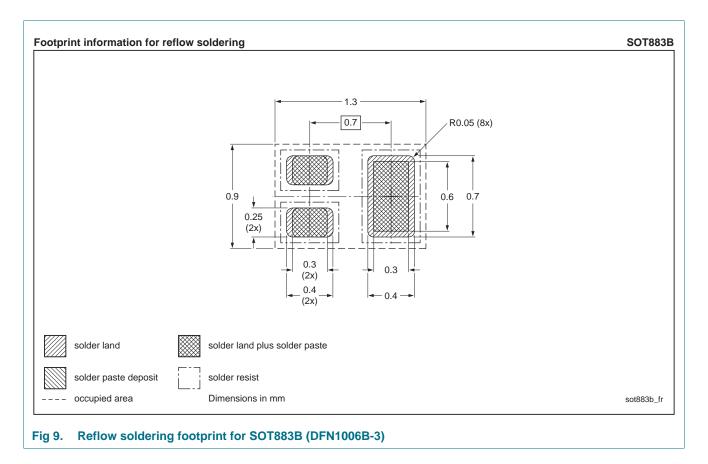
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 9. Package outline



PNP resistor-equipped transistor;  $R1 = 4.7 \text{ k}\Omega$ , R2 = open

# **10. Soldering**



PNP resistor-equipped transistor; R1 = 4.7 k $\Omega$ , R2 = open

# **11. Revision history**

Table 8. Revision	history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PDTA143TMB v.2	20120504	Product data sheet	-	PDTA143TMB v.1
Modifications:	• 7 "Characteris	stics": parameter name corre	ected	
PDTA143TMB v.1	20122504	Product data sheet	-	-

PNP resistor-equipped transistor;  $R1 = 4.7 k\Omega$ , R2 = open

## 12. Legal information

#### **12.1 Data sheet status**

Document status[1] [2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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**Product data sheet** 

PDTA143TMB

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#### PNP resistor-equipped transistor; $R1 = 4.7 \text{ k}\Omega$ , R2 = open

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