

# NL17SZ17

## Single Non-Inverting Buffer with Schmitt Trigger

The NL17SZ17 is a single Non-inverting Schmitt Trigger Buffer in two tiny footprint packages. The device performs much as LCX multi-gate products in speed and drive.

### Features

- Tiny SOT-353 and SOT-553 Packages
- Source/Sink 24 mA at 3.0 Volts
- Overvoltage Tolerant Inputs and Outputs
- Chip Complexity: FETs = 20
- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

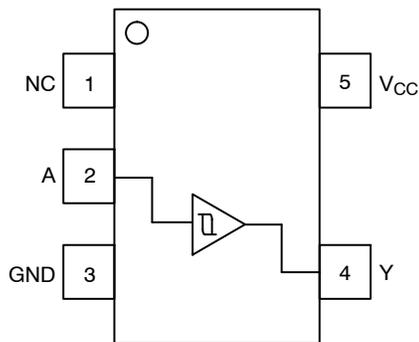


Figure 1. Pinout (Top View)



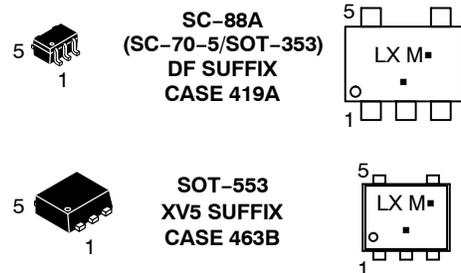
Figure 2. Logic Symbol



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### MARKING DIAGRAMS



LX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(\*Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

### PIN ASSIGNMENT

Pin	Function
1	NC
2	A
3	GND
4	Y
5	$V_{CC}$

### FUNCTION TABLE

A Input	Y Output
L	L
H	H

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

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## MAXIMUM RATINGS

Symbol	Parameter	Value	Units
$V_{CC}$	DC Supply Voltage	-0.5 to +7.0	V
$V_I$	DC Input Voltage	$-0.5 \leq V_I \leq +7.0$	V
$V_O$	DC Output Voltage	Output in High or LOW State (Note 1)	$-0.5 \leq V_O \leq +7.0$
$I_{IK}$	DC Input Diode Current	$V_I < \text{GND}$	-50
$I_{OK}$	DC Output Diode Current	$V_O < \text{GND}$	-50
$I_O$	DC Output Sink Current		$\pm 50$
$I_{CC}$	DC Supply Current per Supply Pin		$\pm 100$
$I_{GND}$	DC Ground Current per Ground Pin		$\pm 100$
$T_{STG}$	Storage Temperature Range		-65 to +150
$T_L$	Lead Temperature, 1 mm from Case for 10 Seconds		260
$T_J$	Junction Temperature under Bias		+150
$\theta_{JA}$	Thermal Resistance	SOT-353 (Note 2) SOT-553	350 496
$P_D$	Power Dissipation in Still Air at 85°C	SOT-353 SOT-553	186 135
MSL	Moisture Sensitivity		Level 1
$F_R$	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in
ESD	ESD Classification	Human Body Model (Note 3) Machine Model (Note 4) Charged Device Model	Class 2 Class C N/A
$I_{LATCHUP}$	Latchup Performance Above $V_{CC}$ and Below GND at 85°C (Note 5)		$\pm 500$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- $I_O$  absolute maximum rating must be observed.
- Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.
- Tested to EIA/JESD22-A114-A, rated to EIA/JESD22-A114-B.
- Tested to EIA/JESD22-A115-A, rated to EIA/JESD22-A115-A.
- Tested to EIA/JESD78.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Units
$V_{CC}$	Supply Voltage	Operating Data Retention Only	1.65 5.5	V
$V_I$	Input Voltage, (Note 6)	0	5.5	V
$V_O$	Output Voltage	(HIGH or LOW State)	0	5.5
$T_A$	Operating Free-Air Temperature	-55	+125	°C
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 2.5 \text{ V} \pm 0.2 \text{ V}$ $V_{CC} = 3.0 \text{ V} \pm 0.3 \text{ V}$ $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	0 0 0	No Limit No Limit No Limit

- Unused inputs may not be left open. All inputs must be tied to a high-logic voltage level or a low-logic input voltage level.

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## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-55°C ≤ T <sub>A</sub> ≤ 125°C		Units
				Min	Typ	Max	Min	Max	
V <sub>T+</sub>	Positive Input Threshold Voltage		1.65	0.6	1.0	1.4	0.6	1.4	V
			2.3	1.0	1.5	1.8	1.0	1.8	
			2.7	1.2	1.7	2.0	1.2	2.0	
			3.0	1.3	1.9	2.2	1.3	2.2	
			4.5	1.9	2.7	3.1	1.9	3.1	
			5.5	2.2	3.3	3.6	2.2	3.6	
V <sub>T-</sub>	Negative Input Threshold Voltage		1.65	0.2	0.5	0.8	0.2	0.8	V
			2.3	0.4	0.75	1.15	0.4	1.15	
			2.7	0.5	0.87	1.4	0.5	1.4	
			3.0	0.6	1.0	1.5	0.6	1.5	
			4.5	1.0	1.5	2.0	1.0	2.0	
			5.5	1.2	1.9	2.3	1.2	2.3	
V <sub>H</sub>	Input Hysteresis Voltage		1.65	0.1	0.48	0.9	0.1	0.9	V
			2.3	0.25	0.75	1.1	0.25	1.1	
			2.7	0.3	0.83	1.15	0.3	1.15	
			3.0	0.4	0.93	1.2	0.4	1.2	
			4.5	0.6	1.2	1.5	0.6	1.5	
			5.5	0.7	1.4	1.7	0.7	1.7	
V <sub>OH</sub>	High-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA I <sub>OH</sub> = -3 mA I <sub>OH</sub> = -8 mA I <sub>OH</sub> = -12 mA I <sub>OH</sub> = -16 mA I <sub>OH</sub> = -24 mA I <sub>OH</sub> = -32 mA	1.65 to 5.5	V <sub>CC</sub> - 0.1	V <sub>CC</sub>		V <sub>CC</sub> - 0.1		V
			1.65	1.29	1.52		1.29		
			2.3	1.9	2.1		1.9		
			2.7	2.2	2.4		2.2		
			3.0	2.4	2.7		2.4		
			4.5	3.8	4.0		3.8		
V <sub>OL</sub>	Low-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA I <sub>OL</sub> = 4 mA I <sub>OL</sub> = 8 mA I <sub>OL</sub> = 12 mA I <sub>OL</sub> = 16 mA I <sub>OL</sub> = 24 mA I <sub>OL</sub> = 32 mA	1.65 to 5.5			0.1		0.1	V
			1.65		0.08	0.24		0.24	
			2.3		0.2	0.3		0.3	
			2.7		0.22	0.4		0.4	
			3.0		0.28	0.4		0.4	
			4.5		0.38	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 5.5 V or GND	0 to 5.5			±0.1		±1.0	μA
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 5.5 V or V <sub>OUT</sub> = 5.5 V	0			1.0		10	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = 5.5 V or GND	5.5			1.0		10	μA

## AC ELECTRICAL CHARACTERISTICS (Input t<sub>r</sub> = t<sub>f</sub> = 3.0 ns)

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-55°C ≤ T <sub>A</sub> ≤ 125°C		Units
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Input A to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	1.65	2.0	9.1	15	2.0	15.6	ns
			1.8	2.0	7.6	12.5	2.0	13	
			2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5	
			3.3 ± 0.3	1.0	3.7	6.3	1.0	6.5	
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	
			3.3 ± 0.3	1.5	4.4	7.2	1.5	7.5	
		R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF	5.0 ± 0.5	0.8	3.7	5.9	0.8	6.2	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Units
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	> 2.5	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 7)	10 MHz, V <sub>CC</sub> = 3.3 V, V <sub>I</sub> = 0 V or V <sub>CC</sub> 10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	9 11	pF

7. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

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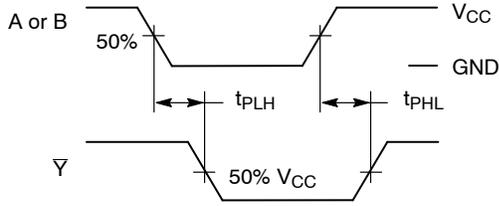
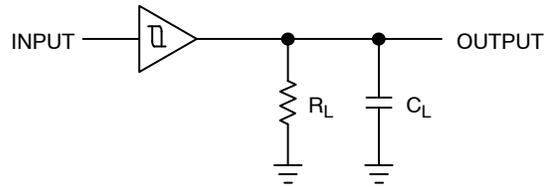


Figure 3. Switching Waveforms



A 1 MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit

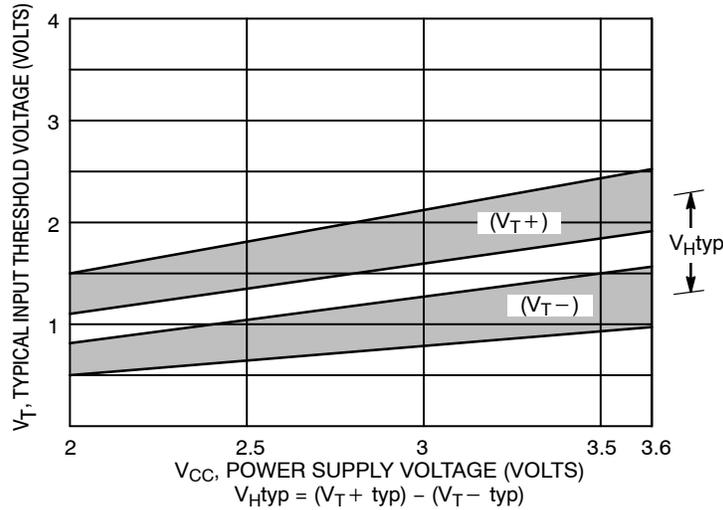
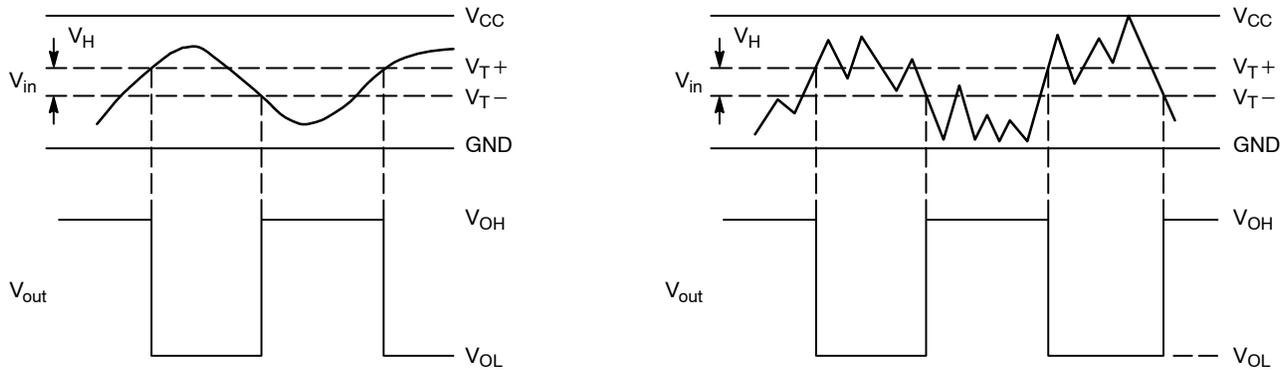


Figure 5. Typical Input Threshold,  $V_{T+}$ ,  $V_{T-}$  versus Power Supply Voltage



(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times

(b) A Schmitt-Trigger Offers Maximum Noise Immunity

Figure 6. Typical Schmitt-Trigger Applications

## DEVICE ORDERING INFORMATION

Device Order Number	Package Type	Tape/Reel Size <sup>†</sup>
NL17SZ17DFT2G	SC-88A/SC-70-5/SOT-353 (Pb-Free)	3000 Units / Tape & Reel
NLV17SZ17DFT2G*	SC-88A/SC-70-5/SOT-353 (Pb-Free)	3000 Units / Tape & Reel
NL17SZ17XV5T2G	SOT-553 (Pb-Free)	4000 Units / Tape & Reel

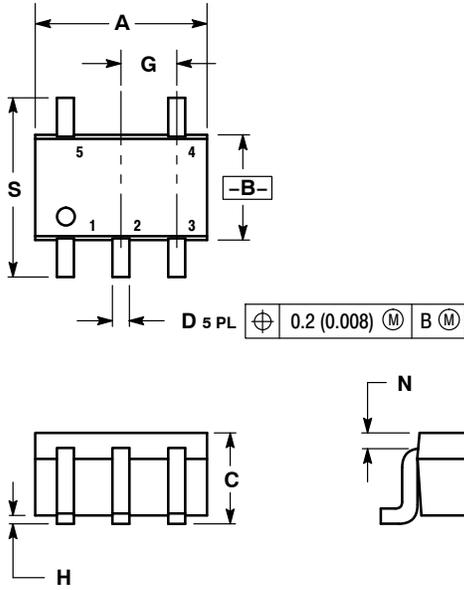
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

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## PACKAGE DIMENSIONS

SC-88A (SC-70-5/SOT-353)  
CASE 419A-02  
ISSUE L

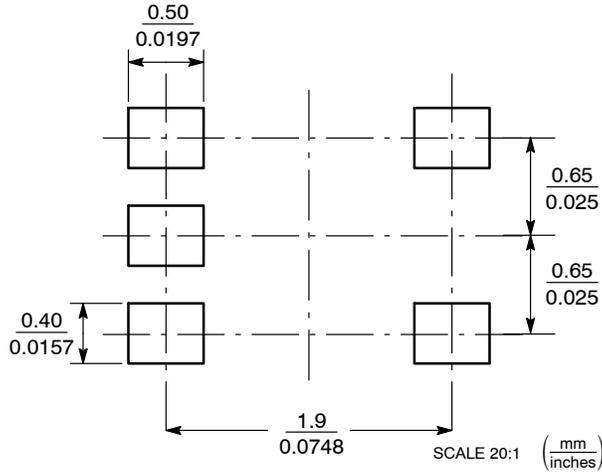


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20

### SOLDER FOOTPRINT\*

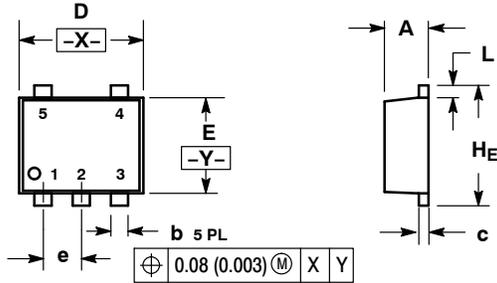


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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## PACKAGE DIMENSIONS

**SOT-553**  
**XV5 SUFFIX**  
**CASE 463B**  
**ISSUE B**

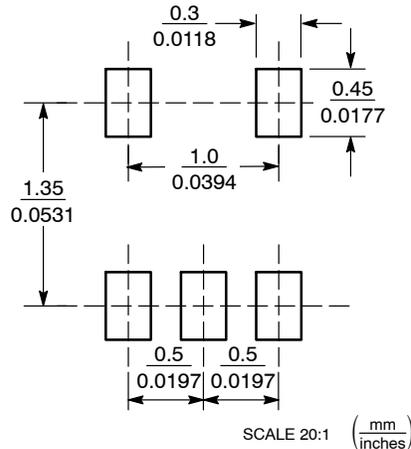


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
b	0.17	0.22	0.27	0.007	0.009	0.011
c	0.08	0.13	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.063	0.067
E	1.10	1.20	1.30	0.043	0.047	0.051
e	0.50 BSC			0.020 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	1.50	1.60	1.70	0.059	0.063	0.067

### SOLDERING FOOTPRINT\*



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