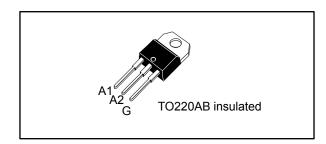


# 12 A Snubberless™ Triac

Datasheet -production data



## **Features**

- High static dV/dt
- High dynamic turn-off commutation (dl/dt)c
- 150 °C maximum T<sub>i</sub>
- Three quadrants
- Built-in ceramic for tab insulation
- Compliance to UL1557 standard (ref : E81734)
- ECOPACK®2 compliant component
- Complies with UL94,V0
- Surge capability V<sub>DSM</sub>, V<sub>RSM</sub> = 900 V

## **Benefits**

- High immunity to false turn-on thanks to high static dV/dt
- Better turn-off in high temperature environments thanks to (dl/dt)c
- Increase of thermal margin due to extended working  $T_j$  up to 150  $^{\circ}C$
- Better thermal resistance due to the ceramic inside the package

# **Applications**

- General purpose AC line load switching
- Motor control circuits
- Home appliances
- Heating
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

## **Description**

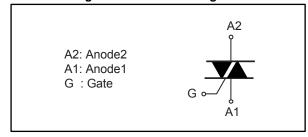
Available in through-hole package, the T1235T-8I Triac can be used for the on/off or phase angle control function in general purpose AC switching where high commutation capability is required. This device can be used without a snubber RC circuit when the limits defined are respected.

TO-220AB insulated provides tab insulation, UL1557 certified, rated at 2.5 kV RMS and UL-94, V0 resin compliance.

Package environmentally friendly Ecopack®2 graded (RoHS and Halogen Free compliance).

Snubberless™ is a trademark of STMicroelectronics.

Figure 1: Functional diagram



**Table 1: Device summary** 

Symbol	Value	Unit
I <sub>T(RMS)</sub>	12	Α
V <sub>DRM</sub> /V <sub>RRM</sub>	800	V
V <sub>DSM</sub> /V <sub>RSM</sub>	900	٧
I <sub>GT</sub>	35	mA

Characteristics T1235T-8I

# 1 Characteristics

Table 2: Absolute maximum ratings (limiting values)

Symbol	Parameter			Value	Unit	
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave)			12	Α	
I <sub>TSM</sub>	Non repetitive surge peak on-state current,		current, $t_p$ = 16.7 ms		_	
IISM	T <sub>j</sub> initial = 25 °C		t <sub>p</sub> = 20 ms	90	Α	
l <sup>2</sup> t	I <sup>2</sup> t value for fusing		T <sub>j</sub> initial = 25 °C	54	$A^2s$	
dl/dt	Critical rate of rise of on-state current, I <sub>G</sub> = 2 x I <sub>GT</sub> , tr ≤ 100 ns		f = 100 Hz	100	A/µs	
\/\/\/	December 1		T <sub>j</sub> = 150 °C	600	V	
V <sub>DRM</sub> /V <sub>RRM</sub>	Repetitive peak off-state voltage		T <sub>j</sub> = 125 °C	800	V	
V <sub>DSM</sub> /V <sub>RSM</sub>	Non Repetitive peak off-state voltage		t <sub>p</sub> = 10 ms	900	V	
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 150 °C	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation T <sub>j</sub> = 150 °C			1	W	
T <sub>stg</sub>	Storage junction temperature range			-40 to +150	°C	
Tj	Operating junction temperature range			-40 to +150	°C	
TL	Maximum lead temperature for soldering during 10 s			260	ů	
Vins	Insulation RMS voltage, 1 minut	Insulation RMS voltage, 1 minute, UL1557 certified (E81734)				

Table 3: Electrical characteristics (T<sub>j</sub> = 25 °C, unless otherwise specified)

Symbol	Test conditions	Quadrants; T <sub>j</sub>		Value	Unit
1	V <sub>D</sub> = 12 V, R <sub>L</sub> = 33 Ω	1 - 11 - 111	Min.	1.75	mA
I <sub>GT</sub>	$V_D = 12 \text{ V}, R_L = 33 \Omega$	1 - 11 - 111	Max.	35	mA
$V_{GT}$	$V_D = 12 \text{ V}, R_L = 33 \Omega$	1 - 11 - 111	Max.	1.3	V
$V_{GD}$	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, T_j = 150 \text{ °C}$	1 - 11 - 111	Min.	0.2	V
	I <sub>G</sub> = 1.2 x I <sub>GT</sub>	I - III	Max.	60	mA
IL	I <sub>G</sub> = 1.2 x I <sub>GT</sub>	II	Max.	80	mA
IH <sup>(1)</sup>	I <sub>T</sub> = 500 mA, gate open		Max.	40	mA
dV/dt <sup>(1)</sup>	V <sub>D</sub> = 536 V, gate open	T <sub>j</sub> = 125 °C	Min.	2000	V/µs
αν/αι**	V <sub>D</sub> = 402 V, gate open	T <sub>j</sub> = 150 °C	Min.	1000	V/µs
(dl/dt)c <sup>(1)</sup>	Without snubber, (dV/dt)c > 20 V/µs	T <sub>j</sub> = 125 °C	Min.	12	A/ms
(ui/ut)C	vviiiiout siiubbei, (αν/αί)c > 20 v/μs	T <sub>j</sub> = 150 °C	Min.	6	A/ms

### Notes:

<sup>&</sup>lt;sup>(1)</sup>For both polarities of A2 referenced to A1.

T1235T-8I Characteristics

**Table 4: Static characteristics** 

Symbol	Test conditions	Tj		Value	Unit
V <sub>TM</sub> <sup>(1)</sup>	I <sub>T</sub> = 17 A, t <sub>p</sub> = 380 μs	25 °C	Max.	1.60	V
V <sub>TO</sub> <sup>(1)</sup>	Threshold on-state voltage	150 °C	Max.	0.85	>
R <sub>D</sub> <sup>(1)</sup>	Dynamic resistance	150 °C	Max.	50	mΩ
	V <sub>DRM</sub> = V <sub>RRM</sub> = 800 V	25 °C	Max.	5	μΑ
I <sub>DRM</sub> /I <sub>RRM</sub>	VDRM - VRRM - 800 V	125°C	IVIAX.	1	mA
	V <sub>DRM</sub> = V <sub>RRM</sub> = 600 V	150 °C	Max.	3.1	mA

## Notes:

**Table 5: Thermal resistance** 

Symbol	Parameter	Value	Unit	
R <sub>th(j-c)</sub>	Junction to case (AC)	Max.	2.6	°C/\\\
R <sub>th(j-a)</sub>	Junction to ambient Typ.		60	°C/W

<sup>&</sup>lt;sup>(1)</sup>For both polarities of A2 referenced to A1.

Characteristics T1235T-8I

# 1.1 Characteristics (curves)

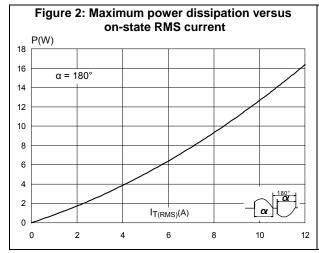


Figure 3: On-state RMS current versus case temperature  $I_{T(RMS)}(A)$ 16  $\alpha = 180^{\circ}$ 12 8 T<sub>C</sub>(°C) 0 0 25 50 75 100 125 150

Figure 4: On-state RMS current versus ambient temperature (free air convection) I<sub>T(RMS)</sub>(A) 3.5  $\alpha = 180^{\circ}$ 3.0 2.5 2.0 1.5 1.0 0.5 0.0 25 50 75 100 150

Figure 5: Relative variation of thermal impedance versus pulse duration

K = [Z<sub>th</sub>/R<sub>th</sub>]

1.0E+00

1.0E-01

1.0E-02

1.0E-03

1.0E-02

1.0E-03

1.0E-02

1.0E-03

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1.0E-03

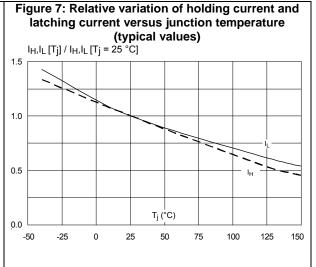
1.0E-01

1.0E+03

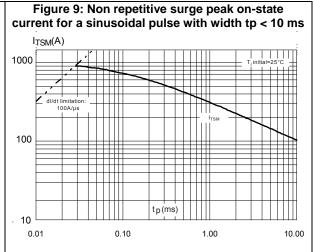
1.0E+04

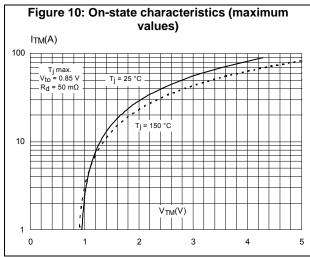
1.0E+05

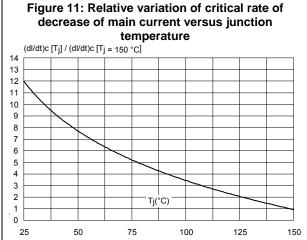
Figure 6: Relative variation of gate trigger voltage and current versus junction temperature (typical values)  $I_{GT}, V_{GT}[T_i] / I_{GT}, V_{GT}[T_i = 25 °C]$ 2.5 2.0 I<sub>GT</sub> Q3 1.5 V<sub>GT</sub> Q1-Q2-Q3 1.0 0.5 T<sub>j</sub> (°C) 0.0 -50 50 150

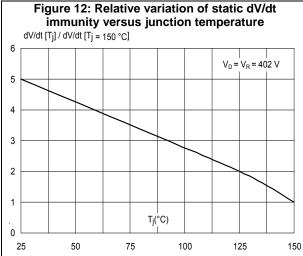


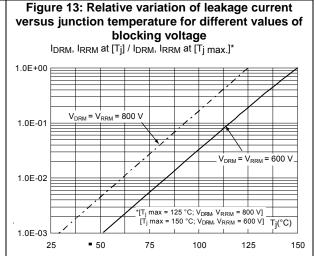
T1235T-8I Characteristics











Package information T1235T-8I

#### 2 **Package information**

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

- ECOPACK®2 (Lead-free plating and Halogen free package compliance)
- Lead-free package leads finishing
- Halogen-free molding compound resin meets UL94 standard level V0.
- Recommended torque (for package screwing assembly): 0.4 to 0.6 N·m

#### **TO-220AB Insulated package information** 2.1

С В b2 Resin gate 0.5 mm max. protusion(1) F Α 14 13 c2 a1 12 a2 Μ c1 Resin gate 0.5 mm b1 max. protusion<sup>(1)</sup>

Figure 14: TO-220AB Insulated package outline

(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

T1235T-8I Package information

Table 6: TO-220AB Insulated package mechanical data

	Dimensions					
Ref.	ef. Millimeters			Inches <sup>(1)</sup>		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	15.20		15.90	0.5984		0.6260
a1		3.75			0.1476	
a2	13.00		14.00	0.5118		0.5512
В	10.00		10.40	0.3937		0.4094
b1	0.61		0.88	0.0240		0.0346
b2	1.23		1.32	0.0484		0.0520
С	4.40		4.60	0.1732		0.1811
c1	0.49		0.70	0.0193		0.0276
c2	2.40		2.72	0.0945		0.1071
е	2.40		2.70	0.0945		0.1063
F	6.20		6.60	0.2441		0.2598
- 1	3.73		3.88	0.1469		0.1528
L	2.65		2.95	0.1043		0.1161
12	1.14		1.70	0.0449		0.0669
13	1.14		1.70	0.0449		0.0669
14	15.80	16.40	16.80	0.6220	0.6457	0.6614
М		2.6			0.1024	

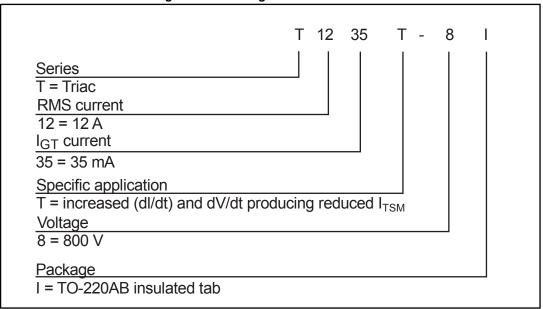
## Notes:

<sup>&</sup>lt;sup>(1)</sup>Inch dimensions are for reference only.

Ordering information T1235T-8I

# 3 Ordering information

Figure 15: Ordering information scheme



**Table 7: Ordering information** 

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T1235T-8I	T1235T-8I	TO-220AB insulated	2.3 g	50	Tube

# 4 Revision history

**Table 8: Document revision history** 

Date	Revision	Changes
17-Oct-2017	1	Initial release.
18-Dec-2017 2		Updated Table 4: "Static characteristics".

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