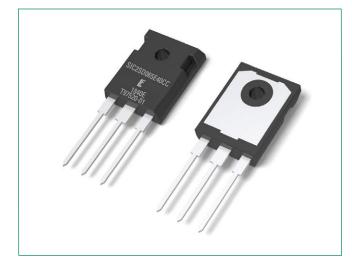
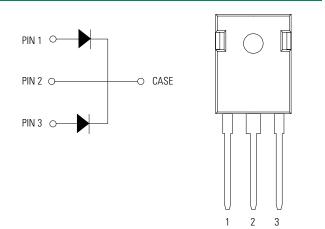
# LSIC2SD065E40CCA 650 V, 40 A SiC Schottky Barrier Diode

HF RoHS 🕫



#### Circuit Diagram TO-247-3L



#### Description

This series of silicon carbide (SiC) Schottky diodes has negligible reverse recovery current, high surge capability, and a maximum operating junction temperature of 175 °C. This diode series is ideal for applications where improvements in efficiency, reliability, and thermal management are desired.

#### Features

- AEC-Q101 qualified
- Positive temperature coefficient for safe operation and ease of paralleling
- 175 °C. maximum operating junction temperature
- Excellent surge capability
- Extremely fast, temperature-independent switching behavior
- Dramatically reduced switching losses compared to Si bipolar diodes

#### Applications

supplies

 Boost diodes in PFC or DC/DC stages

Switch-mode power

- Solar inverters
- Industrial motor drives
- EV charging stations
- Uninterruptible power supplies

#### Environmental

- Littelfuse "RoHS" logo = RoHS RoHS conform
- Littelfuse "HF" logo = **HF** Halogen Free
- Littelfuse "Pb-free" logo = Pb-free lead plating

#### **Maximum Ratings**

Characteristics	Symbol	Conditions	Value	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	-	650	V
DC Blocking Voltage	V <sub>R</sub>	T <sub>J</sub> = 25 °C	650	V
Continuous Forward Current		$T_c = 25 \text{ °C}$	45 / 90	
(Per Leg/Component)	I <sub>F</sub>	T <sub>c</sub> = 135 °C	20 / 40	A
Non-Repetitive Forward Surge Current (Per Leg)	I <sub>FSM</sub>	$T_c = 25 \text{ °C}, T_p = 10 \text{ ms}, \text{ Half sine pulse}$	90	А
Power Dissipation		$T_c = 25 \text{ °C}$	135 / 270	10/
(Per Leg/Component)	P <sub>Tot</sub>	T <sub>c</sub> = 110 °C	60 / 120	W
Operating Junction Temperature	T	-	-55 to 175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to 150	°C
Soldering Temperature	T <sub>sold</sub>	-	260	°C

# GEN2 SiC Schottky Diode

LSIC2SD065E40CCA, 650V, 40A, TO-247-3L

Electrical Characteristics (T <sub>1</sub> = 25 °C unless otherwise specified)
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Characteristics Symb	Cumhal	Conditions	Value			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	N	I <sub>F</sub> = 20 A, T <sub>J</sub> = 25 °C	-	1.5	1.8	V
	V <sub>F</sub>	I <sub>F</sub> = 20 A, T <sub>J</sub> = 175 °C	-	1.85	-	V
Reverse Current I <sub>R</sub>	$V_{_{ m R}} = 650 \text{ V}, \text{ T}_{_{ m J}} = 25 \ ^{\circ}\text{C}$	-	<1	50		
	I <sub>R</sub>	V <sub>R</sub> = 650 V, T <sub>J</sub> = 175 °C	-	60	-	μΑ
Total Capacitance C	V <sub>R</sub> = 1 V, f = 1 MHz	-	960	-		
	V <sub>R</sub> = 200 V, f = 1 MHz	-	120	-	pF	
		V <sub>R</sub> = 400 V, f = 1 MHz	-	86	-	
Total Capacitive Charge	Q <sub>c</sub>	$V_{R} = 400 \text{ V},  \Omega_{c} = \int_{0}^{V_{R}} C(V) dV$	-	63	-	nC

#### **Thermal Characteristics**

**Figure 1: Typical Foward Characteristics** 

Characteristics	Symbol	Value	Unit
Thermal Resistance (Per Leg/Component)	R <sub>ejc</sub>	1.10 / 0.55	°C/W

#### 20 18 T<sub>1</sub> = -55 °C T<sub>J</sub> = 25°C . 16 T<sub>1</sub> = 125 °C 14 T<sub>J</sub> = 150 °C T<sub>j</sub> = 175 °C 12 Current (A) 10 8 6 4 2 0 0 0.25 0.5 0.75 1 1.25 1.5 1.75 2 Voltage (V)

#### Figure 3: Power Derating

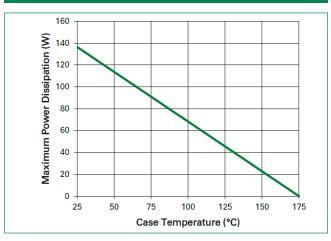


Figure 2: Typical Reverse Characteristics

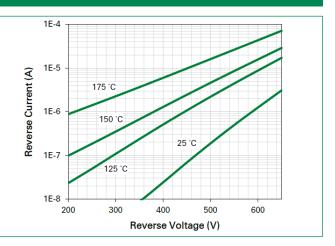
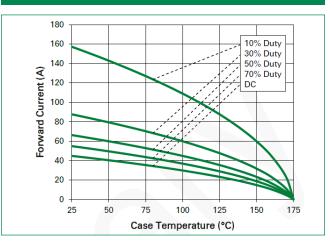


Figure 4: Current Derating



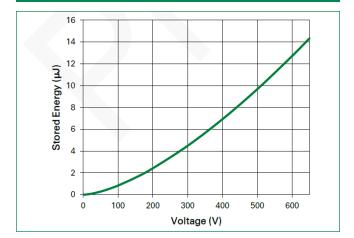
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# Littelfuse Power

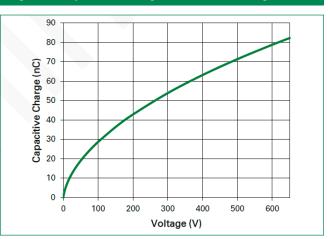
#### **GEN2 SiC Schottky Diode** LSIC2SD065E40CCA, 650V, 40A, TO-247-3L

#### Figure 5: Capacitance vs. Reverse Voltage 1000 900 800 700 Capacitance (pF) 600 500 400 300 200 100 0 1 10 100 Voltage (V)

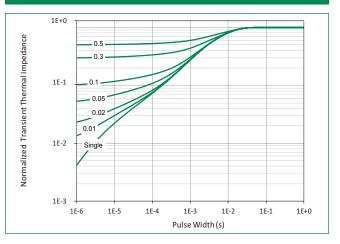
#### Figure 7: Stored Energy vs. Reverse Voltage



#### Figure 6: Capacitive Charge vs. Reverse Voltage



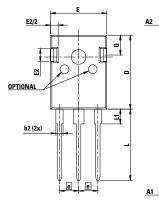
#### Figure 8: Transient Thermal Impedance

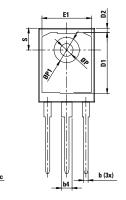


### GEN2 SiC Schottky Diode

LSIC2SD065E40CCA, 650V, 40A, TO-247-3L

#### Package Dimensions TO-247-3L



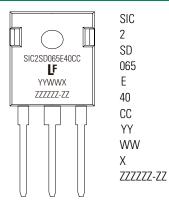


Recommended Hole Pattern Layout

Notes: 1. Dimensions are in millimeters 2. Dimension D, E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These measured at the outermost extreme of plastic body. 3.eP to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 0.154"

Or mark at	Millimeters			
Symbol	Min	Nom	Max	
Α	4.80	5.03	5.20	
A1	2.25	2.38	2.54	
A2	1.85	1.98	2.11	
b	0.99	-	1.40	
b2	1.65	-	2.39	
b4	2.59	-	3.43	
C	0.38	0.64	0.89	
D	20.80	20.96	21.34	
D1	13.50	-	-	
D2	0.51	1.19	1.35	
е	5.44 BSC			
E	15.75	15.90	16.13	
E1	13.06	14.02	14.15	
E2	4.19	4.32	4.83	
L	19.81	20.19	20.57	
L1	3.81	4.19	4.45	
øP	3.55	3.61	3.66	
øP1	7.06	7.19	7.32	
Q	5.49	5.61	6.20	
S	6.05	6.17	6.30	

#### Part Numbering and Marking System



- = SiC = Gen2
- = Schottky Diode
- = Voltage Rating (650 V)
- = T0-247-3L
- = Current Rating (40 A)
- = Common Cathode
- = Year
- = Week
- = Trace Code (Any Letter)
- = Lot Number

# Packing Options Part Number Marking Packing Mode M.O.Q LSIC2SD065E40CCA SIC2SD065E40CC Tube (30pcs) 450

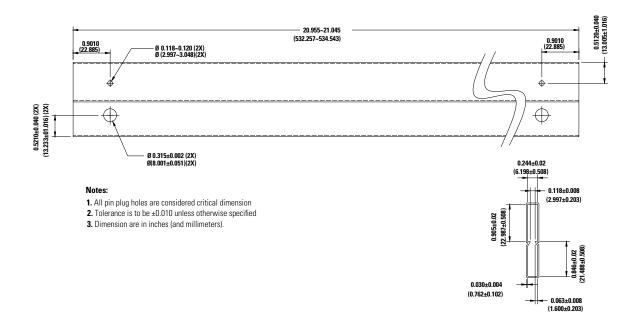
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# **GEN2 SiC Schottky Diode**

LSIC2SD065E40CCA, 650V, 40A, TO-247-3L

Packing Specification TO-247-3L



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