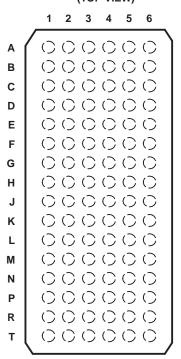




FEATURES

- Member of the Texas Instruments Widebus+™
 Family
- State-of-the-Art Advanced BiCMOS
 Technology (ABT) Design for 3.3-V Operation
 and Low Static-Power Dissipation
- Typical V_{OLP} (Output Ground Bounce) <0.8 V at V_{CC} = 3.3 V, T_A = 25°C
- I_{off} and Power-Up 3-State Support Hot Insertion
- Supports Unregulated Battery Operation Down to 2.7 V

GKE OR ZKE PACKAGE (TOP VIEW)



- Supports Mixed-Mode Signal Operation (5-V Input and Output Voltages With 3.3-V V_{CC})
- Bus Hold on Data Inputs Eliminates the Need for External Pullup/Pulldown Resistors
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection Exceeds JESD 22
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

TERMINAL ASSIGNMENTS

	1	2	3	4	5	6
Α	1B2	1B1	1DIR	1 OE	1A1	1A2
В	1B4	1B3	GND	GND	1A3	1A4
С	1B6	1B5	1V _{CC}	1V _{CC}	1A5	1A6
D	1B8	1B7	GND	GND	1A7	1A8
E	2B2	2B1	GND	GND	2A1	2A2
F	2B4	2B3	1V _{CC}	1V _{CC}	2A3	2A4
G	2B6	2B5	GND	GND	2A5	2A6
Н	2B7	2B8	2DIR	2 OE	2A8	2A7
J	3B2	3B1	3DIR	3 OE	3A1	3A2
K	3B4	3B3	GND	GND	3A3	3A4
L	3B6	3B5	2V _{CC}	2V _{CC}	3A5	3A6
M	3B8	3B7	GND	GND	3A7	3A8
N	4B2	4B1	GND	GND	4A1	4A2
Р	4B4	4B3	2V _{CC}	2V _{CC}	4A3	4A4
R	4B6	4B5	GND	GND	4A5	4A6
T	4B7	4B8	4DIR	4 OE	4A8	4A7

ORDERING INFORMATION

T _A	PACKAGE ⁽¹⁾	ı	ORDERABLE PART NUMBER	TOP-SIDE MARKING
–40°C to 85°C	LFBGA – GKE	Reel of 1000	SN74LVTH32245GKER	HV245
-40 C to 65 C	LFBGA – ZKE (Pb-free)	Reel of 1000	SN74LVTH32245ZKER	HV245

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

44

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SN74LVTH32245 3.3-V ABT 32-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS

SCBS750B-OCTOBER 2000-REVISED NOVEMBER 2006



DESCRIPTION/ORDERING INFORMATION

The SN74LVTH32245 is a 32-bit noninverting 3-state transceiver designed for low-voltage (3.3-V) V_{CC} operation, but with the capability to provide a TTL interface to a 5-V system environment.

This device can be used as four 8-bit transceivers, two 16-bit transceivers, or one 32-bit transceiver. The device allows data transmission from the A bus to the B bus or from the B bus to the A bus, depending on the logic level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can be used to disable the devices so that the buses are effectively isolated.

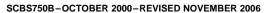
When V_{CC} is between 0 and 1.5 V, the device is in the high-impedance state during power up or power down. However, to ensure the high-impedance state above 1.5 V, \overline{OE} should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

This device is fully specified for hot-insertion applications using I_{off} and power-up 3-state. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down. The power-up 3-state circuitry places the outputs in the high-impedance state during power up and power down, which prevents driver conflict.

Active bus-hold circuitry holds unused or undriven inputs at a valid logic state. Use of pullup or pulldown resistors with the bus-hold circuitry is not recommended.

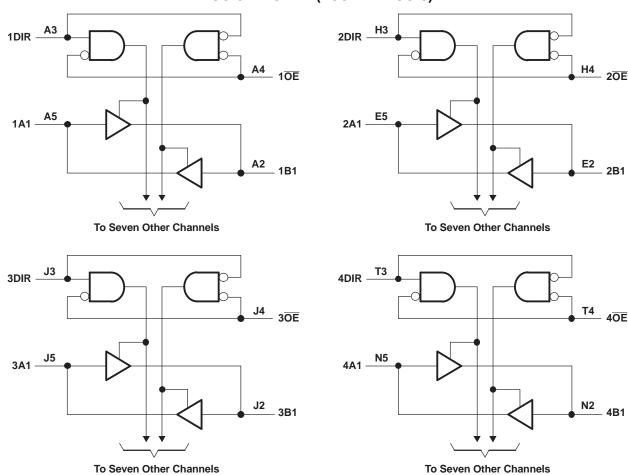
FUNCTION TABLE (each 8-bit transceiver

INP	UTS	ODEDATION		
ŌĒ	DIR	OPERATION		
L	L	B data to A bus		
L	Н	A data to B bus		
Н	X	Isolation		





LOGIC DIAGRAM (POSITIVE LOGIC)



Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V_{CC}	Supply voltage range		-0.5	4.6	V
VI	Input voltage range ⁽²⁾		-0.5	7	V
Vo	Voltage range applied to any output in the	high-impedance or power-off state ⁽²⁾	-0.5	7	V
Vo	Voltage range applied to any output in the	-0.5	V _{CC} + 0.5	V	
Io	Current into any output in the low state		128	mA	
Io	Current into any output in the high state (3)			64	mA
I _{IK}	Input clamp current	V _I < 0		-50	mA
I _{OK}	Output clamp current	V _O < 0		-50	mA
θ_{JA}	Package thermal impedance ⁽⁴⁾	GKE/ZKE package		40	°C/W
T _{stg}	Storage temperature range		-65	150	°C

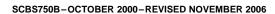
⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

⁽²⁾ The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

⁽³⁾ This current flows only when the output is in the high state and $V_0 > V_{CC}$.

⁽⁴⁾ The package thermal impedance is calculated in accordance with JESD 51-7.

SN74LVTH32245 3.3-V ABT 32-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS





Recommended Operating Conditions(1)

			MIN	MAX	UNIT
V _{CC}	Supply voltage		2.7	3.6	V
V_{IH}	High-level input voltage		2		V
V_{IL}	Low-level input voltage		0.8	V	
V_{I}	Input voltage		5.5	V	
I _{OH}	High-level output current			-32	mA
I _{OL}	Low-level output current			64	mA
Δt/Δν	Input transition rise or fall rate	Outputs enabled		10	ns/V
$\Delta t/\Delta V_{CC}$	Power-up ramp rate		200		μs/V
T_A	Operating free-air temperature	·	-40	85	°C

⁽¹⁾ All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.



WITH 3-STATE OUTPUTS

SCBS750B-OCTOBER 2000-REVISED NOVEMBER 2006

Electrical Characteristics

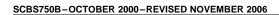
over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CON	DITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT	
V_{IK}		V _{CC} = 2.7 V,	I _I = -18 mA			-1.2	V	
		$V_{CC} = 2.7 \text{ V to } 3.6 \text{ V},$	$I_{OH} = -100 \mu A$	V _{CC} - 0.2				
V_{OH}		$V_{CC} = 2.7 \text{ V},$	$I_{OH} = -8 \text{ mA}$	2.4			V	
		V _{CC} = 3 V,	$I_{OH} = -32 \text{ mA}$	2				
		V _{CC} = 2.7 V	I _{OL} = 100 μA			0.2		
		V _{CC} = 2.7 V	$I_{OL} = 24 \text{ mA}$			0.5		
V_{OL}			I _{OL} = 16 mA			0.4	V	
		$V_{CC} = 3 V$	I _{OL} = 32 mA			0.5		
			$I_{OL} = 64 \text{ mA}$			0.55		
	Control innuito	V _{CC} = 3.6 V,	$V_I = V_{CC}$ or GND			±1		
	Control inputs	$V_{CC} = 0 \text{ or } 3.6 \text{ V},$ $V_{I} = 5.$				10		
I			V _I = 5.5 V			20	μА	
	A or B ports ⁽²⁾	V _{CC} = 3.6 V	$V_I = V_{CC}$			1		
			$V_1 = 0$			-5		
I _{off}		V _{CC} = 0,	V_{1} or $V_{0} = 0$ to 4.5 V			±100	μΑ	
		V 0.V	V _I = 0.8 V	75				
I _{I(hold)}	A or B ports	$V_{CC} = 3 V$	V _I = 2 V	-75			μΑ	
		$V_{CC} = 3.6 \text{ V},^{(3)}$	$V_1 = 0 \text{ to } 3.6 \text{ V}$			±500		
l _{OZPU}		$V_{CC} = 0 \text{ to } 1.5 \text{ V}, V_{O} = 0.5 \text{ V to}$	3 V, OE = don't care			±100	μΑ	
I _{OZPD}		$V_{CC} = 1.5 \text{ V to } 0, V_{O} = 0.5 \text{ V to}$	3 V, OE = don't care			±100	μΑ	
-			Outputs high			0.38		
I _{CC}		$V_{CC} = 3.6 \text{ V}, I_{O} = 0,$ $V_{I} = V_{CC} \text{ or GND}$	Outputs low			10	mA	
		VI = VCC OF GIVE	Outputs disabled			0.38		
Δl _{CC} ⁽⁴⁾		V _{CC} = 3 V to 3.6 V, One input a Other inputs at V _{CC} or GND	t V _{CC} – 0.6 V,			0.2	mA	
Cı		V _I = 3 V or 0			4		pF	
Co		V _O = 3 V or 0			10		pF	

 ⁽¹⁾ All typical values are at V_{CC} = 3.3 V, T_A = 25°C.
 (2) Unused pins at V_{CC} or GND
 (3) This is the bus-hold maximum dynamic current. It is the minimum overdrive current required to switch the input from one state to

This is the increase in supply current for each input that is at the specified TTL voltage level, rather than V_{CC} or GND.

SN74LVTH32245 3.3-V ABT 32-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS





Switching Characteristics

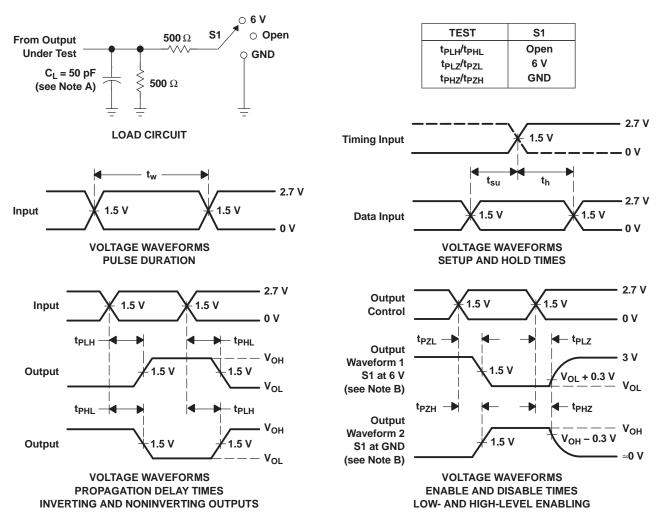
over recommended operating free-air temperature range (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	TO (OUTPUT)				V _{CC} = 2.7 V		UNIT
	(INPUT)	(OUTPUT)	MIN	TYP ⁽¹⁾	MAX	MIN	MAX	
t _{PLH}	A or B	D or A	1.5	2.3	3.3		3.7	20
t _{PHL}	AUID	B or A	1.3	2.1	3.3		3.5	ns
t _{PZH}	ŌĒ	A or B	1.5	2.8	4.5		5.3	ns
t _{PZL}	OE	A or B	1.6	2.9	4.6		5.2	115
t _{PHZ}	ŌĒ	A or B	2.3	3.7	5.1		5.5	20
t _{PLZ}	OE	AUID	2.2	3.5	5.1		5.4	ns
t _{sk(LH)}					0.5			20
t _{sk(HL)}					0.5			ns

⁽¹⁾ All typical values are at V_{CC} = 3.3 V, T_A = 25°C.



PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_I includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, Z_{O} = 50 Ω , $t_{f} \leq$ 2.5 ns, $t_{f} \leq$ 2.5 ns.
- D. The outputs are measured one at a time, with one transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms



PACKAGE OPTION ADDENDUM

www.ti.com 29-Apr-2010

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74LVTH32245GKER	NRND	LFBGA	GKE	96	1000	TBD	SNPB	Level-2-235C-1 YEAR
SN74LVTH32245ZKER	ACTIVE	LFBGA	ZKE	96	1000	Green (RoHS & no Sb/Br)	SNAGCU	Level-3-260C-168 HR

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE MATERIALS INFORMATION

www.ti.com 23-Jul-2011

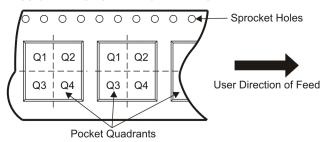
TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LVTH32245GKER	LFBGA	GKE	96	1000	330.0	24.4	5.7	13.7	2.0	8.0	24.0	Q1
SN74LVTH32245ZKER	LFBGA	ZKE	96	1000	330.0	24.4	5.7	13.7	2.0	8.0	24.0	Q1

www.ti.com 23-Jul-2011

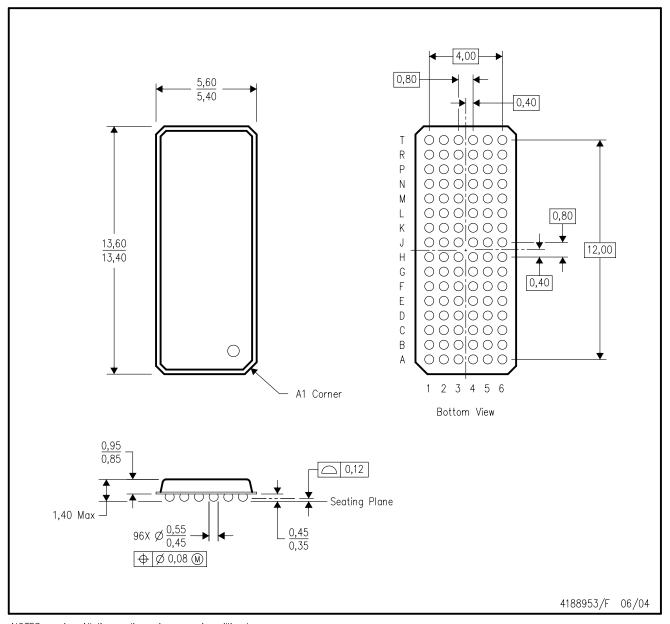


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LVTH32245GKER	LFBGA	GKE	96	1000	333.2	345.9	31.8
SN74LVTH32245ZKER	LFBGA	ZKE	96	1000	333.2	345.9	31.8

GKE (R-PBGA-N96)

PLASTIC BALL GRID ARRAY



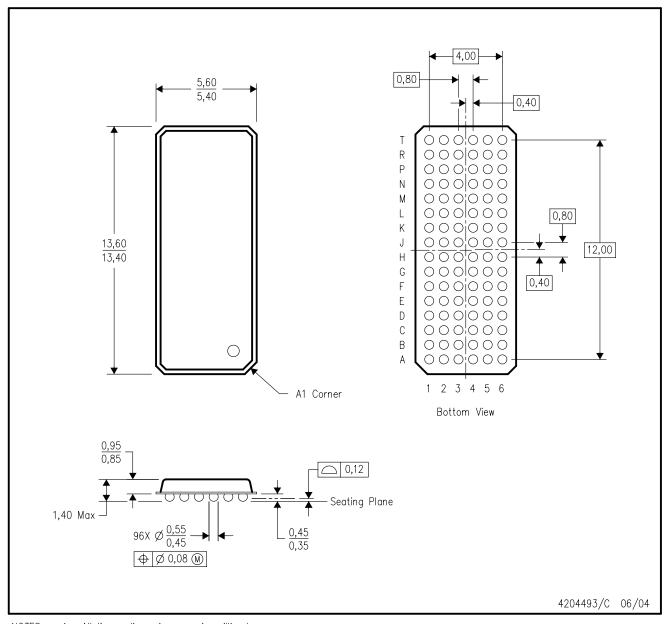
NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MO-205 variation CC.
- D. This package is tin-lead (SnPb). Refer to the 96 ZKE package (drawing 4204493) for lead-free.



ZKE (R-PBGA-N96)

PLASTIC BALL GRID ARRAY



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MO-205 variation CC.
- D. This package is lead-free. Refer to the 96 GKE package (drawing 4188953) for tin-lead (SnPb).



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