

600 V, 0.5 A NPN high-voltage low VCEsat (BISS) transistor13 March 2015Product data sheet

1. General description

NPN high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9560Z

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability
- High collector current gain h_{FE} at high I_C
- AEC-Q101 qualified

3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

4. Quick reference data

Table 1. Quick reference data								
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{CEO}	collector-emitter voltage	open base		-	-	600	V	
I _C	collector current			-	-	0.5	А	
h _{FE}	DC current gain	V_{CE} = 10 V; I _C = 50 mA; T _{amb} = 25 °C		70	135	-		



5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	2,4
2	С	collector		1-1
3	Е	emitter		· M
4	С	collector	[]1 []2 []3 SC-73 (SOT223)	3 sym016

6. Ordering information

Table 3. Ordering information								
Type number	Package							
	Name	Description	Version					
PBHV8560Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223					

7. Marking

Table 4. Marking codes	
Type number	Marking code
PBHV8560Z	HV856Z

Limiting values 8.

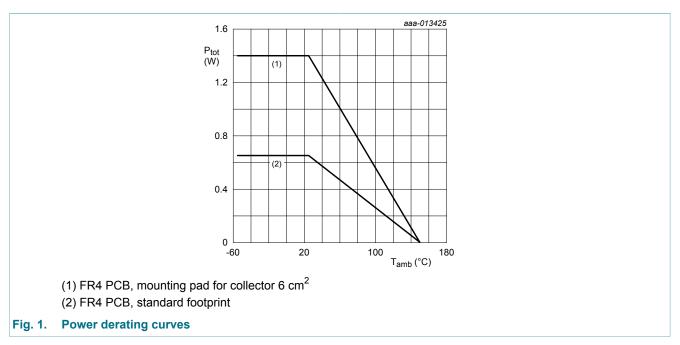
Table 5. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CBO}	collector-base voltage	open emitter		-	600	V
V _{CEO}	collector-emitter voltage	open base		-	600	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	600	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	0.5	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.4	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. [1] [2]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



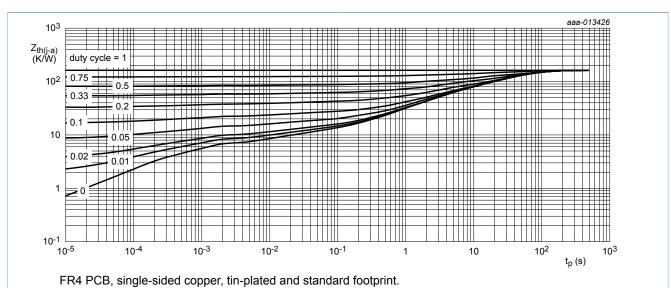
Product data sheet

9. Thermal characteristics

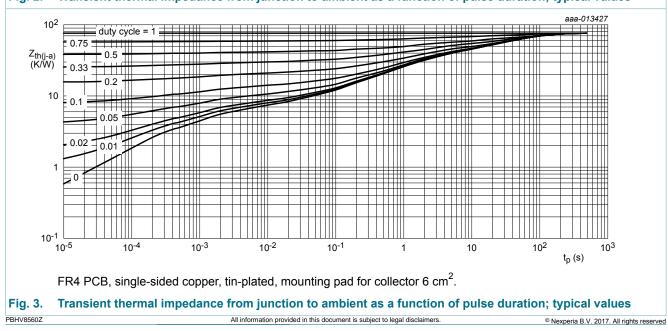
Table 6. Thermal characteristics								
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
from ju	thermal resistance	in free air	[1]	-	-	190	K/W	
	from junction to ambient		[2]	-	-	89	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W	

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



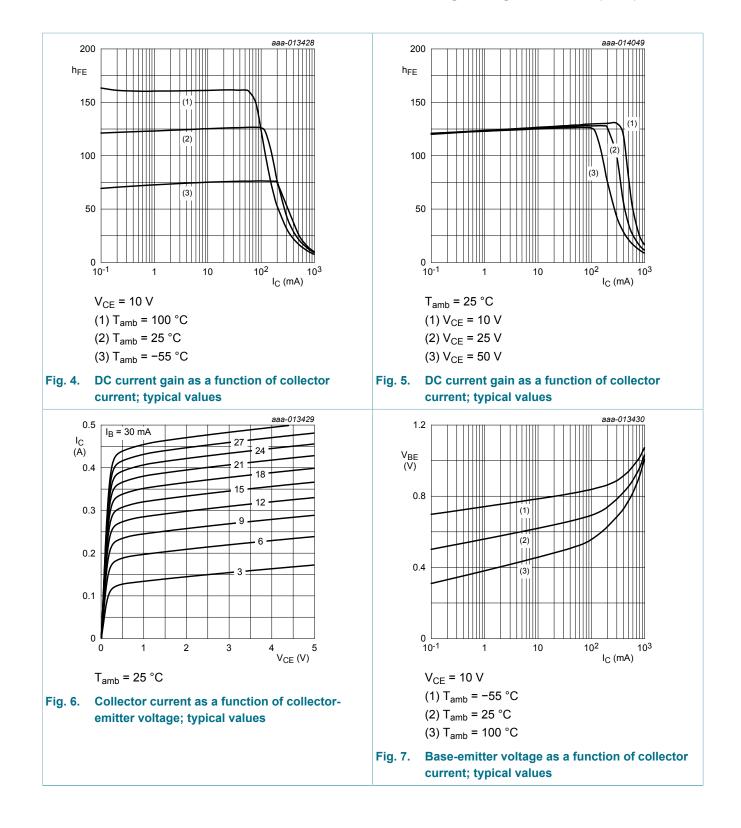




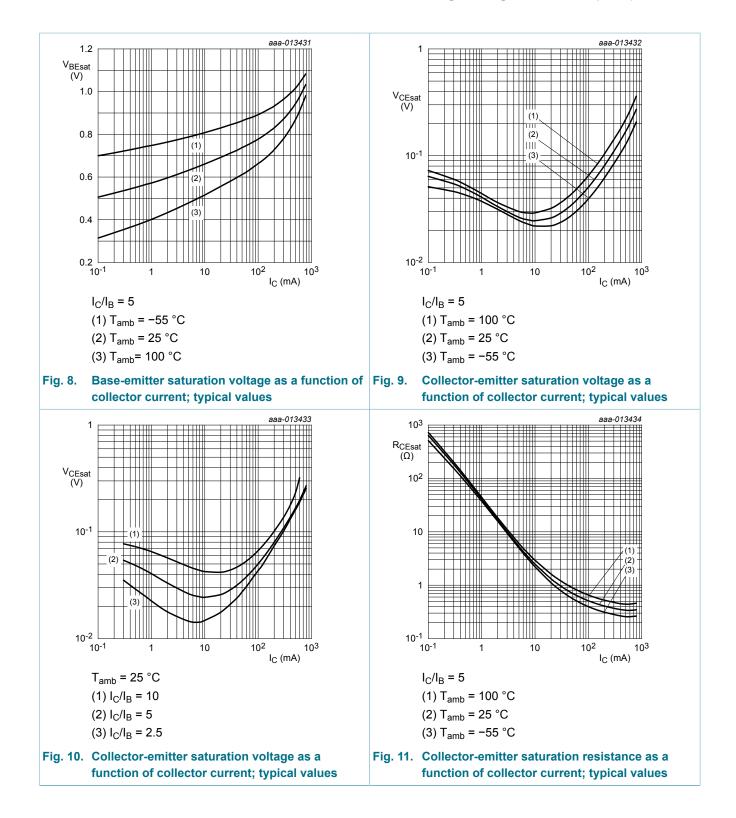
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Symbol	Falameter			тур	IVIAA	Onit
I _{CBO}	collector-base cut-off	V _{CB} = 400 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V_{CB} = 400 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{CES}	collector-emitter cut-off current	V_{CE} = 400 V; V_{BE} = 0 V; T_{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V_{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE} DC	DC current gain	V_{CE} = 10 V; I _C = 50 mA; T _{amb} = 25 °C	70	135	-	
		$\begin{split} V_{CE} &= 10 \text{ V}; \text{ I}_{C} = 100 \text{ mA}; \text{t}_{p} \leq 300 \mu\text{s}; \\ \delta \leq 0.02 ; \text{T}_{amb} = 25 ^{\circ}\text{C} \end{split}$	70	135	-	
V _{CEsat}	collector-emitter	I_{C} = 50 mA; I_{B} = 5 mA; T_{amb} = 25 °C	-	50	100	mV
	saturation voltage	$\begin{split} I_{C} &= 100 \text{ mA; } I_{B} = 20 \text{ mA; } t_{p} \leq 300 \mu\text{s;} \\ \delta \leq 0.02 ; T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	50	100	mV
V _{BEsat}	base-emitter saturation voltage	$\begin{split} I_{C} &= 50 \text{ mA; } I_{B} = 5 \text{ mA; pulsed;} \\ t_{p} &\leq 300 \mu\text{s; } \delta \leq 0.02 \text{; } T_{amb} = 25 ^{\circ}\text{C} \end{split}$	-	-	950	mV
C _c	collector capacitance	$V_{CB} = 20 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A};$ f = 1 MHz; T _{amb} = 25 °C	-	7.5	-	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	710	-	pF

600 V, 0.5 A NPN high-voltage low VCEsat (BISS) transistor



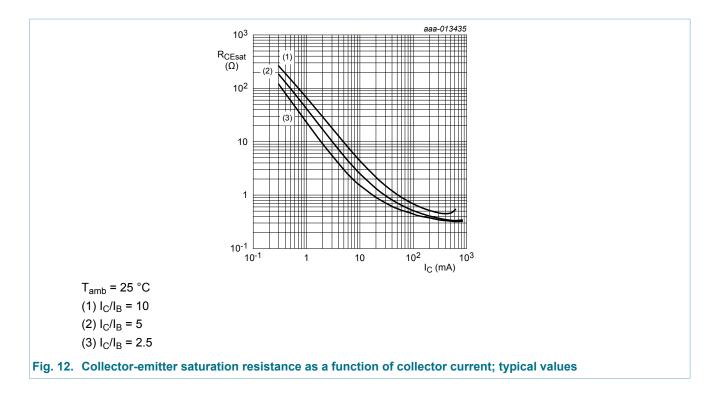
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PBHV8560Z

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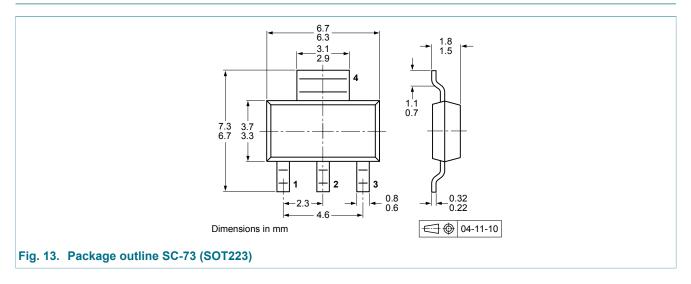


11. Test information

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

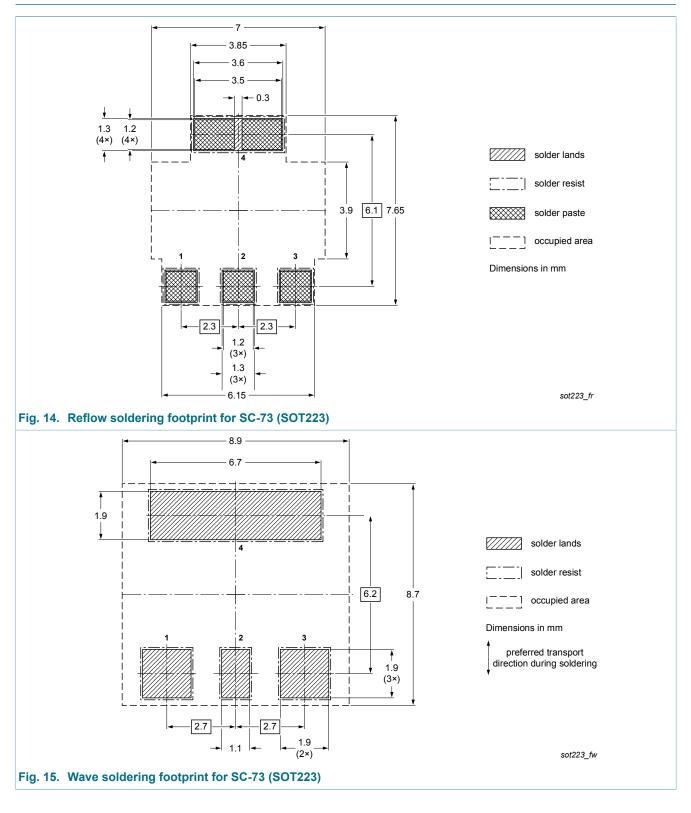
12. Package outline



Product data sheet

600 V, 0.5 A NPN high-voltage low VCEsat (BISS) transistor

13. Soldering



14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV8560Z v.1	20150313	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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600 V, 0.5 A NPN high-voltage low VCEsat (BISS) transistor

16. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	5
11	Test information	8
11.1	Quality information	8
12	Package outline	8
13	Soldering	9
14	Revision history	10
15	Legal information	11
15.1	Data sheet status	11
15.2	Definitions	11
15.3	Disclaimers	11
15.4	Trademarks	12

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