



# High Temperature Silicon Carbide Power Schottky Diode

## $V_{RRM}$ = 600 V $I_{F (Tc=25^{\circ}C)}$ = 4 A $Q_{C}$ = 9 nC

#### **Features**

- 600 V Schottky rectifier
- 225 °C maximum operating temperature
- Zero reverse recovery charge
- · Superior surge current capability
- Positive temperature coefficient of V<sub>F</sub>
- Temperature independent switching behavior
- Lowest figure of merit Q<sub>C</sub>/I<sub>E</sub>
- Available screened to Mil-PRF-19500

#### **Package**

RoHS Compliant





TO - 46

## **Advantages**

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- · Low switching losses
- Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

## **Applications**

- Down Hole Oil Drilling
- Geothermal Instrumentation
- · Solenoid Actuators
- General Purpose High-Temperature Switching
- Amplifiers
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)

#### Maximum Ratings at T<sub>j</sub> = 225 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Continuous forward current	$I_{F}$	$T_C = 25  ^{\circ}C$	4	Α
Continuous forward current	$I_{F}$	T <sub>C</sub> ≤ 225 °C	2	Α
RMS forward current	I <sub>F(RMS)</sub>	T <sub>C</sub> ≤ 225 °C	4	Α
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	$T_C = 25  ^{\circ}C,  t_P = 10  \text{ms}$	10	Α
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25  ^{\circ}C, t_P = 10  \mu s$	65	Α
l <sup>2</sup> t value	∫i² dt	$T_C = 25$ °C, $t_P = 10$ ms	0.5	$A^2S$
Power dissipation	P <sub>tot</sub>	T <sub>C</sub> = 25 °C	64	W
Operating and storage temperature	$T_j$ , $T_stg$		-55 to 225	°C

#### Electrical Characteristics at T<sub>i</sub> = 225 °C, unless otherwise specified

Parameter	Cumbal	Conditions —		Values		I I m i 4	
	Symbol			min.	typ.	max.	Unit
Diode forward voltage	W	$I_F = 1 \text{ A}, T_j = 25 \text{ °C}$ $I_F = 1 \text{ A}, T_j = 225 \text{ °C}$		1.6			
	$V_{F}$				2.6		V
Reverse current	1	$V_R = 600 \text{ V}, T_j = 25 ^{\circ}\text{C}$		1	5	μΑ	
	I <sub>R</sub>	$V_R = 600 \text{ V}, T_j = 225 ^{\circ}\text{C}$		5	50		
Total capacitive charge	$Q_{C}$	$I_F \le I_{F,MAX}$ - $dI_F/dt = 200 \text{ A/}\mu\text{s}$	V <sub>R</sub> = 600 V		9		nC
Switching time	t <sub>s</sub>	T <sub>j</sub> = 210 °C	V <sub>R</sub> = 600 V		< 17		ns
Total capacitance	С	$V_R = 1 V$ , $f = 1 MHz$ ,	T <sub>j</sub> = 25 °C		76		nE
	C	$V_{P} = 600 \text{ V. } f = 1 \text{ MHz}$	. T <sub>i</sub> = 25 °C		15		pF

#### **Thermal Characteristics**

Thermal resistance, junction - case	$R_{thJC}$	5.55	*C/W
Mechanical Properties			
Mounting torque	M	0.6	Nm



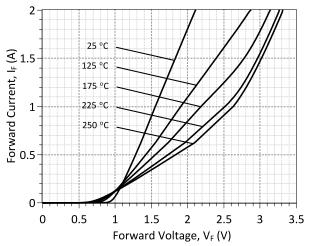


Figure 1: Typical Forward Characteristics

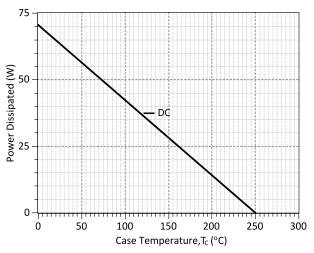


Figure 3: Power Derating Curve

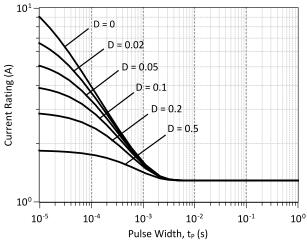


Figure 5: Current vs Pulse Duration Curves at T<sub>C</sub> = 225 °C

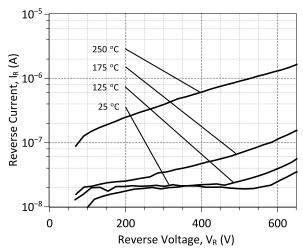


Figure 2: Typical Reverse Characteristics

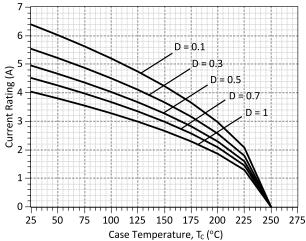


Figure 4: Current Derating Curves (D =  $t_P/T$ ,  $t_P$ = 400  $\mu$ s) (Considering worst case  $Z_{th}$  conditions )

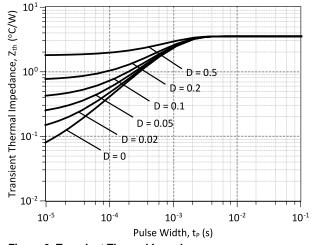


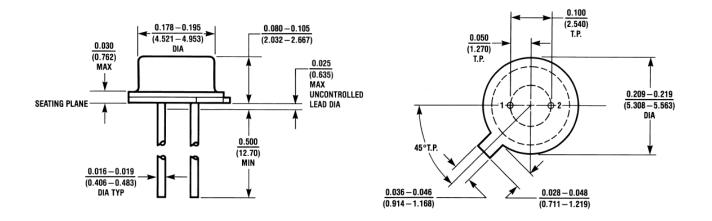
Figure 6: Transient Thermal Impedance



### **Package Dimensions:**

TO-46

#### **PACKAGE OUTLINE**



#### NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

Revision History					
Date	Revision	Comments	Supersedes		
2014/08/29	0	Initial release			

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.



## **SPICE Model Parameters**

This is a secure document. Copy this code from the SPICE model PDF file on our website into a SPICE software program for simulation of the GB02SHT06-46.

```
MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.0
                                 $
     $Date: 29-AUG-2014
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
     Dulles, VA 20166
     COPYRIGHT (C) 2014 GeneSiC Semiconductor Inc.
     ALL RIGHTS RESERVED
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of GB02SHT06-46 SPICE Model
.SUBCKT GB02SHT06ANODE KATHODE
D1 ANODE KATHODE GB02SHT06 25C; Call the Schottky Diode Model
D2 ANODE KATHODE GB02SHT06 PIN; Call the PiN Diode Model
.MODEL GB02SHT06 25C D
+ IS
         3.57E-18
                                      0.49751
                           RS
+ TRS1
          0.0057
                           TRS2
                                      2.40E-05
+ N
          1
                          IKF
                                      322
+ EG
          1.2
                          XTI
                                      3
                                      0.371817384
+ CJO
         9.12E-11
                           VJ
+ M
          1.527759838
                          FC
                                      0.5
+ TT
          1.00E-10
                                      600
                           BV
          1.00E-03
                           VPK
                                      600
+ IBV
+ IAVE
                           TYPE
                                      SiC Schottky
+ MFG
          GeneSiC Semiconductor
.MODEL GB02SHT06 PIN D
+ IS
       5.73E-11
                           RS
                                      0.72994
+ N
                           IKF
                                      800
          3.23
                                      -14
+ EG
                           XTI
+ FC
          0.5
                           TT
                                      0
+ BV
          600
                           IBV
                                      1.00E-03
          600
+ VPK
                           IAVE
+ TYPE
          SiC PiN
.ENDS
```

\* End of GB02SHT06 SPICE Model