# VS-200MT40KPbF

Vishay Semiconductors



# Three Phase Bridge (Power Module), 200 A



PRODUCT SUMMARY		
Ιο	200 A	
V <sub>RRM</sub>	400 V	
Package	MT-K	
Circuit	Three phase bridge	

## FEATURES

 Package fully compatible with the industry standard INT-A-PAK power modules series



**RoHS** COMPLIANT

- High thermal conductivity package, electrically insulated case
- Low power loss
- Excellent power volume ratio, outline for easy connections to power transistor and IGBT modules
- 4000 V<sub>RMS</sub> isolating voltage
- UL E78996 approved 🔊
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

It extends the existing range of MT...KB bridges an extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
		200	A	
l <sub>O</sub>	T <sub>C</sub>	85	°C	
I <sub>FSM</sub>	50 Hz	1800	٨	
	60 Hz	1880	— A	
l <sup>2</sup> t	50 Hz	16.2	kA <sup>2</sup> s	
	60 Hz	14.7	KA-S	
l²√t		162	kA²√s	
V <sub>RRM</sub>		400	V	
T <sub>Stg</sub>	Pango	-40 to 150	°C	
TJ	Range	-40 10 150	C	

#### ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS					
TYPE NUMBER	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = 150 °C mA		
VS-200MT40KPbF	400	500	6		

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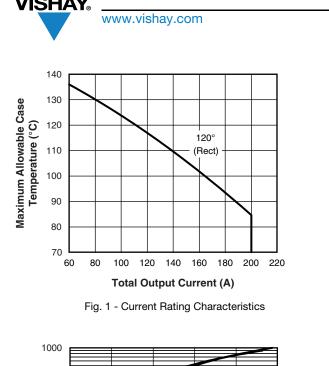
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FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum RMS output current		AS output current Io 120° rect. conduction angle	100° rest conduction angle		200	А
at case temperature	Ι <sub>Ο</sub>	120 1601.001	iduction angle		85	°C
	I <sub>TSM</sub>	t = 10 ms	No voltage	eapplied	1800	A
Maximum peak, one-cycle forward. non-repetitive on state surge current		t = 8.3 ms	reapplied		1880	
		t = 10 ms	100 % V <sub>RRM</sub>		1520	
		t = 8.3 ms	reapplied	Initial T T. maximum	1590	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage	Initial $T_J = T_J$ maximum	16.2	kA <sup>2</sup> s
		t = 8.3 ms	reapplied		14.7	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		11.6	
		t = 8.3 ms			12.6	
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied		162	kA²√s	
Value of threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> maximum		0.76	V	
Slope resistance	r <sub>t</sub>			2.4	mΩ	
Maximum forward voltage drop	V <sub>FM</sub>	$I_{pk}$ = 200 A, $T_J$ = 25 °C, $t_p$ = 400 $\mu s$ single junction		1.40	V	
Isolation voltage	V <sub>ISOL</sub>	$T_J = 25 \text{ °C}$ all terminal shorted, f = 50 Hz, t = 1 s 4000		V		

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-40 to 150	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation per module	0.12	K/W
		DC operation per junction	0.69	
		120° rect. conduction angle per module	0.14	
		120° rect. conduction angle per junction	0.82	
Maximum thermal resistance, case to heatsink per module	R <sub>thCS</sub>	Mounting surface smooth, flat and greased. Heatsink compund thermal conductivity = 0.42 W/mK	0.033	
Mounting torque ± 10 % to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow	4 to 6	Nm
Approximate weight		for the spread of the compound. Lubricated threads.	176	g



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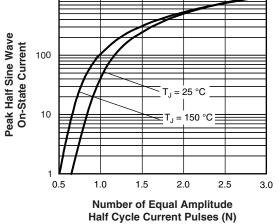


Fig. 2 - On-State Voltage Drop Characteristics

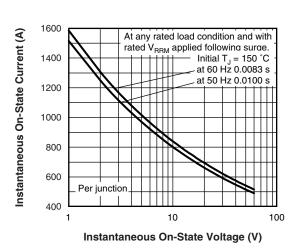
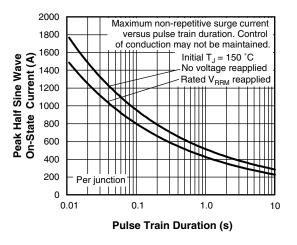


Fig. 3 - Maximum Non-Repetitve Surge Current





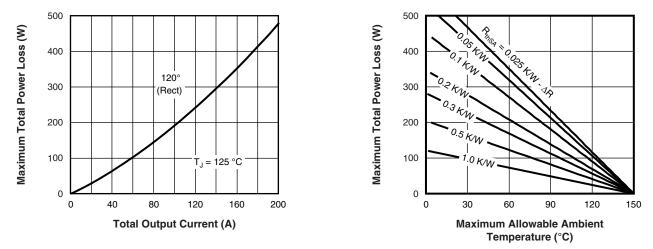


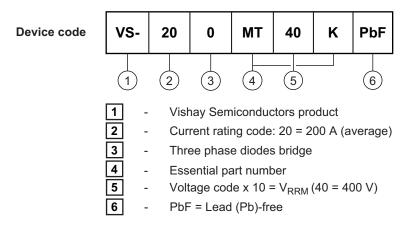
Fig. 5 - Current Rating Nomogram (1 Module Per Heatsink)

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#### VS-200MT40KPbF www.vishay.com **Vishay Semiconductors** 10 Z<sub>thJC</sub> - Transient Thermal Impedance (K/W) Steady state value R<sub>thJC</sub> per junction = 0.69 K/W (DC operation) 1 0.1 0.01 0.0001 0.001 0.01 0.1 1 10 Square Wave Pulse Duration (s)

Fig. 6 - Thermal Impedance ZthJC Characteristics

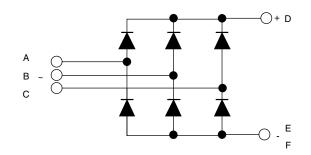
### **ORDERING INFORMATION TABLE**



#### Note

• To order the optional hardware go to www.vishay.com/doc?95172

#### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95004	

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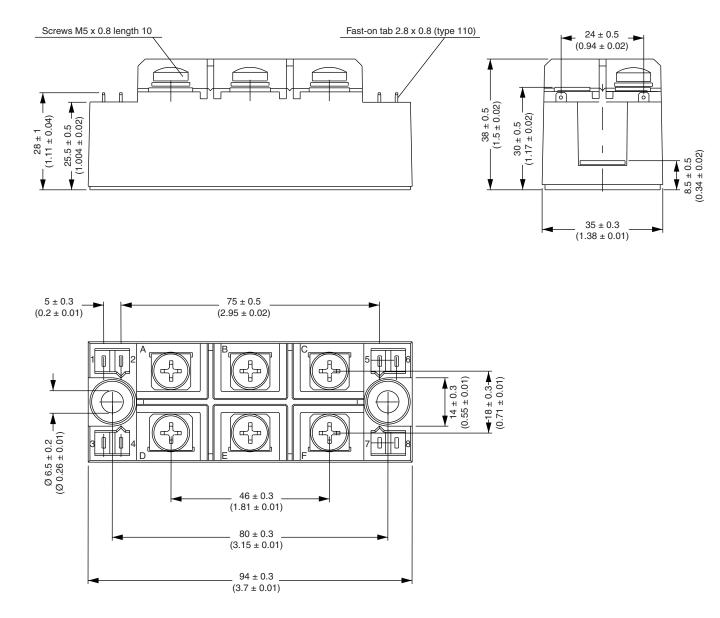


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# MTK (with and without optional barrier)

#### **DIMENSIONS WITH OPTIONAL BARRIERS** in millimeters (inches)

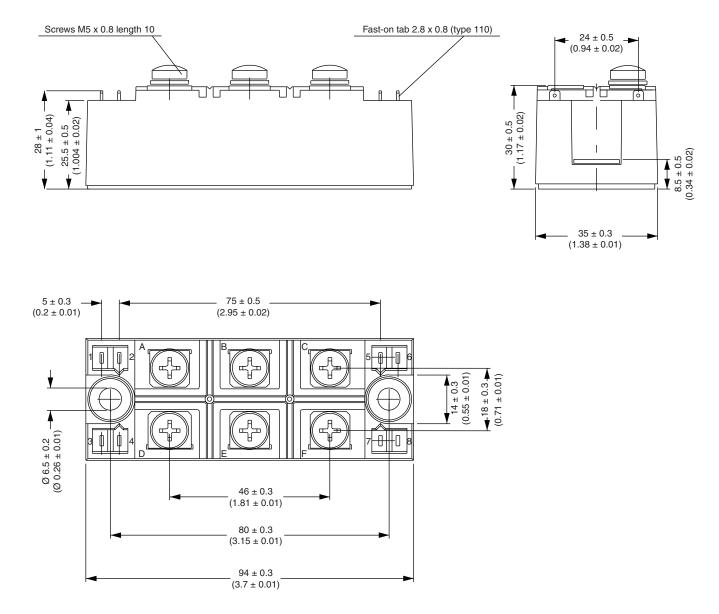
SHAY



Vishay Semiconductors MTK (with and without optional barrier)



## DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)





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