

### Vishay Semiconductors

COMPLIANT

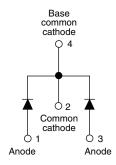
HALOGEN

FREE

## Ultrafast Rectifier, 2 x 3 A FRED Pt®



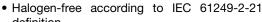
D-PAK (TO-252AA)



PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I <sub>F(AV)</sub>	2 x 3 A						
$V_{R}$	200 V						
V <sub>F</sub> at I <sub>F</sub>	1.0 V						
t <sub>rr</sub> typ.	See Recovery table						
T <sub>J</sub> max.	175 °C						
Diode variation	Common cathode						

### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Compliant to RoHS Directive 2002/95/EC







#### **DESCRIPTION/APPLICATIONS**

VS-MURD620CT-M3 is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Peak repetitive reverse voltage	$V_{RRM}$		200	V					
Average rectified forward current per device	I <sub>F(AV)</sub>	Total device, rated V <sub>R</sub> , T <sub>C</sub> = 146 °C	6						
Non-repetitive peak surge current	I <sub>FSM</sub>		50	Α					
Peak repetitive forward current per diode	I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 146 °C	6						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	200	-	-					
		I <sub>F</sub> = 3 A	-	-	1.0					
Converd velters	V <sub>F</sub>	I <sub>F</sub> = 3 A, T <sub>J</sub> = 125 °C	-	-	0.96	V				
Forward voltage		I <sub>F</sub> = 6 A	=	-	1.2					
		I <sub>F</sub> = 6 A, T <sub>J</sub> = 125 °C	-	-	1.13					
Deverse legicore aument	-1	V <sub>R</sub> = V <sub>R</sub> rated	=	-	5					
Reverse leakage current	I <sub>R</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	=	-	250	μΑ				
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V - 12		12	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	=	8.0	-	nΗ				

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## VS-MURD620CT-M3

## Vishay Semiconductors Ultrafast Rectifier, 2 x 3 A FRED Pt®



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1.0 A, dI_F/dt =$	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	35			
	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>REC</sub> = 0.25 A		-	-	25			
		T <sub>J</sub> = 25 °C		-	19	-	ns A		
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 3 A dI <sub>F</sub> /dt = 200 A/µs	-	26	-			
Dook recovery ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.1	-			
Peak recovery current		T <sub>J</sub> = 125 °C	V <sub>R</sub> = 160 V	-	4.6	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	30	-	nC		
		T <sub>J</sub> = 125 °C		-	60	-			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C			
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	9.0				
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	80	°C/W			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	-	-				
\Maight			-	0.3	-	g			
Weight			-	0.01	-	oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style D-PAK		MURE	620CT				



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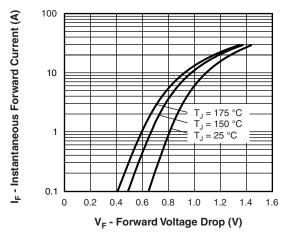


Fig. 1 - Typical Forward Voltage Drop Characteristics

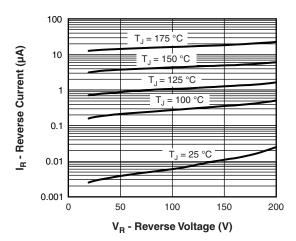


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

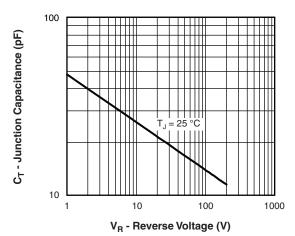


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

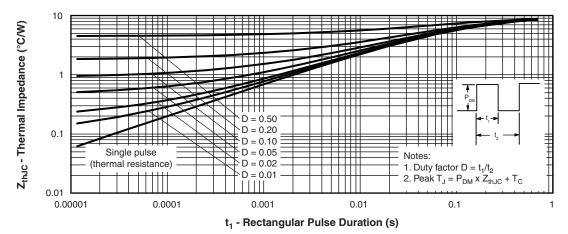


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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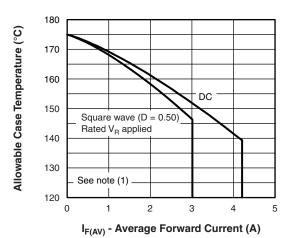


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

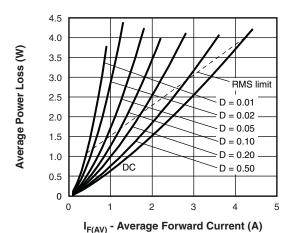


Fig. 6 - Forward Power Loss Characteristics

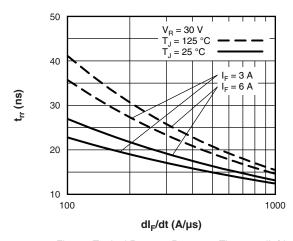


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

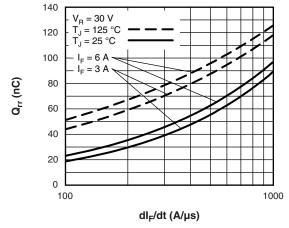


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $^{(1)}$  Formula used:  $T_{C} = T_{J}$  - (Pd + Pd\_{REV}) x  $R_{thJC};$ Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_{R} (1 - D)$ ;  $I_{R}$  at  $V_{R1}$  = Rated  $V_{R1}$ 



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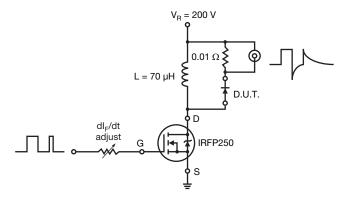
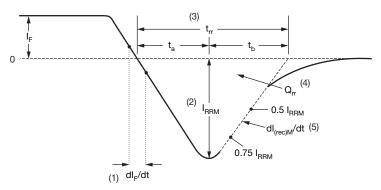


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75  $I_{RRM}$  and 0.50  $I_{RRM}$ extrapolated to zero current.
- (4) Q<sub>rr</sub> area under curve defined by t<sub>rr</sub> and I<sub>RRM</sub>

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

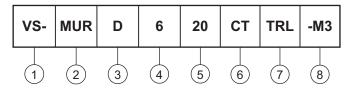
Fig. 10 - Reverse Recovery Waveform and Definitions

# Vishay Semiconductors Ultrafast Rectifier, 2 x 3 A FRED Pt®



#### **ORDERING INFORMATION TABLE**

Device code



Vishay Semiconductors product

Ultrafast MUR series

D = D-PAK

Current rating (6 = 6 A)

Voltage rating (20 = 200 V)

TR = Tape and reel CT = Center tap (dual)

TRL = Tape and reel (left oriented) Tape and reel suffix TRR = Tape and reel (right oriented)

Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-MURD620CT-M3	75	3000	Antistatic plastic tube						
VS-MURD620CTTR-M3	2000	2000	13" diameter reel						
VS-MURD620CTTRL-M3	3000	3000	13" diameter reel						
VS-MURD620CTTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					



### Vishay Semiconductors

**NOTES** 

3

2

MAX.

0.410

0.070

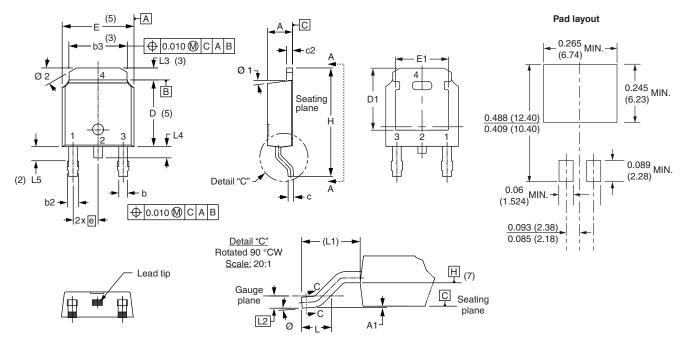
0.050

0.040

0.060

# **D-PAK (TO-252AA)**

#### **DIMENSIONS** in millimeters and inches



Ī	SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES			
		MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX	
ſ	Α	2.18	2.39	0.086	0.094		e 2.2		e 2.29 BSC		0.090	BSC	
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41	
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07	
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74 BSC 0.51 BSC		0.108 REF. 0.020 BSC		
ſ	b3	4.95	5.46	0.195	0.215	3		L2					
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05	
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04	
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06	
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



### **Legal Disclaimer Notice**

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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

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