VS-ETU1506-M3,VS-ETU1506FP-M3

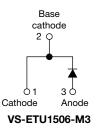
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Vishay Semiconductors

Ultrafast Rectifier, 15 A FRED Pt[®]



2L TO-220AC





Cathode Anode

PRODUCT SUMMARY				
Package	2L TO-220AC, 2L TO-220FP			
I _{F(AV)}	15 A			
V _R	600 V			
V _F at I _F	1.9 V			
t _{rr} (typ.)	24 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES

- Low forward voltage drop
- Ultrafast soft recovery time
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- True 2 pin package
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition
- Designed and qualified according to JEDEC-JESD47

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		600	V		
Average restified forward surrent in DC	I _{F(AV)}	T _C = 151 °C	- 15	А		
Average rectified forward current in DC		T _C = 103 °C	15			
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	160			
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MAX.			MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
	N	I _F = 15 A	-	1.35	1.9	V	
Forward voltage	V _F	I _F = 15 A, T _J = 150 °C	-	1.1	1.3		
Povoroo lookago ourront		$V_R = V_R$ rated	-	0.01	15		
Reverse leakage current I _R T _J =		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	200	μA	
Junction capacitance	CT	V _R = 600 V	-	12	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH	

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(Pb) (e3) RoHS

COMPLIANT HALOGEN



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I_F = 1 A, dI_F/dt = 100 A/µs, V_R = 30 V		-	24	28	
Reverse recovery time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 1000 \text{ cm}^2$	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		36	47	
neverse recovery time	t _{rr}	T _J = 25 °C		-	40	-	ns
		T _J = 125 °C	I _F = 15 A dI _F /dt = 200 A/μs V _R = 390 V	-	87	-	
		T _J = 25 °C		-	5	-	А
Peak recovery current	I _{RRM}	T _J = 125 °C		-	9	-	~
	0	T _J = 25 °C		-	107	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	430	-	nc
Reverse recovery time	t _{rr}	··· ··· ··· ··· ··· ··· ··· ··· ··· ··	I _F = 15 A	-	53	-	ns
Peak recovery current	I _{RRM}		dI _F /dt = 800 A/µs	-	25	-	А
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	730	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance,			-	1.2	1.4	
junction to case FULL-PAK	R _{thJC}		-	3.7	4.3	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
			-	2	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style 2L TO-220AC	ETU1506		•	
Marking device		Case style 2L TO-220 FULL-PAK		ETU1	506FP	

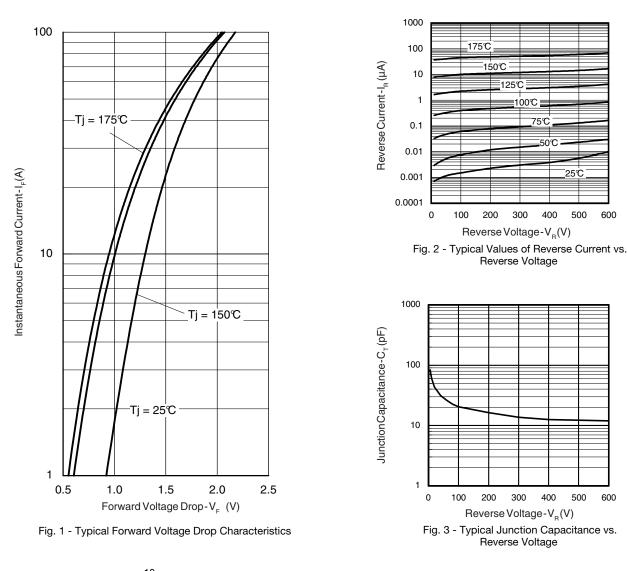


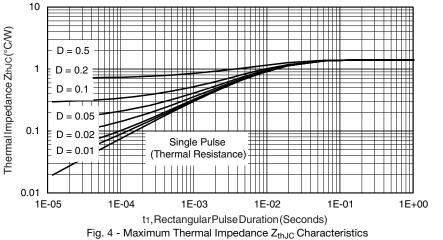
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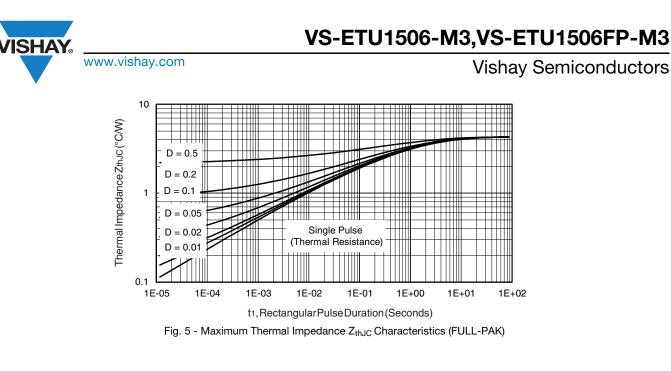
600

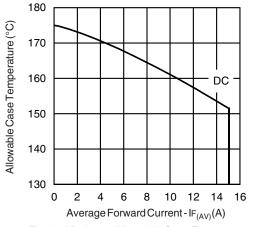
600

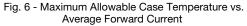


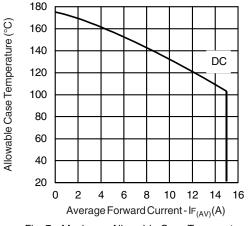


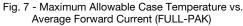
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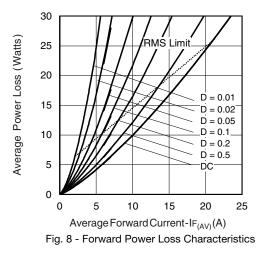






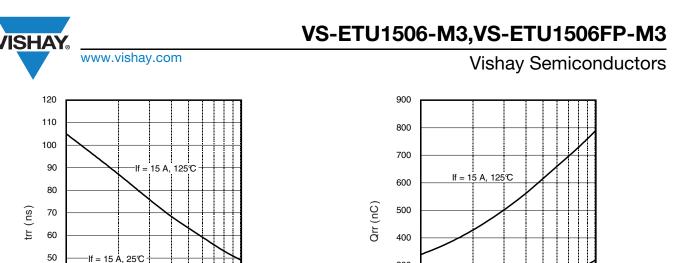






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40

30

20

10

100

typical value

di_F/dt (A/µs)

Fig. 9 - Typical Reverse Recovery vs. dI_F/dt

300

200

100

0

100

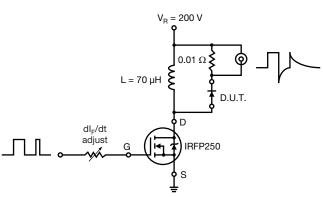
lf = 15 A, 25℃

di_F/dt (A/µs)

Fig. 10 - Typical Stored Charge vs. dl_F/dt

typical value

1000



1000

Fig. 11 - Reverse Recovery Parameter Test Circuit

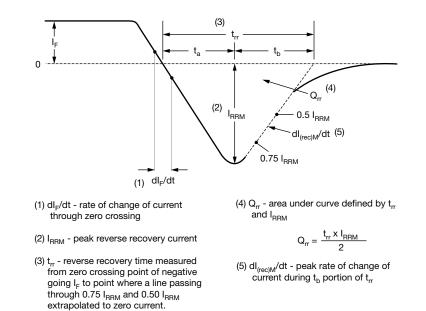


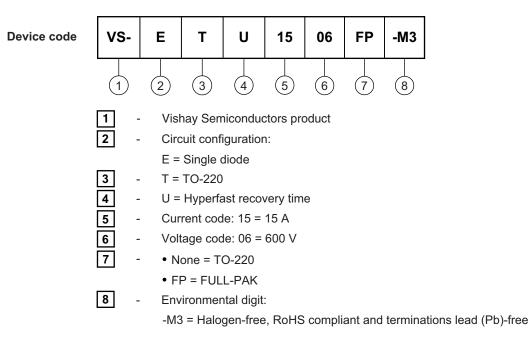
Fig. 12 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)					
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-ETU1506-M3	50	1000	Antistatic plastic tube		
VS-ETU1506FP-M3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS					
Dimensions 2L TO-220AC www.vishay.com/doc?95259					
Dimensions	2L TO-220 FULL-PAK	www.vishay.com/doc?95260			
Part marking information	2L TO-220AC	www.vishay.com/doc?95391			
	2L TO-220 FULL-PAK	www.vishay.com/doc?95392			

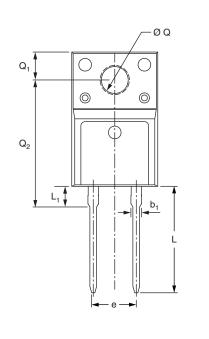


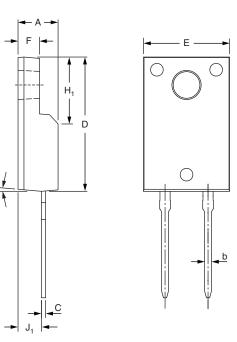


True 2 Pin TO-220 FULL-PAK

θ

DIMENSIONS in millimeters and inches





SYMBOL -	MILLIN	METERS	INC	HES
	MIN.	MAX.	MIN.	MAX.
A	4.53	4.93	0.178	0.194
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	15.67	16.07	0.617	0.633
E	9.96	10.36	0.392	0.408
е	5.08	5.08 typical		typical
F	2.34	2.74	0.092	0.107
H ₁	6.50	6.90	0.256	0.272
J ₁	2.56	2.96	0.101	0.117
L	12.78	13.18	0.503	0.519
L ₁	2.23	2.63	0.088	0.104
ØQ	2.98	3.38	0.117	0.133
Q ₁	3.10	3.50	0.122	0.138
Q ₂	14.80	15.20	0.583	0.598
θ	0°	5°	0°	5°

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