

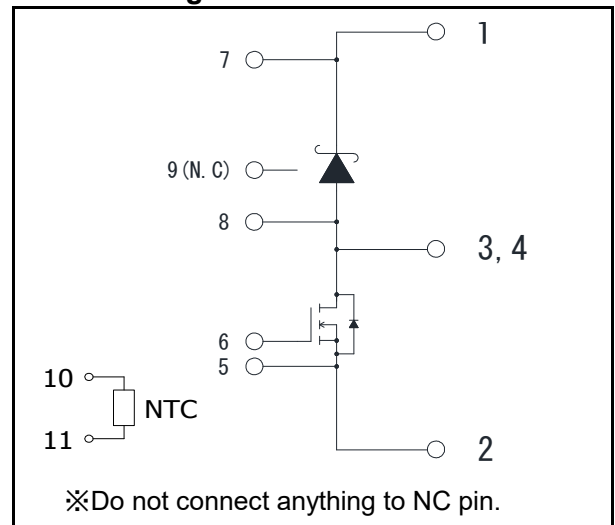
●Application

- Motor drive
- Converter
- Photovoltaics, wind power generation.

●Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

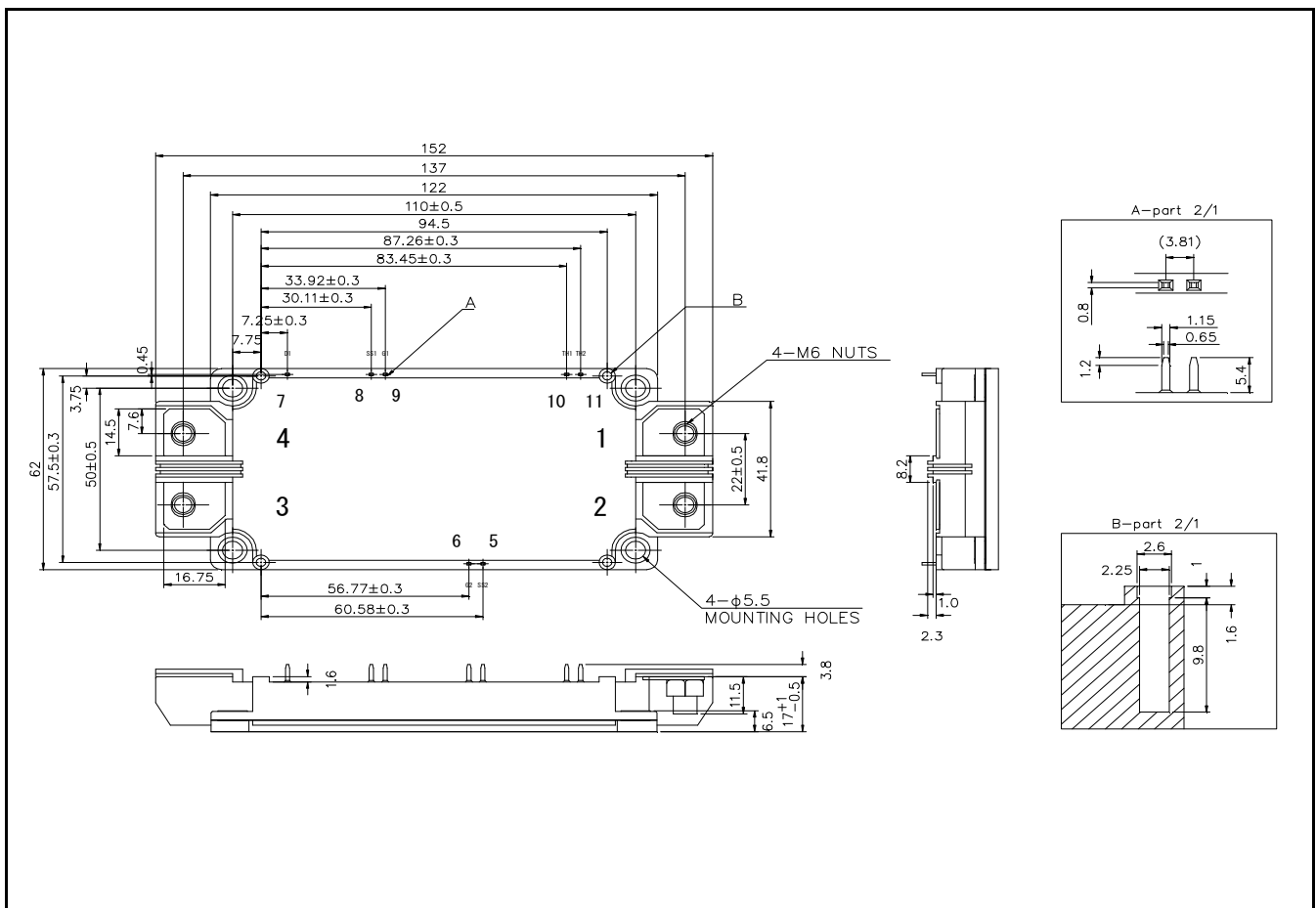
●Circuit diagram



●Construction

This product is a chopper module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

●Dimensions & Pin layout (Unit : mm)



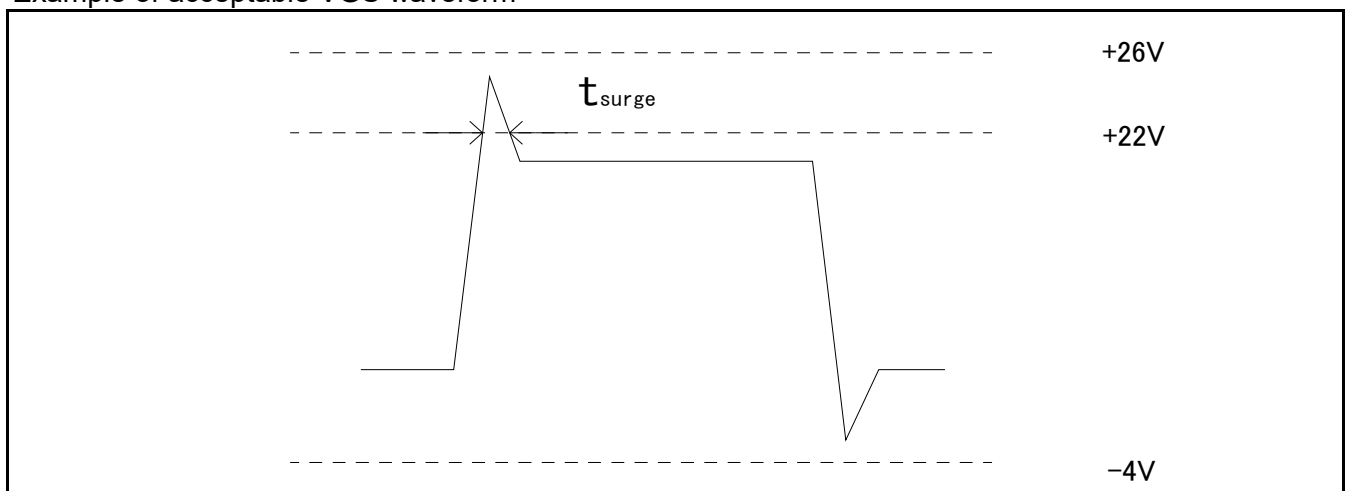
●Absolute maximum ratings ($T_j = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Ratings	Unit
Drain - Source Voltage	V_{DSS}	G-S short	1200	V
Repetitive Reverse Voltage	V_{RRM}	Clamp diode	1200	
Gate - Source Voltage (+)	V_{GSS}	D-S short	22	
Gate - Source Voltage (-)	V_{GSS}	D-S short	-4	
G - S Voltage ($t_{\text{surge}} < 300\text{nsec}$)	V_{GSSsurge}	D-S short	-4 to 26	
Drain Current Note 1)	I_{D}	DC($T_c=60^\circ\text{C}$) $V_{\text{GS}}=18\text{V}$	576	A
	I_{D}	DC($T_c=50^\circ\text{C}$) $V_{\text{GS}}=18\text{V}$	600	
	I_{DRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms $V_{\text{GS}}=18\text{V}$ Note 2)	1200	
Source Current Note 1)	I_{S}	DC($T_c=60^\circ\text{C}$) $V_{\text{GS}}=18\text{V}$	576	
	I_{S}	DC($T_c=50^\circ\text{C}$) $V_{\text{GS}}=18\text{V}$	600	
	I_{SRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms $V_{\text{GS}}=18\text{V}$ Note 2)	1200	
	I_{SRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms $V_{\text{GS}}=0\text{V}$ Note 2)	1200	
Forward Current (clamp diode) Note 1)	I_{F}	DC($T_c = 60^\circ\text{C}$)	600	
	I_{FRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms Note 2)	1200	
Total Power Dissipation Note 3)	P_{tot}	$T_c = 25^\circ\text{C}$	2460	W
Max Junction Temperature	T_{jmax}		175	$^\circ\text{C}$
Junction Temperature	T_{jop}		-40 to 150	
Storage Temperature	T_{stg}		-40 to 125	
Isolation Voltage	V_{isol}	Terminals to baseplate $f = 60\text{Hz}$ AC 1 min.	2500	Vrms
Mounting Torque	-	Main Terminals : M6 screw	4.5	N · m
		Mounting to heat sink M5 screw	3.5	

Note 1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

Note 2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{jmax} .

Note 3) T_j is less than 175°C .

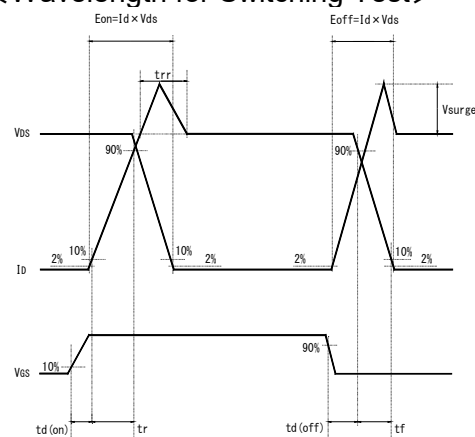
Example of acceptable VGS waveform


●Electrical characteristics (T_j=25°C)

Parameter	Symbol	Conditions		Ratings			Unit
				Min.	Typ.	Max.	
On-state static Drain-Source Voltage	V _{DS(on)}	I _D =600A,V _{GS} =18V	T _j =25°C	—	1.8	2.4	V
			T _j =125°C	—	2.6	—	
			T _j =150°C	—	2.9	4.1	
Drain Cutoff Current	I _{DSS}	V _{DS} =1200V,V _{GS} =0V		—	—	10	uA
Forward Voltage	V _F	I _F =600A	T _j =25°C	—	1.8	2.2	V
			T _j =125°C	—	2.4	—	
			T _j =150°C	—	2.6	3.2	
Reverse current	I _{RRM}	Clamp diode		—	—	4	mA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =10V,I _D =182mA		2.7	—	5.6	V
Gate-Source Leak Current	I _{GSS}	V _{GS} =22V,V _{DS} =0V		—	—	0.5	μA
		V _{GS} =-4V,V _{DS} =0V		-0.5	—	—	
Switching Characteristics	td(on)	V _{GS} (on)=18V、V _{GS} (off)=0V V _{DS} =600V I _D =600A R _G (on)=0.2 ohm, R _G (off)=0.2 ohm Inductive load		—	70	—	ns
	tr			—	50	—	
	trr			—	20	—	
	td (off)			—	240	—	
	tf			—	65	—	
Input Capacitance	C _{iss}	V _{DS} =10V,V _{GS} =0V,200kHz		—	28	—	nF
Gate Resistance	R _{Gint}	T _j =25°C		—	1.4	—	Ω
NTC Rated Resistance	R ₂₅			—	5.0	—	kΩ
NTC B Value	B _{50/25}			—	3370	—	K
Stray Inductance	L _s			—	10.0	—	nH
Creepage Distance	-	Terminal to heat sink		—	16.7	—	mm
		Terminal to terminal		—	16.7	—	mm
Clearance Distance	-	Terminal to heat sink		—	12.0	—	mm
		Terminal to terminal		—	11.0	—	mm
Junction-to -Case Thermal Resistance	R _{th(j-c)}	UMOSFET (1/2 module) Note 4)		—	—	61	°C/kW
		SBD (1/2 module) Note 4)		—	—	61	
Case-to -heat sink Thermal Resistance	R _{th(c-f)}	Case to heat sink, per 1 module. Thermal grease applied. Note 5)		—	15	—	

- Note 4) Measurement of T_c is to be done at the point just under the chip.
- Note 5) Typical value is measured by using thermally conductive grease of λ=0.9W/(m·K).
- Note 6) SiC devices have lower short circuit withstand capability due to high current density. Please be advised to pay careful attention to short circuit accident and try to adjust protection time to shutdown them as short as possible.
- Note 7) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be damaged, please replace such Product with a new one.

<Wavelength for Switching Test>



●Electrical characteristic curves (Typical)

Fig.1 Output characteristic 25°C (TYP)

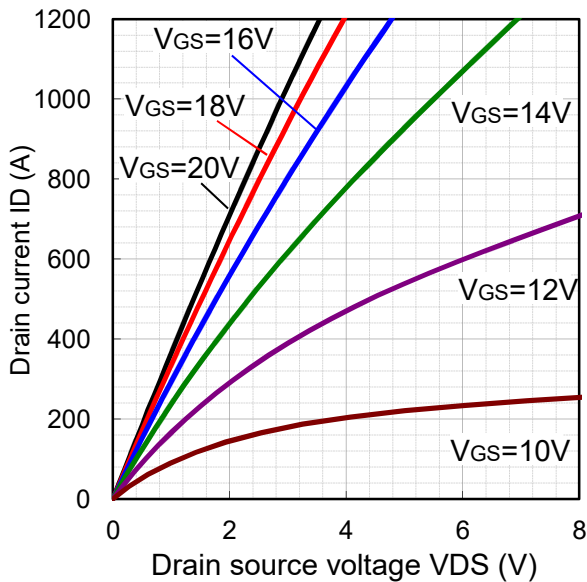


Fig.2 Drain source voltage characteristic (TYP)

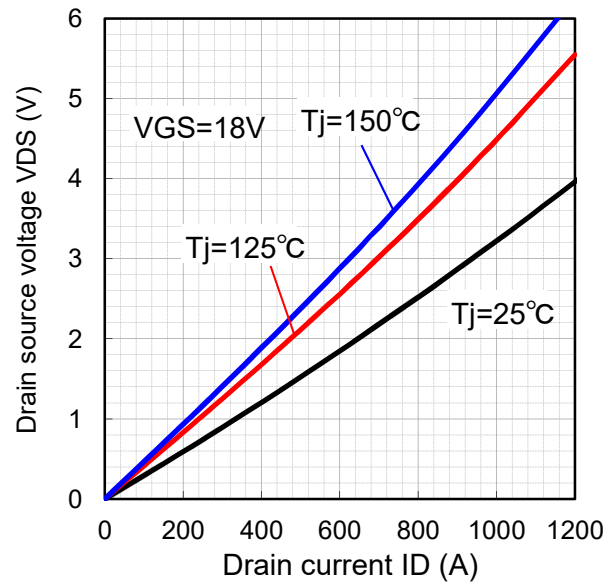
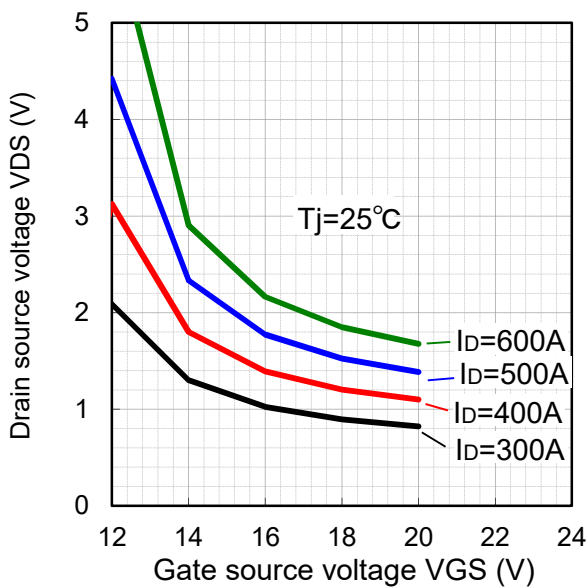
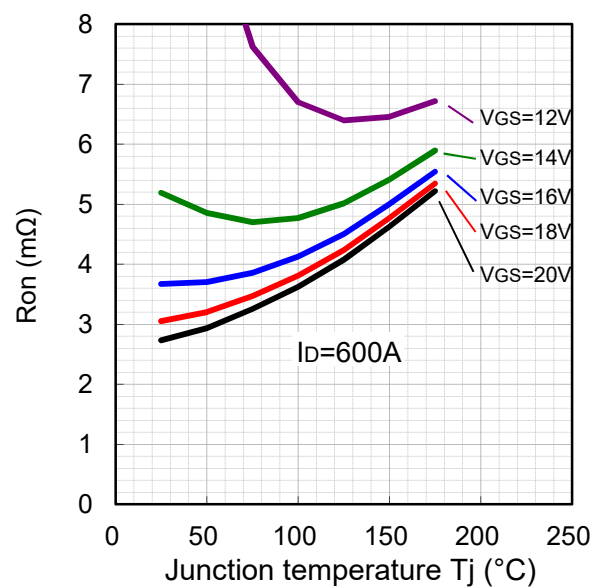


Fig.3 Drain source voltage characteristic 25°C (TYP)

Fig.4 R_{on} vs T_J characteristic (TYP)

●Electrical characteristic curves (Typical)

Fig.5 Forward characteristic of Diode (TYP)

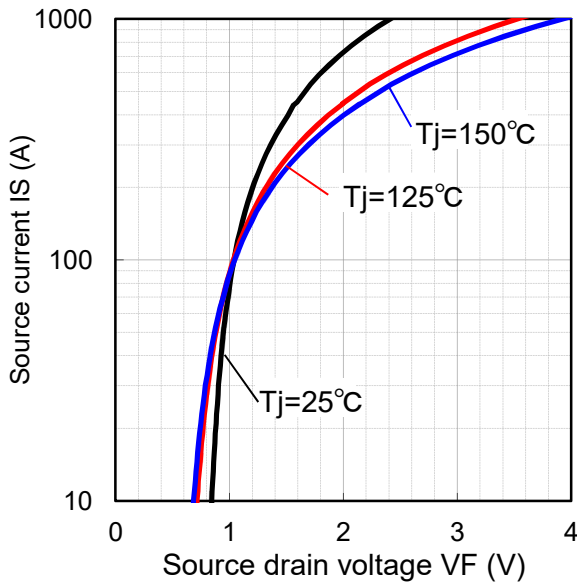


Fig.6 Forward characteristic of Diode (TYP)

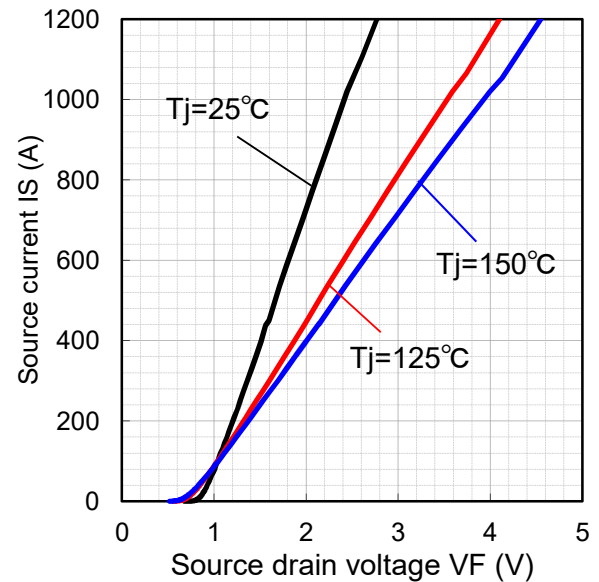


Fig.7 Drain Current vs Gate Voltage (TYP)

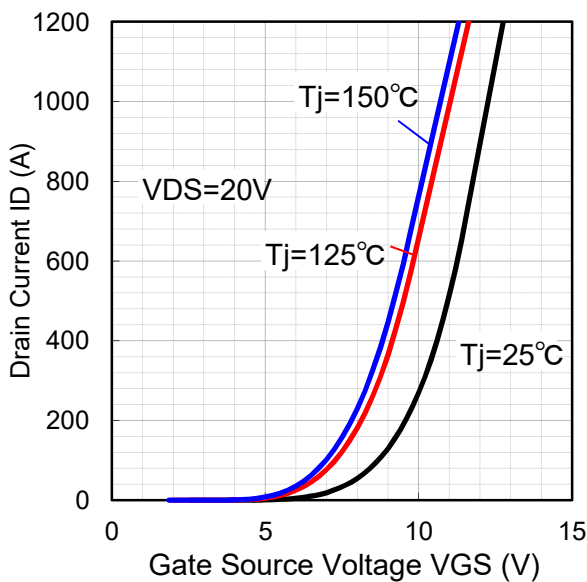
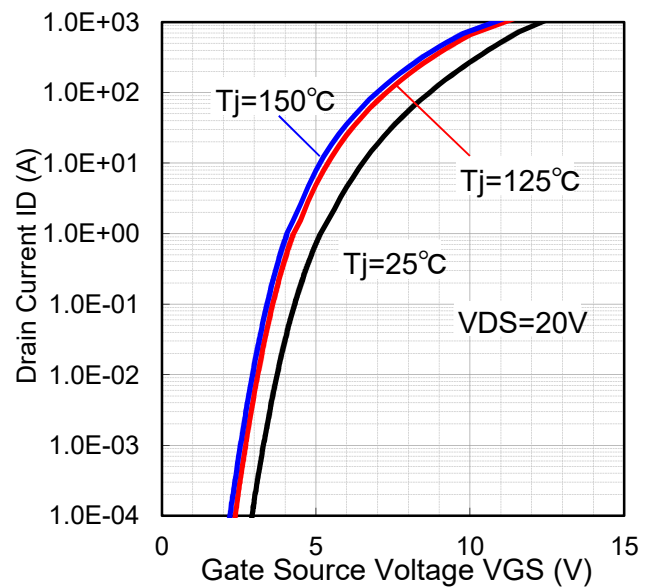


Fig.8 Drain Current vs Gate Voltage (TYP)



●Electrical characteristic curves (Typical)

Fig.9 Switching time vs drain current at 25°C (TYP)

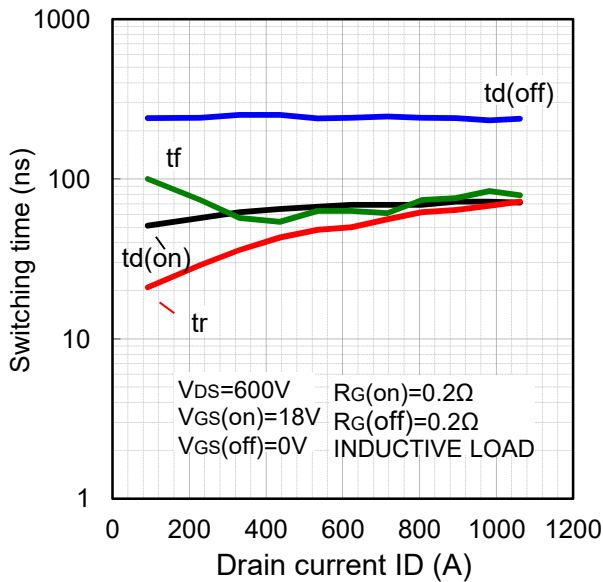


Fig.10 Switching time vs drain current at 125°C (TYP)

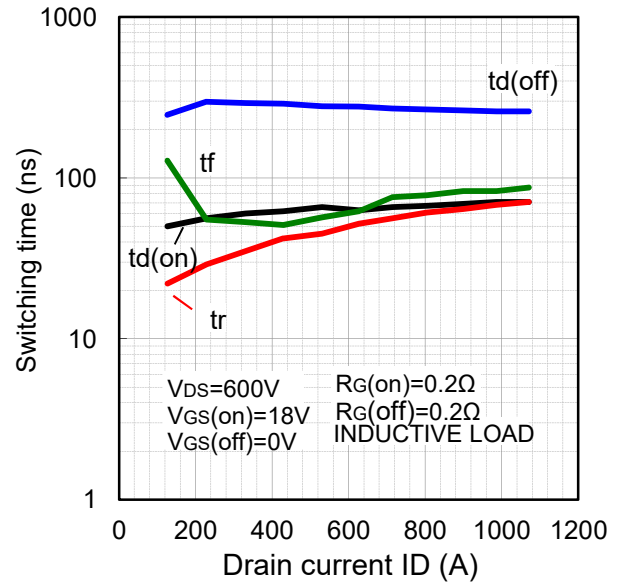


Fig.11 Switching time vs drain current at 150°C (TYP)

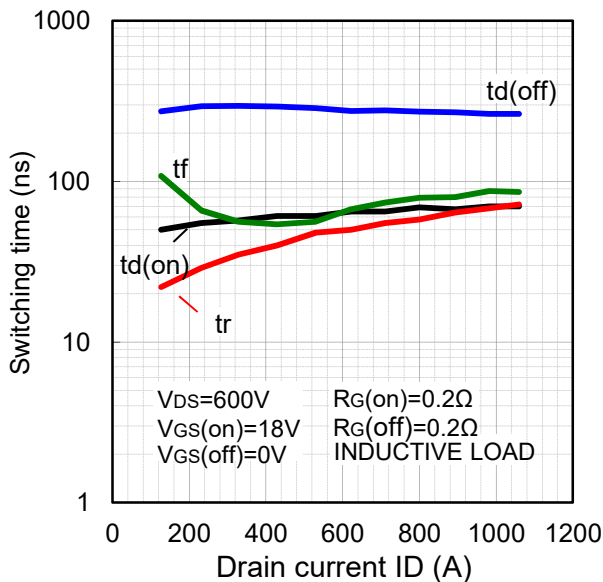
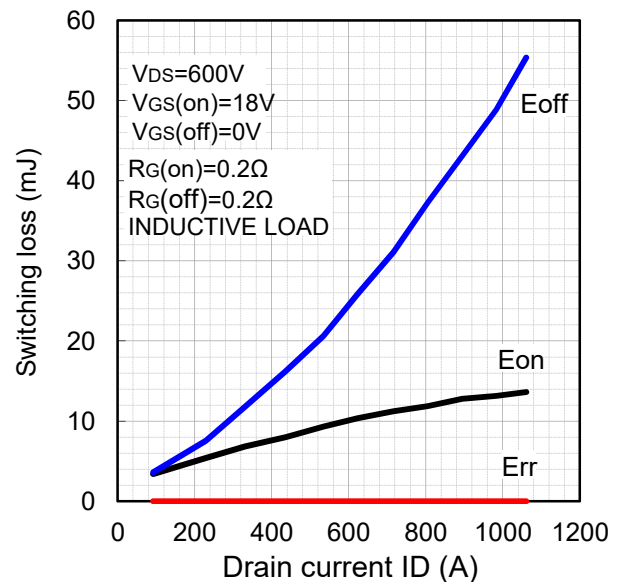


Fig.12 Switching loss vs drain current at 25°C (TYP)



●Electrical characteristic curves (Typical)

Fig.13 Switching loss vs drain current at 125°C (TYP)

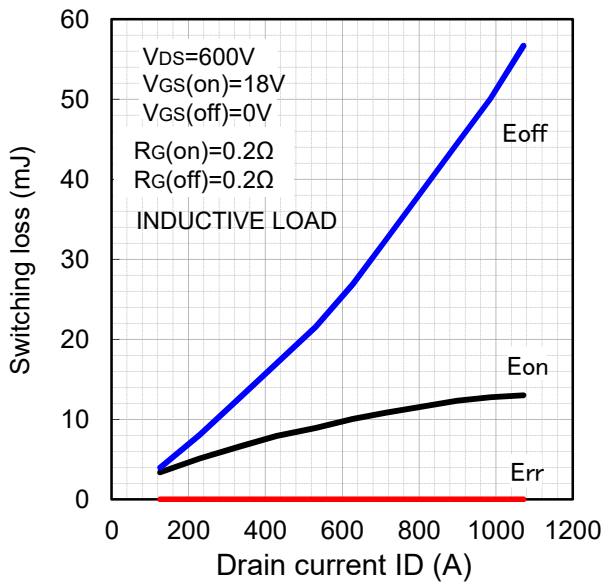


Fig.14 Switching loss vs drain current at 150°C (TYP)

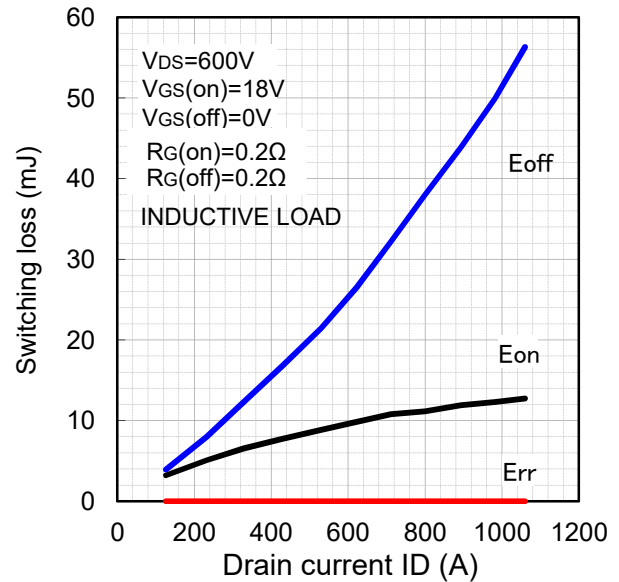


Fig.15 Recovery characteristic vs drain current at 25°C (TYP)

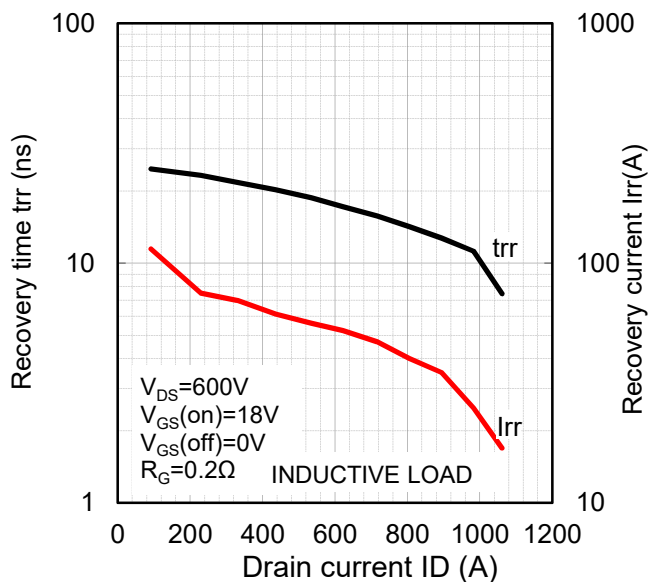
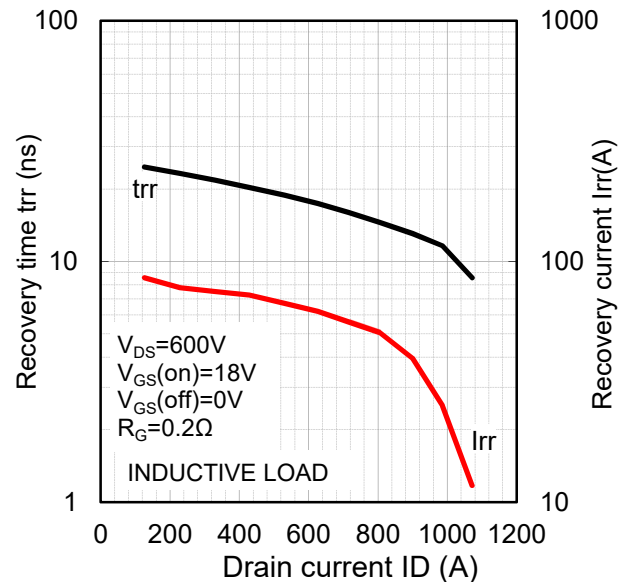


Fig.16 Recovery characteristic vs drain current at 125°C (TYP)



●Electrical characteristic curves (Typical)

Fig.17 Recovery characteristic vs drain current at 150°C (TYP)

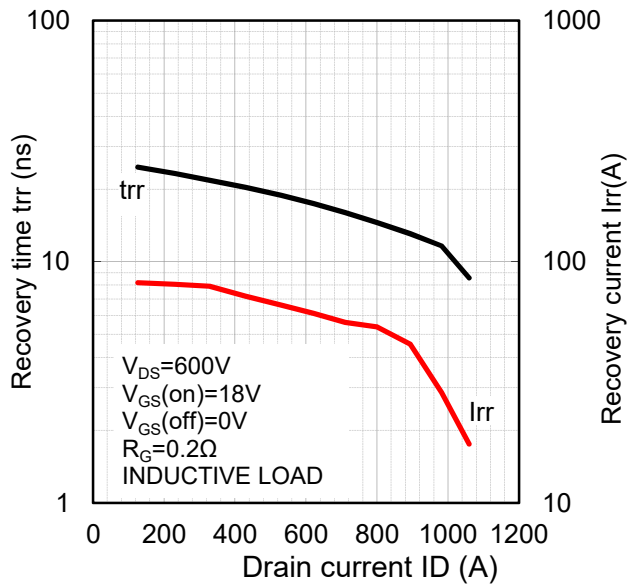


Fig.18 Switching time vs gate resistance at 25°C (TYP)

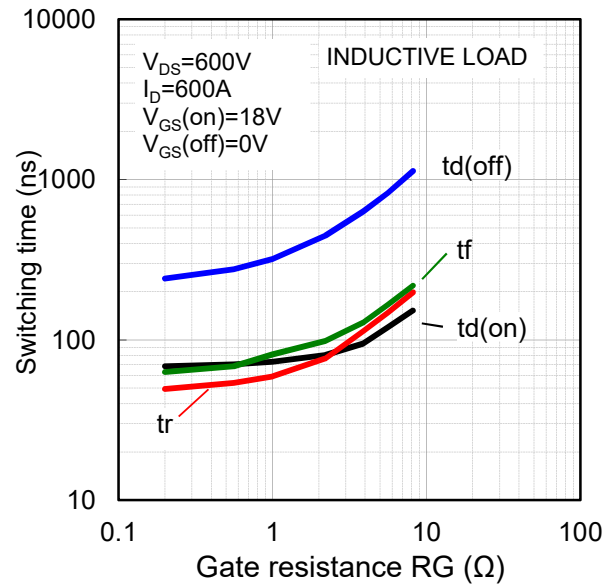


Fig.19 Switching time vs gate resistance at 125°C (TYP)

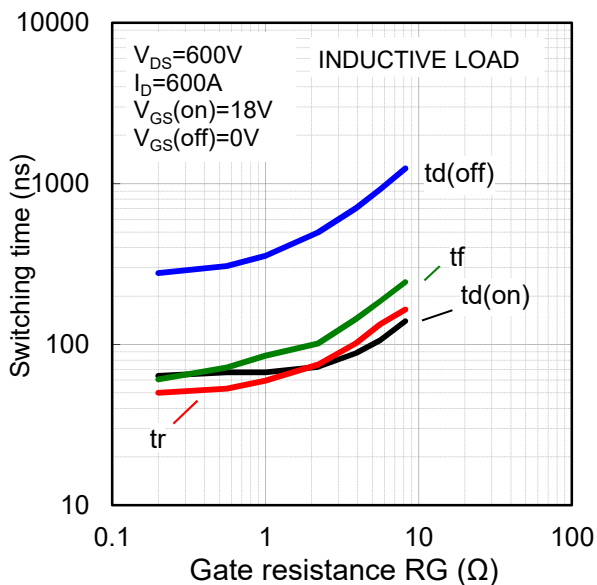
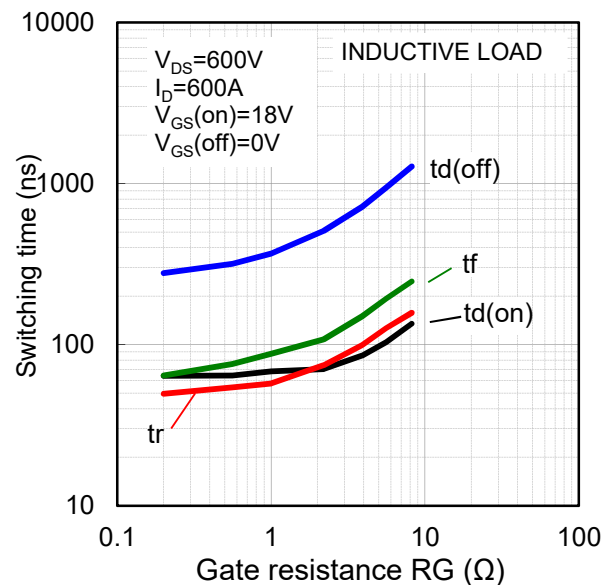


Fig.20 Switching time vs gate resistance at 150°C (TYP)



●Electrical characteristic curves (Typical)

Fig.21 Switching loss vs gate resistance at 25°C (TYP)

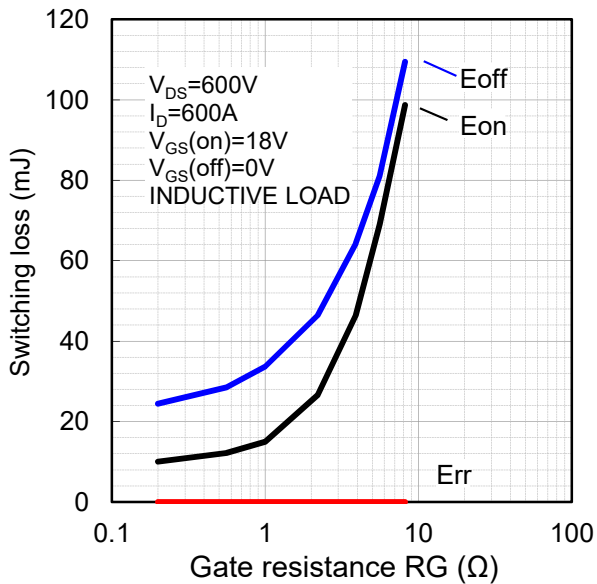


Fig.22 Switching loss vs gate resistance at 125°C (TYP)

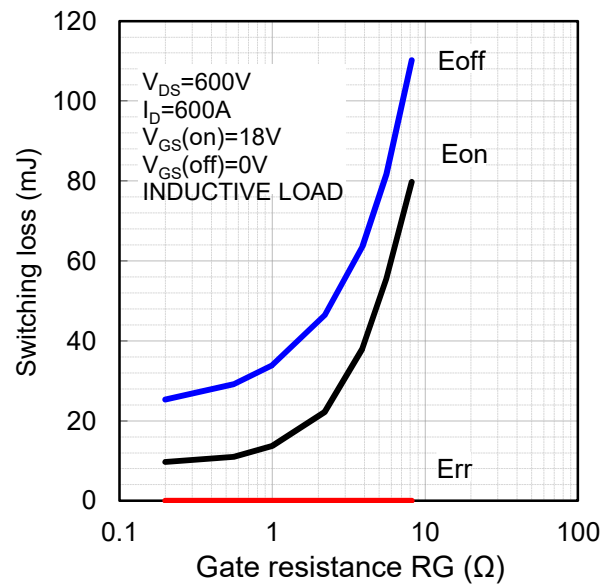
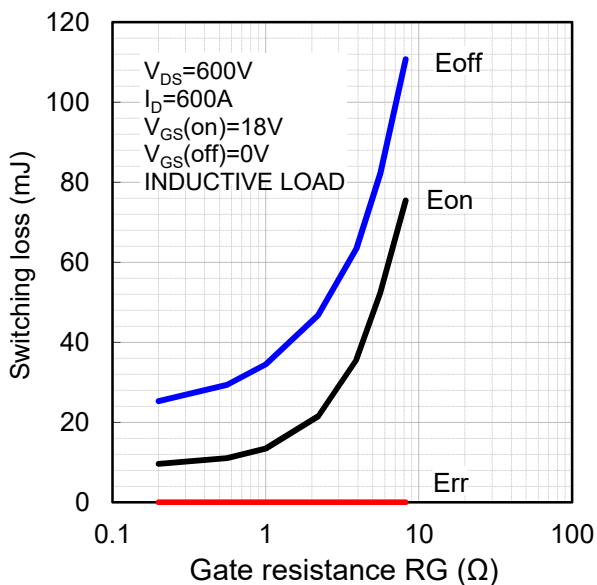


Fig.23 Switching loss vs gate resistance at 150°C (TYP)



●Electrical characteristic curves (Typical)

Fig.24 Capacitance vs Drain source voltage (TYP)

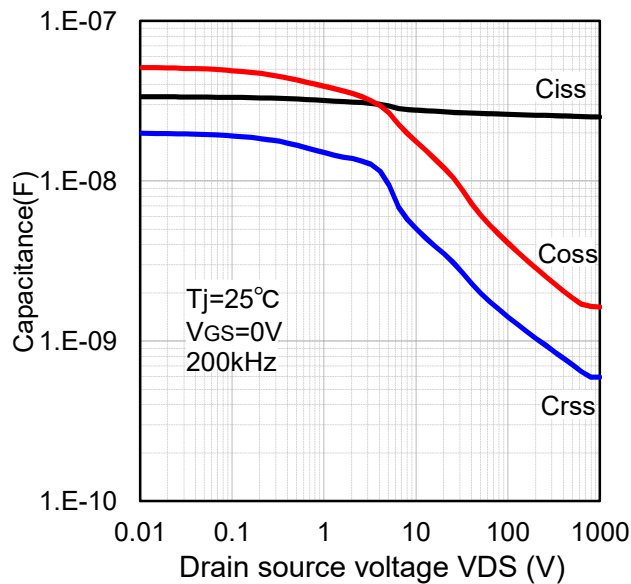


Fig.25 Gate charge characteristic (TYP)

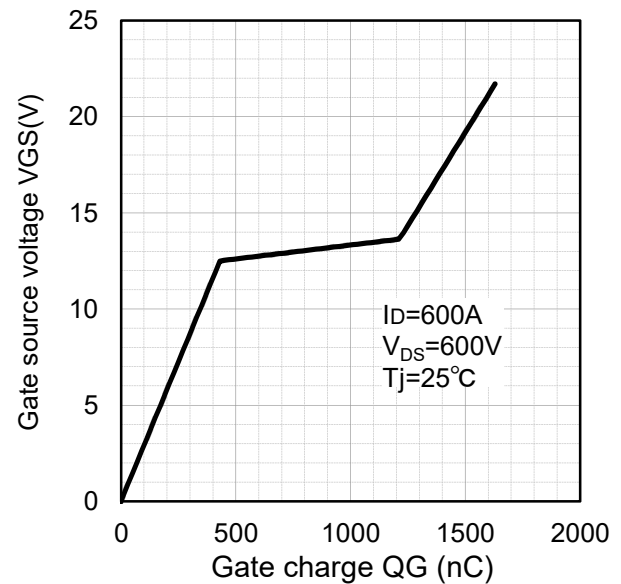
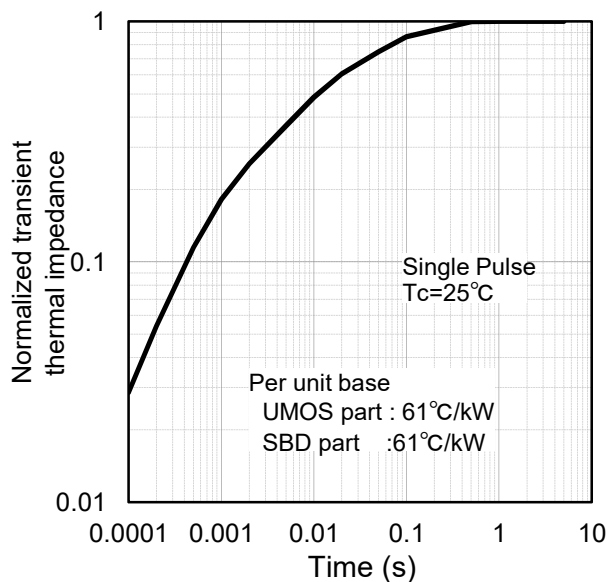


Fig.26 Transient thermal impedance (TYP)



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