

SPECIFICATION

1. SCOPE

This specification covers the characteristics of the "SM" series size 2225, BX dielectric, 3.3 uF capacitance, +/- 10% tolerance, 50 VDC rated ceramic chip capacitors with surface mount "J" shaped leads.

2. DIELECTRIC CHARACTERISTICS - X7R

Operating Temperature Range: -55°C to +125°C

Temperature Coefficient: +/- 15% Change in capacitance @ 1 Vrms
Temperature Voltage Coefficient: +15% -25% Change in capacitance @ 1 Vrms

Dissipation Factor: 2.5% Max. @ 25°C

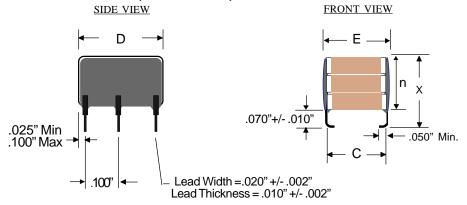
Insulation Resistance, 25°C: 100 Giga-Ohms or 1000 Ohm-Farads, whichever is less Insulation Resistance, 125°C: 10 Giga-Ohms or 100 Ohm-Farads, whichever is less

Dielectric Withstanding Voltage: 125 VDC

Aging Rate: < 2.0% per decade

Test Parameters: 1KHz, 1.0 Volts +/- 0.2 Vrms, 25°C

3. PHYSICAL DIMENSIONS (NOT SCALED)



dimensions	Е	D	Х	С	Number of Leads per Side	n= Number of Units Stacked
inches (mm)	.300 (7.62)	.250 (6.35)	.440 (11.2)	.250 (6.35)	3	3
+/- in (mm)	Max	.025 (.635)	Max	.025 (.635)	n/a	n/a

4. PART NUMBER DESCRIPTION

SM 2225 X 335 K 500 LJ X H - 3

SM = Stacked Capacitor Assembly (High Reliability Testing per Group A of Mil-Prf-49470)

2225 = Size

X = BX Dielectric

335 = Capacitance Code (pF): First two digits are significant, third digit denotes number of zeros

K = Capacitance tolerance: K = +/- 10%

500 = DC Voltage Rating: First two digits are significant, third digit denotes number of zeros

LJ = Surface Mount Leads, "J" Shape

X = Overall Height: .440" Max. (11.2mm Max.)

H = High Reliability Testing

- 3 = Quantity of capacitors stacked in the assembly

REVISION: A

NOVACAP P/N: SM2225X335K500LJXH-3	SPEC DESIGNER MK 2/26/2016
CUSTOMER:	SPEC MANAGER BN 2/26/2016
CUST. P/N:	ENGINEERING BN 2/26/2016