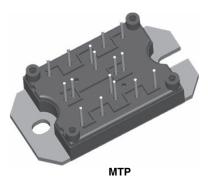
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Vishay Semiconductors

# "Full Bridge" IGBT MTP (Ultrafast NPT IGBT), 40 A



 PRODUCT SUMMARY

  $V_{CES}$  1200 V

  $I_C$  at  $T_C$  = 25 °C
 40 A

  $V_{CE(on)}$  3.29 V

 Speed
 8 kHz to 30 kHz

 Package
 MTP

 Circuit
 Full bridge

#### FEATURES

- Ultrafast Non Punch Through (NPT) technology
- Positive V<sub>CE(on)</sub> temperature coefficient
- 10 µs short circuit capability
- HEXFRED<sup>®</sup> antiparallel diodes with ultrasoft reverse recovery
- Low diode V<sub>F</sub>
- Square RBSOA
- Aluminum nitride DBC
- Very low stray inductance design for high speed operation
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Optimized for welding, UPS and SMPS applications
- Rugged with ultrafast performance
- Outstanding ZVS and hard switching operation
- Low EMI, requires less snubbing
- Excellent current sharing in parallel operation
- Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter breakdown voltage	V <sub>CES</sub>		1200	V		
Continuous collector current		$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	40			
Continuous collector current	I <sub>C</sub>	T <sub>C</sub> = 106 °C	20			
Pulsed collector current	I <sub>CM</sub>		100	•		
Clamped inductive load current	I <sub>LM</sub>		100	A		
Diode continuous forward current	١ <sub>F</sub>	T <sub>C</sub> = 106 °C	25			
Diode maximum forward current	I <sub>FM</sub>		100			
Gate to emitter voltage	V <sub>GE</sub>		± 20	N		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
	D	T <sub>C</sub> = 25 °C	240	)0/		
Maximum power dissipation (only IGBT)	P <sub>D</sub>	T <sub>C</sub> = 100 °C	96	W		

y Pb-free RoHS

COMPLIANT

1



PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 μA	1200	-	-	V	
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES} / \Delta T_J$	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 3 mA (25 °C to 125 °C)	-	+ 1.3	-	V/°C	
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 20 A	-	3.29	3.59		
	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A	-	4.42	4.66	- V	
Collector to emitter saturation voltage		$V_{GE}$ = 15 V, I <sub>C</sub> = 20 A, T <sub>J</sub> = 125 °C	-	3.87	4.11		
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 40 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	5.32	5.70		
		$V_{GE}$ = 15 V, I <sub>C</sub> = 20 A, T <sub>J</sub> = 150 °C	-	3.99	4.27		
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 250 \ \mu A$	4	-	6		
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	$V_{CE} = V_{GE}$ , $I_C = 3$ mA (25 °C to 125 °C)	-	- 14	-	mV/°C	
Transconductance	g <sub>fe</sub>	$V_{CE} = 50$ V, $I_C = 20$ A, PW = 80 $\mu$ s	-	17.5	-	S	
		$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 1200 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	-	250	μA	
Zero gate voltage collector current	I <sub>CES</sub> <sup>(1)</sup>	$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 1200 \text{ V}, \text{ T}_{\text{J}} = 125 ^{\circ}\text{C}$	-	0.7	3.0		
		$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 1200 \text{ V}, \text{ T}_{\text{J}} = 150 ^{\circ}\text{C}$	-	2.9	9.0	— mA	
Gate to emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ± 20 V	-	-	± 250	nA	

#### Note

 $^{(1)}~~I_{CES}$  includes also opposite leg overall leakage

SWITCHING CHARACTERISTICS ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Total gate charge (turn-on)	Qg	I <sub>C</sub> = 20 A	-	176	264	nC	
Gate to emitter charge (turn-on)	Q <sub>ge</sub>	$V_{CC} = 600 V$	-	19	30		
Gate to collector charge (turn-on)	Q <sub>gc</sub>	V <sub>GE</sub> = 15 V	-	89	134		
Turn-on switching loss	E <sub>on</sub>	$V_{CC} = 600 \text{ V}, I_C = 20 \text{ A}, V_{GE} = 15 \text{ V},$	-	0.92	-	- mJ	
Turn-off switching loss	E <sub>off</sub>	$R_g = 5 \Omega$ , L = 1 mH, $T_J = 25 °C$ , energy losses include tail and	-	0.46	-		
Total switching loss	E <sub>tot</sub>	diode reverse recovery	-	1.38	-		
Turn-on switching loss	E <sub>on</sub>	$V_{CC} = 600 \text{ V}, I_C = 20 \text{ A}, V_{GE} = 15 \text{ V},$	-	1.29	-		
Turn-off switching loss	E <sub>off</sub>	$R_g = 5 \Omega$ , L = 1 mH, $T_J = 125 °C$ , energy losses include tail and	-	0.81	-		
Total switching loss	E <sub>tot</sub>	diode reverse recovery	-	2.1	-		
Input capacitance	C <sub>ies</sub>	V <sub>GE</sub> = 0 V	-	2530	3790		
Output capacitance	C <sub>oes</sub>	$V_{CC} = 30 V$	-	344	516	pF	
Reverse transfer capacitance	C <sub>res</sub>	f = 1.0 MHz	-	78	117		
Reverse bias safe operating area	RBSOA		Fullsquare				
Short circuit safe operating area	SCSOA		10	-	-	μs	

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<b>DIODE SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
	V <sub>FM</sub>	I <sub>C</sub> = 20 A	-	2.48	2.94	v
Diode forward voltage drop		I <sub>C</sub> = 40 A	-	3.28	3.90	
		I <sub>C</sub> = 20 A, T <sub>J</sub> = 125 °C	-	2.44	2.84	
		I <sub>C</sub> = 40 A, T <sub>J</sub> = 125 °C	-	3.45	4.14	
		I <sub>C</sub> = 20 A, T <sub>J</sub> = 150 °C	-	2.21	2.93	
Reverse recovery energy of the diode	E <sub>rec</sub>	$V_{GE} = 15 \text{ V}, \text{ R}_{g} = 5 \Omega, \text{ L} = 200 \mu\text{H}$	-	420	630	μJ
Diode reverse recovery time	t <sub>rr</sub>	V <sub>CC</sub> = 600 V, I <sub>C</sub> = 20 A	-	98	150	ns
Peak reverse recovery current	l <sub>rr</sub>	T <sub>J</sub> = 125 °C	-	33	50	А

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range Storage temperature range		TJ		-40	-	150	°C
		T <sub>Stg</sub>		-40	-	125	
Junction to case	IGBT	P		-	0.35	0.52	
Junction to case	Diode	R <sub>thJC</sub>		-	0.40	0.61	°C/W
Case to sink per module		R <sub>thCS</sub>	Heatsink compound thermal conductivity = 1 W/mK	-	0.06	-	
Clearance			External shortest distance in air between 2 terminals	5.5	-	-	
Creepage			Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm
Mounting torque			A mounting compound is recommended and the torque should be checked after 3 hours to allow for the spread of the compound. Lubricated threads.	3 ± 10 %		Nm	
Weight				66		g	





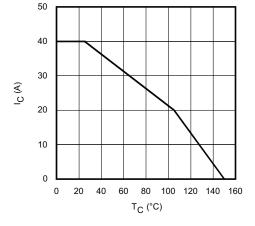


Fig. 1 - Maximum DC Collector Current vs. Case Temperature

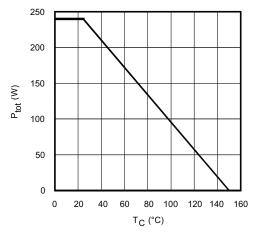


Fig. 2 - Power Dissipation vs. Case Temperature

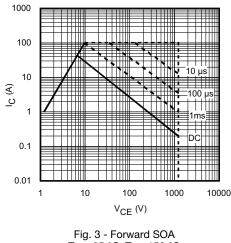


Fig. 3 - Forward SOA  $T_C = 25 \text{ °C}; T_J \le 150 \text{ °C}$ 

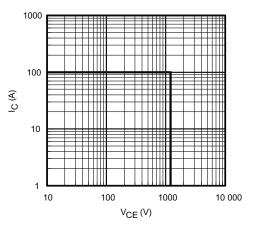


Fig. 4 - Reverse Bias SOA  $T_J$  = 150 °C;  $V_{GE}$  = 15 V

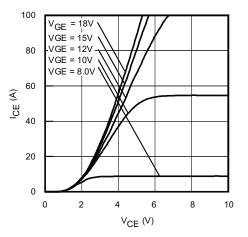


Fig. 5 - Typical IGBT Output Characteristics  $T_J$  = - 40 °C;  $t_p$  = 80  $\mu s$ 

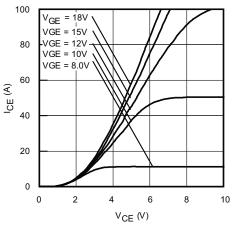
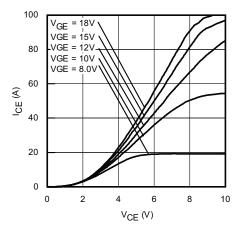


Fig. 6 - Typical IGBT Output Characteristics  $T_J$  = 25 °C;  $t_p$  = 80 µs

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Fig. 7 - Typical IGBT Output Characteristics  $T_J$  = 125 °C;  $t_p$  = 80  $\mu s$ 

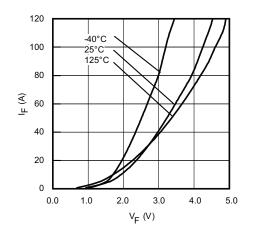
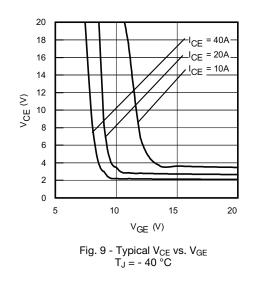


Fig. 8 - Typical Diode Forward Characteristics  $t_p$  = 80  $\mu s$ 



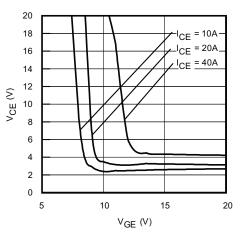


Fig. 10 - Typical V<sub>CE</sub> vs. V<sub>GE</sub>  $T_J$  = 25 °C

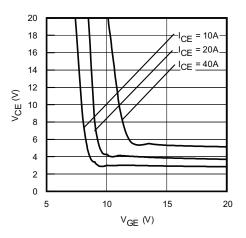


Fig. 11 - Typical V<sub>CE</sub> vs. V<sub>GE</sub>  $T_J$  = 125 °C

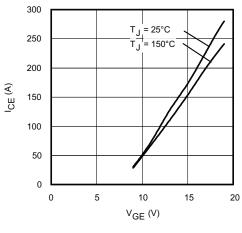


Fig. 12 - Typical Transfer Characteristics  $V_{CE}$  = 50 V;  $t_p$  = 10  $\mu s$ 

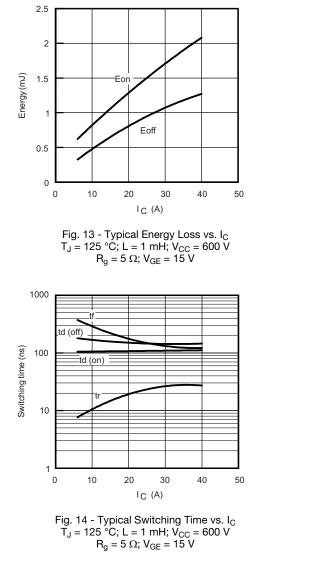
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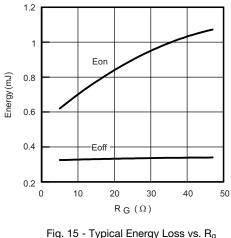


Fig. 15 - Typical Energy Loss vs.  $R_g$   $T_J$  = 125 °C; L = 1 mH;  $V_{CC}$  = 600 V  $I_{CE}$  = 6 A;  $V_{GE}$  = 15 V

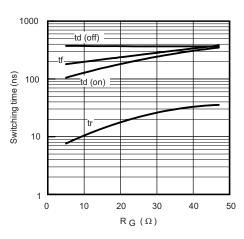


Fig. 16 - Typical Switching Time vs.  $R_g$   $T_J$  = 150 °C; L = 1 mH;  $V_{CC}$  = 600 V  $I_{CE}$  = 6 A;  $V_{GE}$  = 15 V

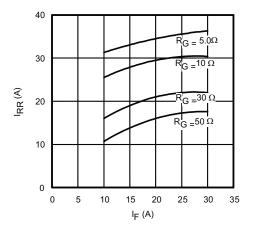


Fig. 17 - Typical Diode I<sub>rr</sub> vs. I<sub>F</sub>  $T_J$  = 150 °C

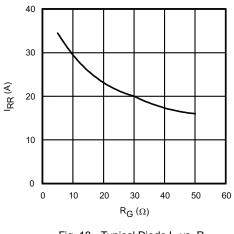
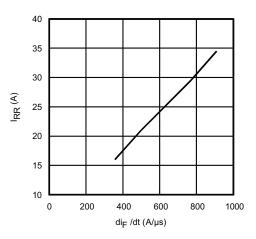


Fig. 18 - Typical Diode I\_{rr} vs. R\_g T\_J = 150 \ ^{\circ}C; I\_F = 5.0 \ A

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Fig. 19 - Typical Diode I $_{rr}$  vs. dI $_{F}/dt$  V\_{CC} = 400 V; V\_{GE} = 15 V; I $_{CE}$  = 5.0 A; T\_J = 150  $^{\circ}C$ 

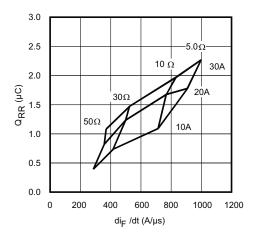


Fig. 20 - Typical Diode  $Q_{rr}$  vs. dI\_F/dt  $V_{CC}$  = 400 V;  $V_{GE}$  = 15 V; T\_J = 150  $^\circ C$ 

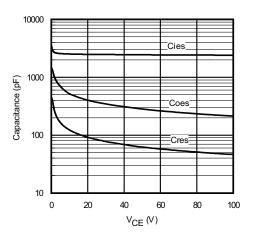


Fig. 21 - Typical Capacitance vs.  $V_{CE}$   $V_{GE}$  = 0 V; f = 1 MHz

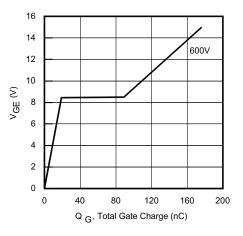


Fig. 22 - Typical Gate Charge vs. V<sub>GE</sub>  $I_{CE}$  = 5.0 A; L = 600  $\mu$ H

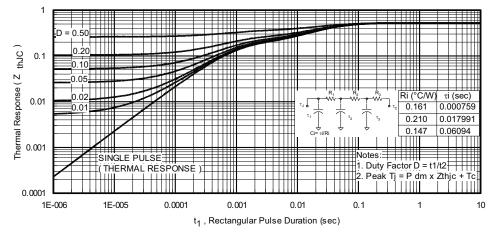


Fig. 23 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

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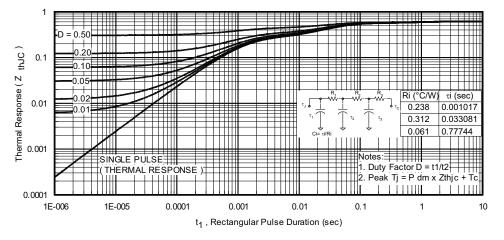
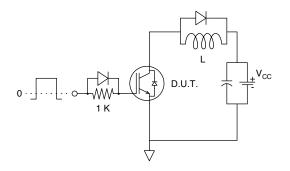


Fig. 24 - Maximum Transient Thermal Impedance, Junction to Case (Diode)



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Fig. 25 - Gate Charge Circuit (Turn-Off)

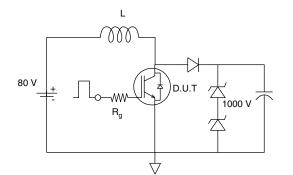


Fig. 26 - RBSOA Circuit

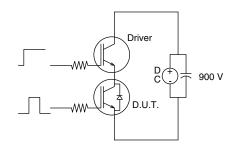


Fig. 27 - S.C. SOA Circuit

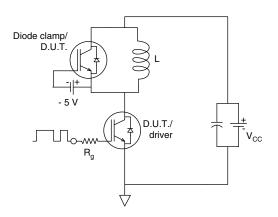


Fig. 28 - Switching Loss Circuit



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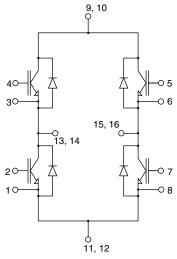
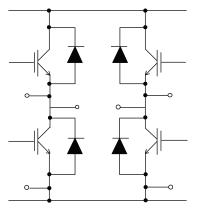


Fig. 29 - Electrical diagram

#### **ORDERING INFORMATION TABLE**

**Device code** VS-20 MT 120 U F Ρ (3)1 2 (4) (5) 6 7 1 Vishay Semiconductors product 2 Current rating (20 = 20 A) 3 Essential part number 4 Voltage code (120 = 1200 V) 5 Speed/type (U = Ultrafast IGBT) 6 Circuit configuration (F = Full bridge) P = Lead (Pb)-free

#### **CIRCUIT CONFIGURATION**

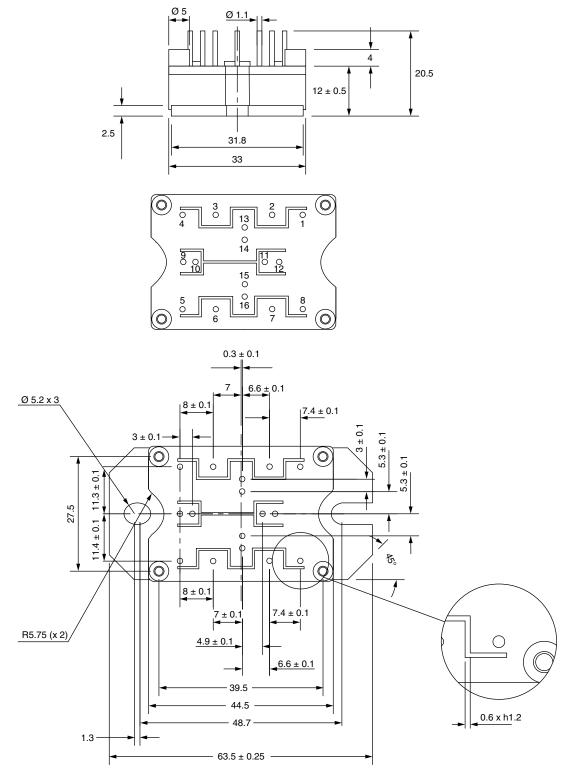


LINKS TO RELATED DOCUMENTS						
Dimensions	www.	vishay.com/doc?95245				
Revision: 10-Jun-15	9	Document Number: 94505				
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# VISHAY.

# MTP MOSFET/IGBT Full-Bridge

**DIMENSIONS** in millimeters





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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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