

Features

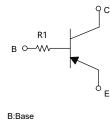
- Built-In Bias Resistors Enable the Configuration of an Inverter Circuit Without Connecting External Input Resistors
- The Bias Resistors Consist of Thin-Film Resistors With Complete Isolation to Allow Negative Biasing of the Input. They Also Have the Advantage of Almost Completely Eliminating Parasitic Effects
- Only the On/Off Conditions Need to Be Set For Operation, Making Device Design Easy
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

Maximum Ratings @ 25°C Unless Otherwise Noted

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	-50	V
Collector-Base Voltage	V _{CBO}	-50	V
Emitter-Base Voltage	V_{EBO}	-5	mA
Collector Current-Continuous	Ι _C	-100	mA
Collector Dissipation	P _C	200	mW
Junction Temperature Range	TJ	-55 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

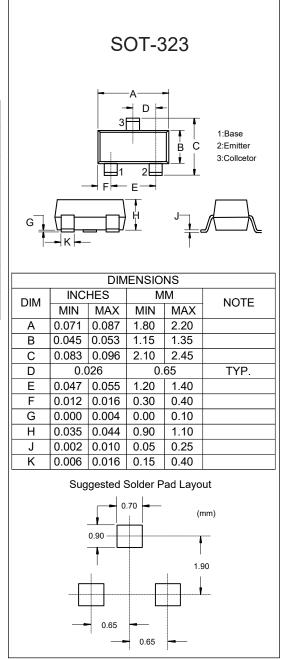
Device Marking: 94

Internal Structure



C:Collcetor E:Emitter





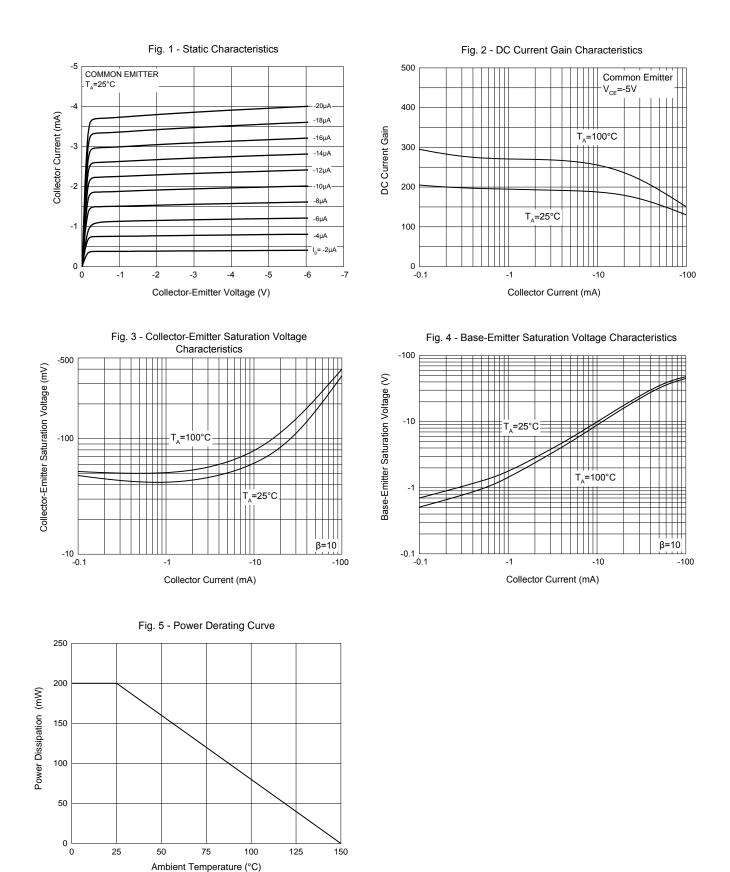


Electrical Characteristics @ 25°C Unless Otherwise Specified

Parameter	Symbol	Min	Тур	Max	Units	Conditions
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-50			V	Ι _C =-50μΑ, Ι _E =0
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-50			V	I _C =-1mA, I _B =0
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5			V	Ι _E =-50μΑ, Ι _C =0
Collector Cut-off Current	I _{CBO}			-0.5	μA	V _{CB} =-50V,I _E =0
Emitter Cut-off Current	I _{EBO}			-0.5	μA	V_{EB} =-4V,I _C =0
DC Current Gain	h _{FE}	100	250	600		I _C =-1mA, V _{CE} =-5V
Collector-Emitter Saturation Voltage	V _{CE(sat)}			-0.3	V	I _C =-10mA, I _B =-1mA
Input Resistance	R ₁	7	10	13	KΩ	
Transition Frequency	f _T		250		MHz	V _{CE} =-10.0V, I _E =5mA, f=100MHz



Curve Characteristics





Ordering Information

Device	Packing		
Part Number-TP	Tape&Reel:3Kpcs/Reel		

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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