

## Evaluating the **ADM3066E** 3.0 V to 5.5 V, $\pm 12$ kV IEC ESD Protected, 50 Mbps RS-485 Transceiver

### FEATURES

- Easy evaluation of **ADM3066E** 50 Mbps RS-485 transceiver
- Board layouts for standard half-duplex RS-485 footprints
  - 10-lead MSOP, **ADM3066EBRMZ** (EVAL-ADM3066EEBZ)
  - 10-lead LFCSP, **ADM3066EBCPZ** (EVAL-ADM3066EEB1Z)
- Power/ground connections through screw terminal block
  - 3.0 V to 5.5 V operating voltage range on  $V_{CC}$
  - 1.62 V to 5.5 V  $V_{IO}$  logic supply
- IEC ESD protection on the RS-485 A and B bus pins
  - $\pm 12$  kV contact discharge and  $\pm 12$  kV air gap discharge
- Screw terminal blocks for logic input/output and RS-485 signals
- Jumper-selectable enable/disable for RE and DE
- Test points for measuring all signals
- Resistors and footprints for termination and biasing networks

### EVALUATION KIT CONTENTS

EVAL-ADM3066EEBZ or EVAL-ADM3066EEB1Z

### ADDITIONAL EQUIPMENT NEEDED

- Oscilloscope
- Signal generator or universal asynchronous receiver/transmitter (UART)
- 3 V to 5 V supply

### GENERAL DESCRIPTION

The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z allow quick and easy evaluation of the **ADM3066E** 50 Mbps RS-485 transceiver with standard MSOP and LFCSP footprints, respectively. The evaluation boards allow the input and output functions to be exercised without external components. Screw terminal blocks provide convenient connections for power and ground, digital input and output, and RS-485 signals. The evaluation boards can be powered by a standard configurable bench power supply within a 3.0 V to 5.5 V range.

The EVAL-ADM3066EEBZ evaluation board has a footprint for the **ADM3066EBRMZ** half-duplex RS-485 transceiver in a 10-lead MSOP package. The EVAL-ADM3066EEB1Z evaluation board has a footprint for a half-duplex RS-485 transceiver in a 10-lead LFCSP package.

Complete specifications for the **ADM3066E** are provided in the **ADM3066E** data sheet and must be consulted in conjunction with this user guide when using the evaluation board.

### EVALUATION BOARD PHOTOGRAPHS



Figure 1. EVAL-ADM3066EEBZ Evaluation Board



Figure 2. EVAL-ADM3066EEB1Z Evaluation Board

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REVISION HISTORY

5/2017—Revision 0: Initial Version

## EVALUATION BOARD HARDWARE

### SETTING UP THE EVALUATION BOARD

The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z evaluation boards are powered by connecting a 3.0 V or 5.5 V power supply to the J1 screw terminals for  $V_{CC}$  and GND at the top of the evaluation board. A 10  $\mu$ F decoupling capacitor, C3, is fitted at the connector between  $V_{CC}$  and GND. The  $V_{CC}$  pin of the RS-485 transceiver is fitted with a 100 nF decoupling capacitor, C1, with a second footprint for an optional additional capacitor, C2.  $V_{IO}$  can be connected with  $V_{CC}$  or a separate 1.8 V power supply.

Corresponding labeled test points allow monitoring of the power supply to the evaluation board and the probe reference to ground.

### INPUT AND OUTPUT CONNECTIONS

Digital input and output signals are connected via the J3 screw terminal block, allowing wire connections from the evaluation boards to a signal generator or UART. The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z include connections for data input (DI), receiver output (RO), receiver enable ( $\overline{RE}$ ), and driver enable (DE). Alternatively, jumper connections can drive these inputs and/or connect them to  $V_{CC}$  and GND (see Table 1). The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z evaluation boards also have an SMA right angle jack (DI), which is the preferred option for delivering a high speed 50 Mbps signal to the evaluation boards.

Connections to an RS-485 bus are made via a screw terminal block, J5. For the EVAL-ADM3066EEBZ or EVAL-ADM3066EEB1Z half-duplex boards, there are two bus input/output signals, A

and B, for noninverting and inverting signals, respectively. The bus cable can also include a common ground connection or shield and can also be connected to the J5 screw terminal block of the evaluation boards. Test points are available on the evaluation boards and are appropriately labeled for all digital and bus input/output signals.

### OTHER BOARD COMPONENTS

The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z evaluation boards include footprints for termination resistors, RT1 and RT2, as well as pull-up and pull-down resistors, R1 and R2. Termination resistors of 120  $\Omega$  are fitted to the evaluation board; these resistors can be removed or replaced with a different value resistor as needed. Inserting both LK3 and LK4 adds a 60  $\Omega$  load to the RS-485 driver.

#### *Biasing Resistors for Bus Idle Fail-Safe*

Although the [ADM3066E](#) has a built-in receiver fail-safe for the bus idle condition, there are footprints on the evaluation boards for fitting the R2 pull-up resistor to  $V_{CC}$  on A, as well as the R1 pull-down resistor to GND on B. These resistors can be fitted if the user is connecting to other devices that require such external biasing resistors on the bus. The exact value required for a 200 mV minimum differential voltage in the bus idle condition depends on the supply voltage (for example, 960  $\Omega$  for 3.3 V and 1440  $\Omega$  for 5 V).

For more information about the bus idle fail-safe, see the [AN-960 Application Note, RS-485/RS-422 Circuit Implementation Guide](#).

**Table 1. Jumper Configurations**

| Link | Connection   | Description                                                                                                                                                                                                                                                                                                         |
|------|--------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| LK1  | A            | Connects the receiver enable ( $\overline{RE}$ ) input of the <a href="#">ADM3066E</a> to $V_{CC}$ . This setting disables the receiver.                                                                                                                                                                            |
|      | B            | Connects the $\overline{RE}$ input of the <a href="#">ADM3066E</a> to GND. This setting enables the receiver.                                                                                                                                                                                                       |
|      | C            | Connects the $\overline{RE}$ input of the <a href="#">ADM3066E</a> to the J3-2 terminal block connector.                                                                                                                                                                                                            |
|      | D            | Connects the $\overline{RE}$ input of the <a href="#">ADM3066E</a> to the J3-3 terminal block connector; that is, the input for both $\overline{RE}$ and DE is set by LK1. This setting ensures that when the driver is enabled, the receiver is disabled, or when the driver is disabled, the receiver is enabled. |
| LK2  | A            | Connects the DE input of the <a href="#">ADM3066E</a> to $V_{CC}$ . This setting enables the driver.                                                                                                                                                                                                                |
|      | B            | Connects the DE input of the <a href="#">ADM3066E</a> to GND. This setting disables the driver.                                                                                                                                                                                                                     |
|      | C            | Connects the DE input of the <a href="#">ADM3066E</a> to the J3-2 terminal block connector.                                                                                                                                                                                                                         |
| LK3  | Inserted     | Connects the 120 $\Omega$ RT1 termination resistor across the RS-485 A and B pins.                                                                                                                                                                                                                                  |
|      | Not inserted | Disconnects the 120 $\Omega$ RT1 termination resistor across the RS-485 A and B pins.                                                                                                                                                                                                                               |
| LK4  | Inserted     | Connects the 120 $\Omega$ RT2 termination resistor across the RS-485 A and B pins.                                                                                                                                                                                                                                  |
|      | Not inserted | Disconnects the 120 $\Omega$ RT2 termination resistor across the RS-485 A and B pins.                                                                                                                                                                                                                               |
| LK6  | Inserted     | Connects $V_{CC}$ with $V_{IO}$ .                                                                                                                                                                                                                                                                                   |
|      | Not inserted | Disconnects $V_{CC}$ with $V_{IO}$ .                                                                                                                                                                                                                                                                                |

## HALF-DUPLEX RS-485 TRANSCEIVERS POINT TO POINT TEST

A point to point test can be set up with either two EVAL-ADM3066EEBZ evaluation boards or two EVAL-ADM3066EEB1Z evaluation boards. Figure 4 shows two half-duplex evaluation boards in this configuration. Note the positions of LK1 and LK2 on each evaluation board that enable the driver on one evaluation board and the receiver on the other evaluation board. Remove LK4 from the EVAL-ADM3066EEBZ or EVAL-ADM3066EEB1Z evaluation board to ensure both ends of the bus have only a 120  $\Omega$  load. Differential signals on the bus are monitored with an oscilloscope, as well as the DI pin and RO pin of the [ADM3066E](#).

## IEC 61000-4-2 ESD PROTECTION TO $\pm 12$ kV (CONTACT) AND $\pm 12$ kV (AIR GAP)

The EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z evaluation boards are tested to achieve protection against IEC 61000-4-2 ESD to  $\pm 12$  kV (contact), and  $\pm 12$  kV (air gap) on the RS-485 A and B bus pins.

IEC 61000-4-2 ESD standard describes testing using two coupling methods known as contact discharge and air gap discharge. Contact discharge implies a direct contact between the discharge gun and the equipment under test (EUT).

During air discharge testing, the charged electrode of the discharge gun is moved toward the EUT until a discharge occurs as an arc across the air gap. The discharge gun does not make direct contact with the EUT.

During testing, the EVAL-ADM3066EEBZ and EVAL-ADM3066EEB1Z A and B pins are subjected to at least 10 positive and 10 negative single discharges with a 1 sec interval between each pulse. The highest specified IEC 61000-4-2 ESD test is Level 4, which defines a contact discharge voltage of  $\pm 8$  kV and an air discharge voltage of  $\pm 15$  kV.

Figure 3 shows the 8 kV contact discharge current waveform as described in the [ADM3066E](#) data sheet. Some key IEC 61000-4-2 waveform parameters are rise times of less than 1 ns and pulse widths of approximately 60 ns.

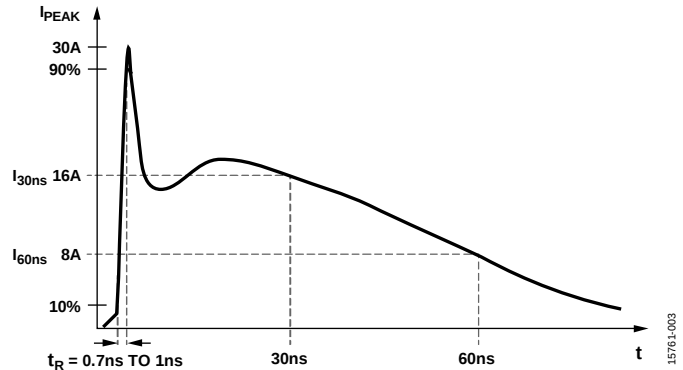


Figure 3. IEC 61000-4-2 ESD Waveform (8 kV)

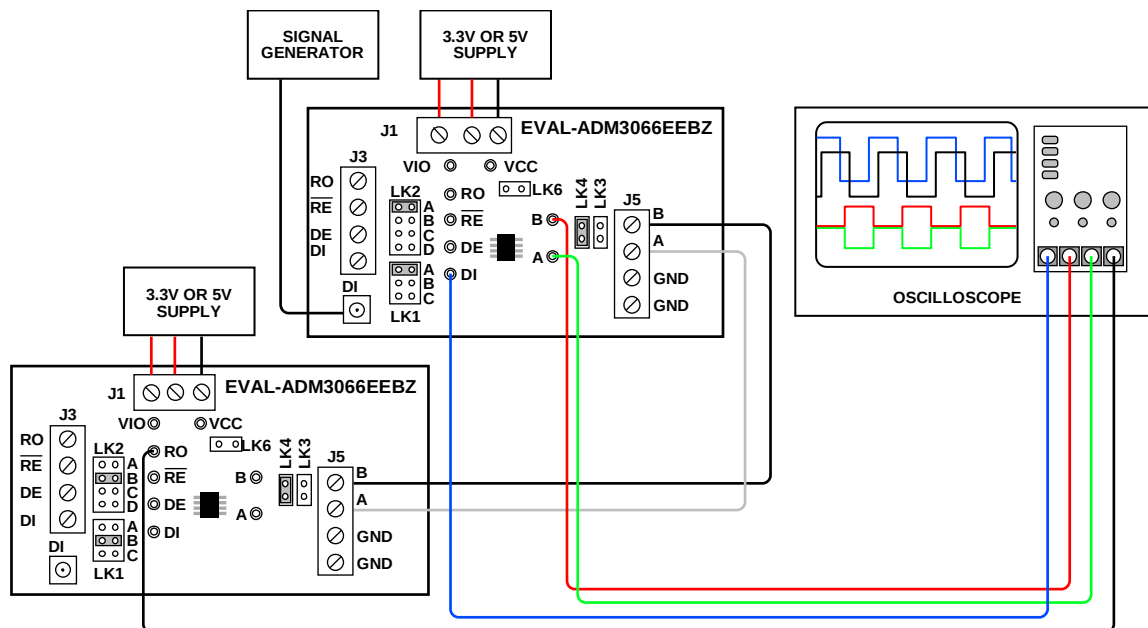


Figure 4. Half-Duplex RS-485 Two Board Point to Point Test

## EVALUATION BOARD SCHEMATICS AND ARTWORK

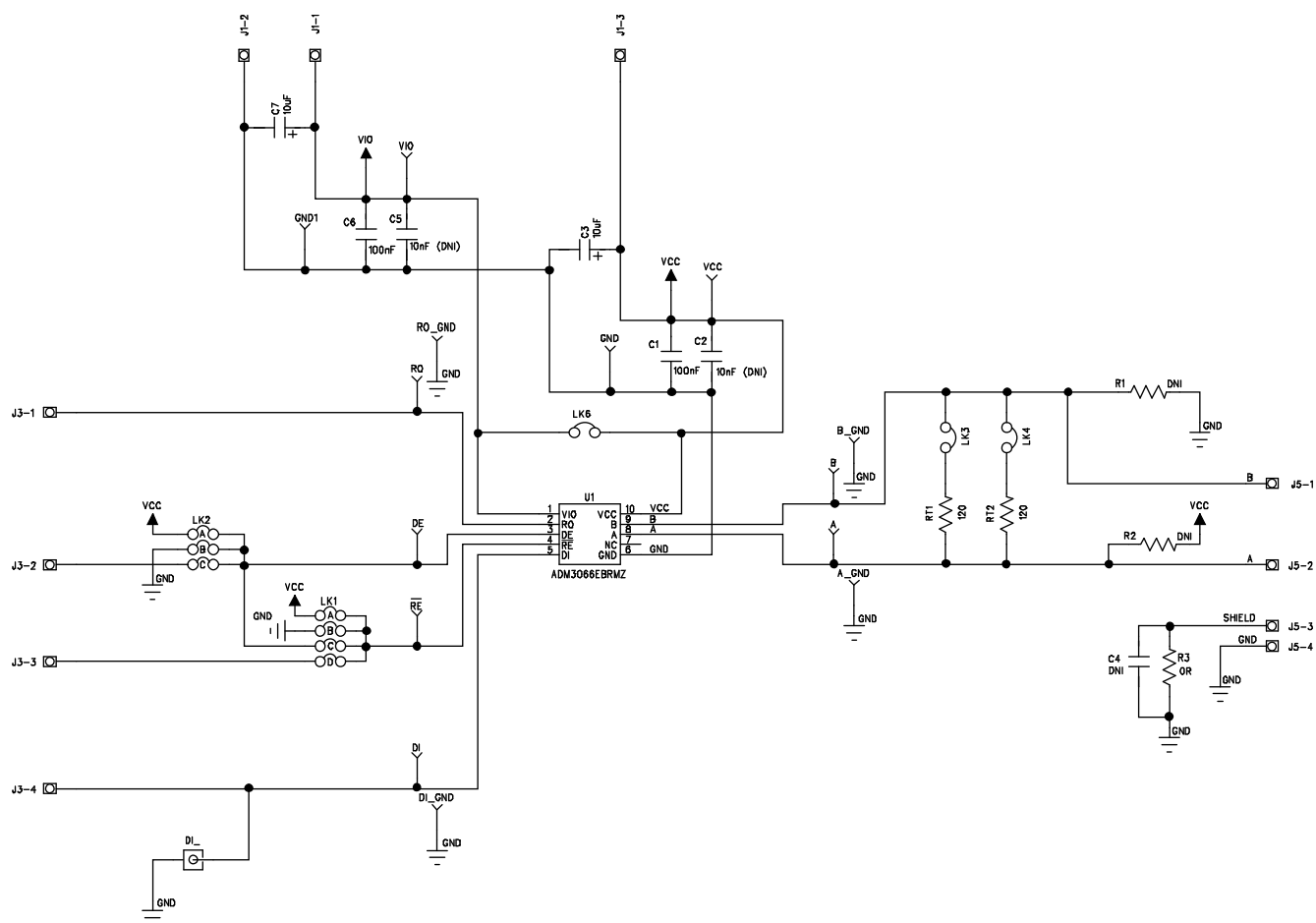


Figure 5. EVAL-ADM3066EEBZ Schematic

15761-005

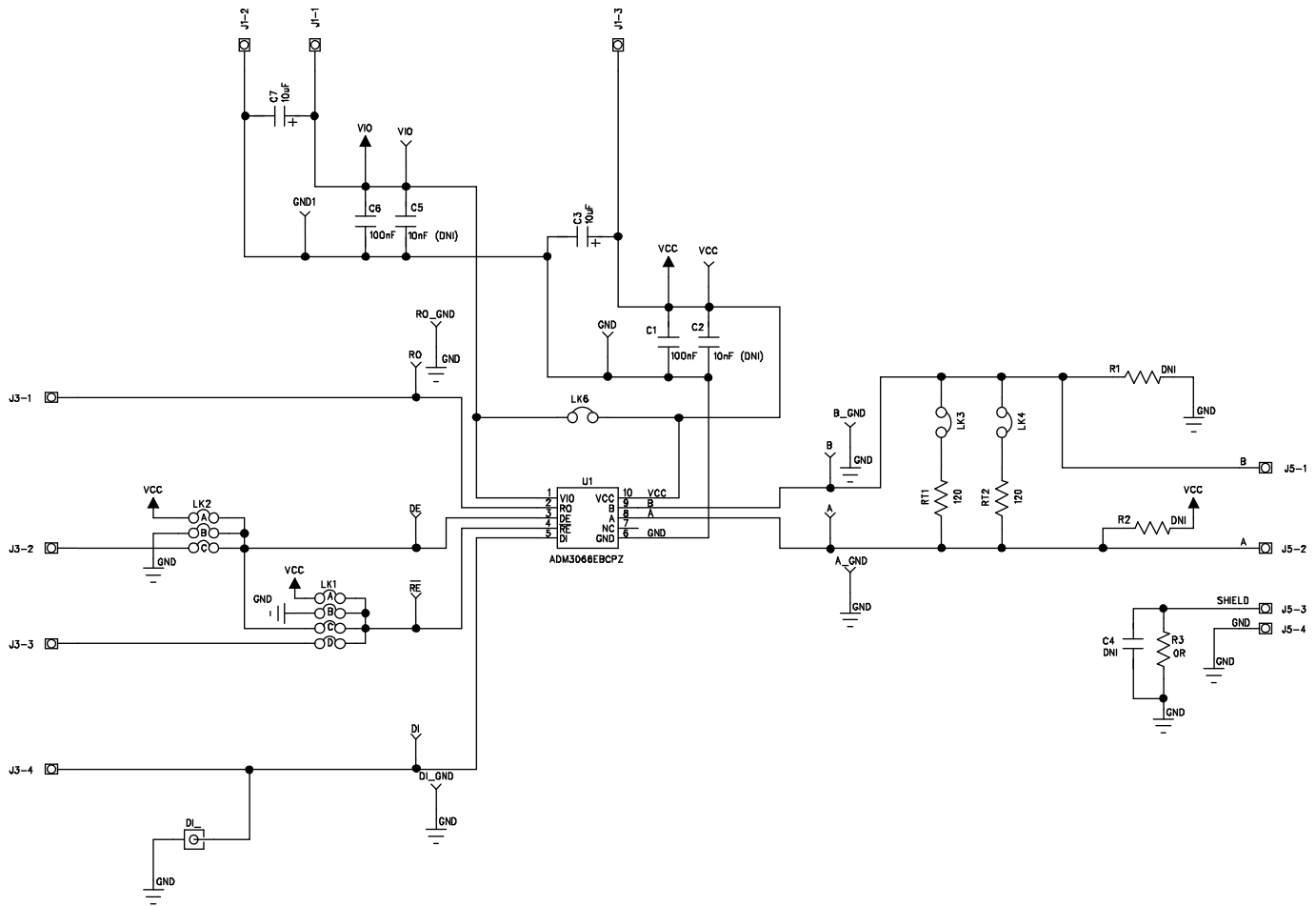


Figure 6. EVAL-ADM3066EEB1Z Schematic



5761-007



15761-008



5761-009



5761-010



5761-011

## ORDERING INFORMATION

## BILL OF MATERIALS

Table 2. EVAL-ADM3066EEBZ

| Qty | Reference Designator                       | Description                                          | Supplier             | Part Number                  |
|-----|--------------------------------------------|------------------------------------------------------|----------------------|------------------------------|
| 2   | C1, C6                                     | Capacitor, 100 nF, 0805                              | Multicomp            | MCCA000274                   |
| 3   | C2, C4, C5                                 | Capacitor, not placed/optional                       | Not applicable       | Not applicable               |
| 2   | C3, C7                                     | Capacitor, 10 $\mu$ F, Case B                        | Kemet                | B45196H3106K209              |
| 8   | A, B, DI, RO, A_GND, B_GND, DI_GND, RO_GND | High speed test point, silver pin                    | Not applicable       | 040/30P/LA/KP2 SILVER        |
| 2   | DE, RE                                     | Test point, yellow                                   | Vero Technologies    | 20-313140                    |
| 1   | GND                                        | Test point, black                                    | Vero Technologies    | 20-2137                      |
| 1   | DI_                                        | SMA right angle jack                                 | Not applicable       | 5-1814400-1                  |
| 1   | J1                                         | Two-way terminal block                               | Lumberg              | KRM 02                       |
| 2   | J3, J5                                     | Four-way terminal block                              | Lumberg              | KRM 04                       |
| 1   | LK1                                        | 8-pin (4 $\times$ 2), 0.1" header and shorting block | Harwin               | M20-9953646 and M7566-05     |
| 1   | LK2                                        | 6-pin (3 $\times$ 2), 0.1" header and shorting block | Harwin               | M20-9953646 and M7566-05     |
| 3   | LK3, LK4, LK6                              | Jumper block, 2-pin, 0.1" spacing                    | Harwin               | M20-9990246 and M7566-05     |
| 2   | R1, R2                                     | Resistor, not placed/optional                        |                      | Not applicable               |
| 1   | R3                                         | Resistor, 0 $\Omega$ , 0805                          | Vishay Draloric      | CRCW08050000Z0EA             |
| 2   | RT1, RT2                                   | Resistor, 120 $\Omega$ , 0805                        | Multicomp            | MC 0.1W 0805 1% 120R         |
| 1   | U1                                         | 10-lead MSOP                                         | Analog Devices, Inc. | <a href="#">ADM3066EBRMZ</a> |
| 2   | VCC, VIO                                   | Test point, red                                      | Vero Technologies    | 20-313137                    |

Table 3. EVAL-ADM3066EEB1Z

| Qty | Reference Designator                       | Description                                          | Supplier             | Part Number                  |
|-----|--------------------------------------------|------------------------------------------------------|----------------------|------------------------------|
| 2   | C1, C6                                     | Capacitor, 100 nF, 0805                              | Multicomp            | MCCA000274                   |
| 3   | C2, C4, C5                                 | Capacitor, not placed/optional                       | Not applicable       | Not applicable               |
| 2   | C3, C7                                     | Capacitor, 10 $\mu$ F, Case B                        | Kemet                | B45196H3106K209              |
| 8   | A, B, DI, RO, A_GND, B_GND, DI_GND, RO_GND | High speed test point, silver pin                    | Not applicable       | 040/30P/LA/KP2 SILVER        |
| 2   | DE, RE                                     | Test point, yellow                                   | Vero Technologies    | 20-313140                    |
| 1   | GND                                        | Test point, black                                    | Vero Technologies    | 20-2137                      |
| 1   | DI_                                        | SMA right angle jack                                 | Not applicable       | 5-1814400-1                  |
| 1   | J1                                         | Two-way terminal block                               | Lumberg              | KRM 02                       |
| 2   | J3, J5                                     | Four-way terminal block                              | Lumberg              | KRM 04                       |
| 1   | LK1                                        | 8-pin (4 $\times$ 2), 0.1" header and shorting block | Harwin               | M20-9953646 and M7566-05     |
| 1   | LK2                                        | 6-pin (3 $\times$ 2), 0.1" header and shorting block | Harwin               | M20-9953646 and M7566-05     |
| 3   | LK3, LK4, LK6                              | Jumper block, 2-pin, 0.1" spacing                    | Harwin               | M20-9990246 and M7566-05     |
| 2   | R1, R2                                     | Resistor, not placed/optional                        |                      | Not applicable               |
| 1   | R3                                         | Resistor, 0 $\Omega$ , 0805                          | Vishay Draloric      | CRCW08050000Z0EA             |
| 2   | RT1, RT2                                   | Resistor, 120 $\Omega$ , 0805                        | Multicomp            | MC 0.1W 0805 1% 120R         |
| 1   | U1                                         | 10-lead LFCSP                                        | Analog Devices, Inc. | <a href="#">ADM3066EBCPZ</a> |
| 2   | VCC, VIO                                   | Test point, red                                      | Vero Technologies    | 20-313137                    |

## RELATED LINKS

| Resource                                    | Description                                                               |
|---------------------------------------------|---------------------------------------------------------------------------|
| <a href="#">ADM3066E</a>                    | 3.0 V to 5.5 V, $\pm 12$ kV IEC ESD Protected, 50 Mbps RS-485 Transceiver |
| <a href="#">AN-960</a>                      | RS-485/RS-422 Circuit Implementation Guide                                |
| <a href="#">Interface RS-485 and RS-422</a> | RS-485/RS-422 Product Selection                                           |

**ESD Caution**

**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

**Legal Terms and Conditions**

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