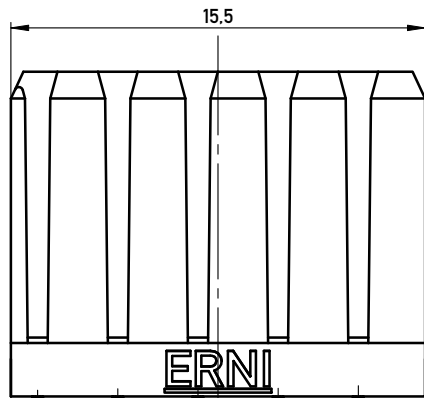
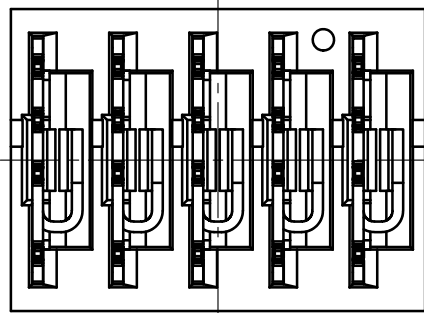
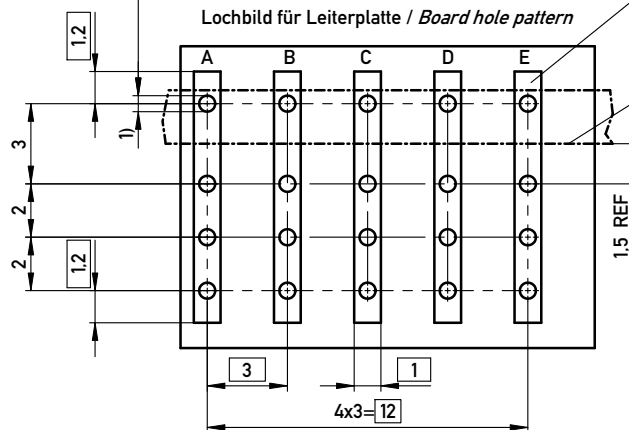




position A
position B
position C
position D
position E

$\phi 0.05$

Lochbild für Leiterplatte / Board hole pattern



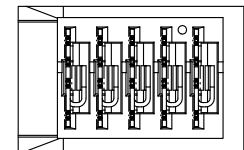
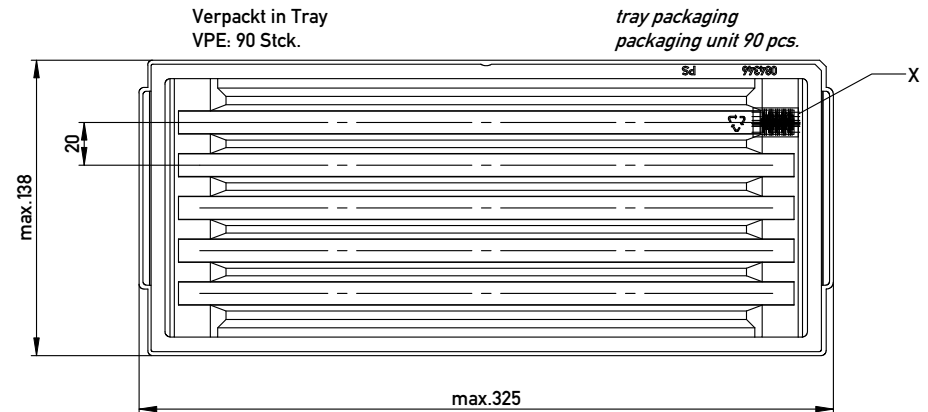
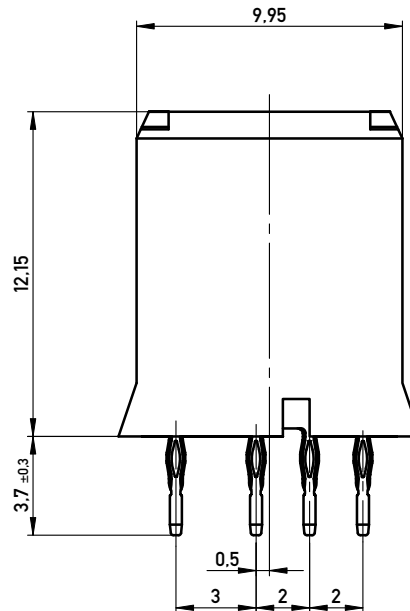
Dieser Bereich muß gleiches Potential auf LP-Oberfläche haben.

This area must have same electrical potential on surface of PCB

Oberkante Tochterkarte to surface of daughtercard REF

- 1) $\phi 0.6 \pm 0.05$ Durchmesser des metallisierten Loches
 $\phi 0.6 \pm 0.05$ Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 114406 oder 164062 Nr.1
Metal plating of plated-through hole see drawing 114406 or 164062 no.1



DETAIL X
MAßSTAB 2 : 1

Gehäuse / housing:
Kontakte / contacts:

PBTP grau 30% GF / PBTP gray 30 %GF
Phosphor Bronze / phosphor bronze

Oberflächenbeschichtung / plating:
Kontaktbereich / mating area:
Anschlussbereich / termination:

2-3 μ m Ni 1.2 μ m Au
2-3 μ m Ni 1 μ m Sn matt

Bedruckungsvorschrift siehe Id.-Nr. 294997 /
printingcode Id.-No. 294997

Anforderungsstufe 1
class 1

Information:		Tolerances		Scale	5:1
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		Subject to modification without prior notice. Drawing will not be updated.		ERmet Federl. PM 5 Pol RC ERmet Female PM 5 Pin RC	
-		18.09.2007	 www.ERNI.com		244995
Index		Date	Class		ERPM
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