

CMPT2484

**SURFACE MOUNT
NPN LOW NOISE
SILICON TRANSISTOR**



www.centrialsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPT2484 type is an NPN silicon low noise transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for low noise amplifier applications.

MARKING CODE: C1U



SOT-23 CASE

MAXIMUM RATINGS: (T_A=25°C)

Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V _{CB0}	60
V _{CEO}	60
V _{EBO}	6.0
I _C	50
P _D	350
T _J , T _{stg}	-65 to +150
θ _{JA}	357

UNITS

V
V
V
mA
mW
°C
°C/W

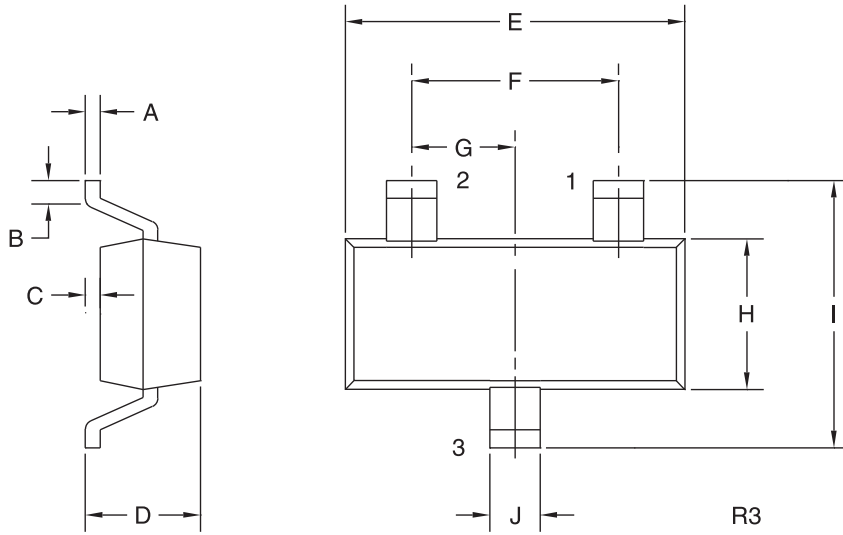
ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =45V		10	nA
I _{CBO}	V _{CB} =45V, T _A =150°C		10	μA
I _{EBO}	V _{EB} =5.0V		10	nA
BV _{CB0}	I _C =10μA	60		V
BV _{CEO}	I _C =10mA	60		V
BV _{EBO}	I _E =10μA	5.0		V
V _{CE(SAT)}	I _C =1.0mA, I _B =100μA		0.35	V
V _{BE(ON)}	V _{CE} =5.0V, I _C =1.0mA		0.95	V
h _{FE}	V _{CE} =5.0V, I _C =1.0mA	250		
h _{FE}	V _{CE} =5.0V, I _C =10mA		800	
C _{ob}	V _{CB} =5.0V, I _E =0, f=1.0MHz		6.0	pF
C _{ib}	V _{EB} =0.5V, I _C =0, f=1.0MHz		6.0	pF
NF	V _{CE} =5.0V, I _C =10μA, R _S =10kΩ, f=1.0kHz, BW=200Hz		3.0	dB

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SOT-23 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Base
- 2) Emitter
- 3) Collector

MARKING CODE: C1U

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.003	0.007	0.08	0.18
B	0.006	-	0.15	-
C	-	0.005	-	0.13
D	0.035	0.043	0.89	1.09
E	0.110	0.120	2.80	3.05
F	0.075		1.90	
G	0.037		0.95	
H	0.047	0.055	1.19	1.40
I	0.083	0.098	2.10	2.49
J	0.014	0.020	0.35	0.50

SOT-23 (REV: R3)

R5 (1-February 2010)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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