



#### 100V P-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on) max</sub>	I <sub>D</sub> T <sub>C</sub> = 25°C
-100V	240mΩ @ $V_{GS} = -10V$	-9A
	300mΩ @ V <sub>GS</sub> = -4.5V	-8A

### **Description**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- DC-DC Converters
- Power management functions
- Analog Switch

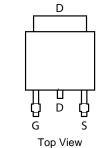
#### **Features**

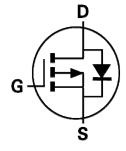
- Low On-Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
  Solderable per MIL-STD-202, Method 208 <a>3</a>
- Weight: 0.33 grams (approximate)







Internal Schematic

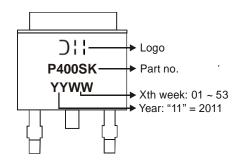
#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP10H400SK3-13	TO252	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com

## **Marking Information**





## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			$V_{DSS}$	-100	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 4) $V_{GS} = -10V$ Steady $T_C = 25^{\circ}C$ State $T_C = 100^{\circ}C$		I <sub>D</sub>	-9 -5.5	А	
Maximum Body Diode Forward Current (Note 4)			Is	-4	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I <sub>DM</sub>	-15	Α

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 4)	$T_C = 25^{\circ}C$	0	42	- W
Total Power Dissipation (Note 4)	T <sub>C</sub> = 100°C	P <sub>D</sub>	17	
Thermal Resistance, Junction to Ambient (Note 4)		$R_{ heta JA}$	44	°C/W
Thermal Resistance, Junction to Case (Note 4)		$R_{\theta JC}$	3	C/VV
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

4. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout. Notes:

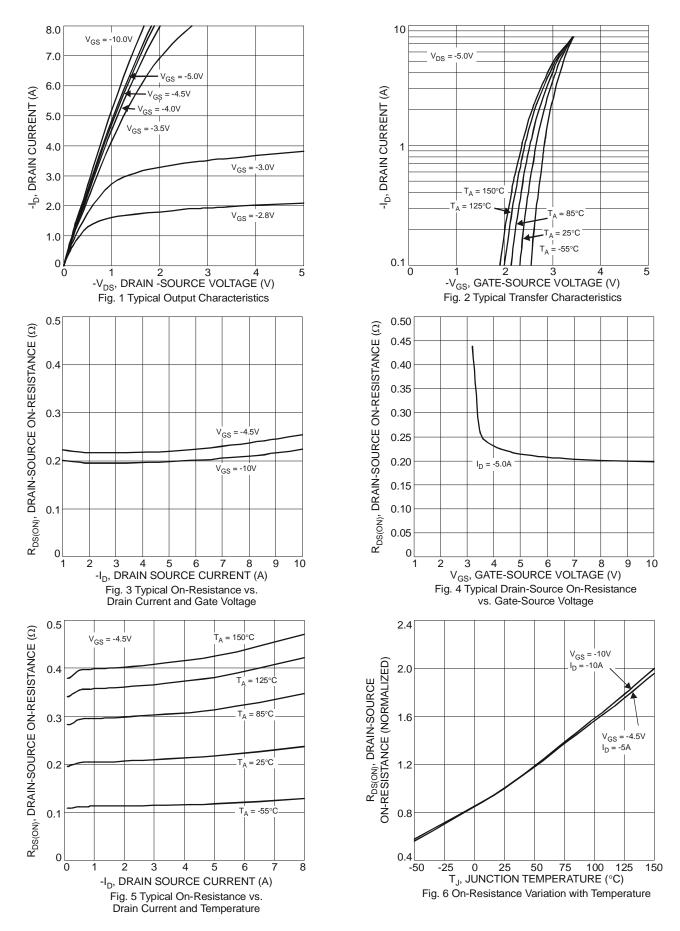
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = -100V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0		3.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	190	240	mΩ	$V_{GS} = -10V, I_D = -5A$	
Static Dialii-Source Off-Resistance	R <sub>DS (ON)</sub>	_	210	300	1112.2	$V_{GS} = -4.5V, I_D = -5A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	7	_	S	$V_{DS} = -30V, I_{D} = -5A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = -5A$	
DYNAMIC CHARACTERISTICS (Note 6)							
Input Capacitance	C <sub>iss</sub>	_	1239	_		V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	42	_	pF		
Reverse Transfer Capacitance	$C_{rss}$	_	28	_			
Gate Resistance	$R_{G}$	_	13	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	$Q_g$	_	8.4	_			
Total Gate Charge (V <sub>GS</sub> = -10V)	$Q_g$	_	17.5	_	nC	V 60V I 5A	
Gate-Source Charge	$Q_{gs}$	_	2.8	_	nc	$V_{DS} = -60V, I_{D} = -5A$	
Gate-Drain Charge	$Q_{gd}$	_	3.2	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	9.1	_			
Turn-On Rise Time	t <sub>r</sub>	_	14.9	_		V 50V B 040 L 54	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	57.4	_	ns	$V_{DD} = -50V, R_G = 9.1\Omega, I_D = -5A$	
Turn-Off Fall Time	t <sub>f</sub>	_	34.4	_			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	25.2	_	ns	$V_{GS} = 0V$ , $I_S = -5A$ , $dI/dt = 100A/\mu s$	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		24.5	_	nC	$V_{GS} = 0V$ , $I_S = -5A$ , $dI/dt = 100A/\mu s$	

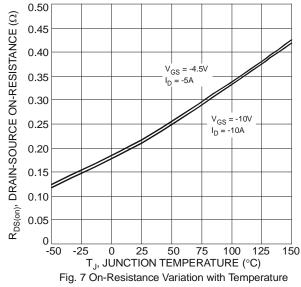
5. Short duration pulse test used to minimize self-heating effect Notes:

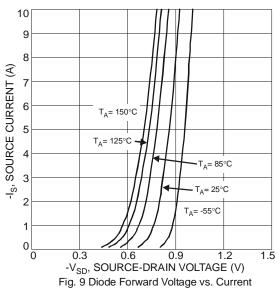
6. Guaranteed by design. Not subject to production testing

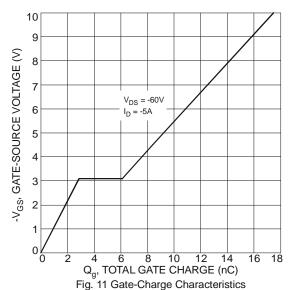












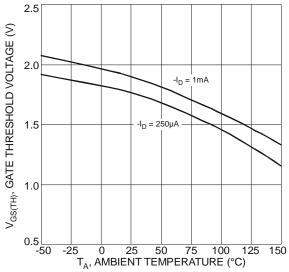
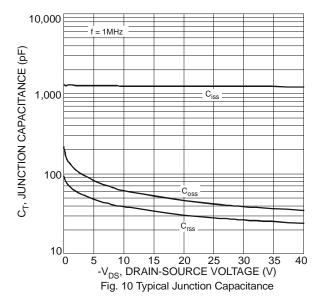
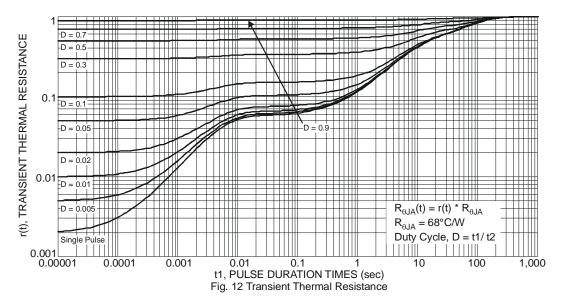


Fig. 8 Gate Threshold Variation vs. Ambient Temperature

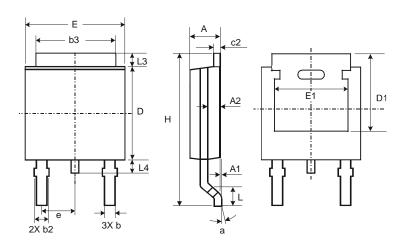






# **Package Outline Dimensions**

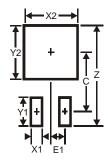
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
<b>A1</b>	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	_	_		
е	_	_	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	_		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
Z	11.6		
X1	1.5		
X2	7.0		
Y1	2.5		
Y2	7.0		
С	6.9		
E1	2.3		



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