MICRO CRYSTAL SWITZERLAND

RV-8564-C3
Development Board

DEVELOPMENT BOARD

RV-8564-C3
Low-Power Real Time Clock / Calendar Module

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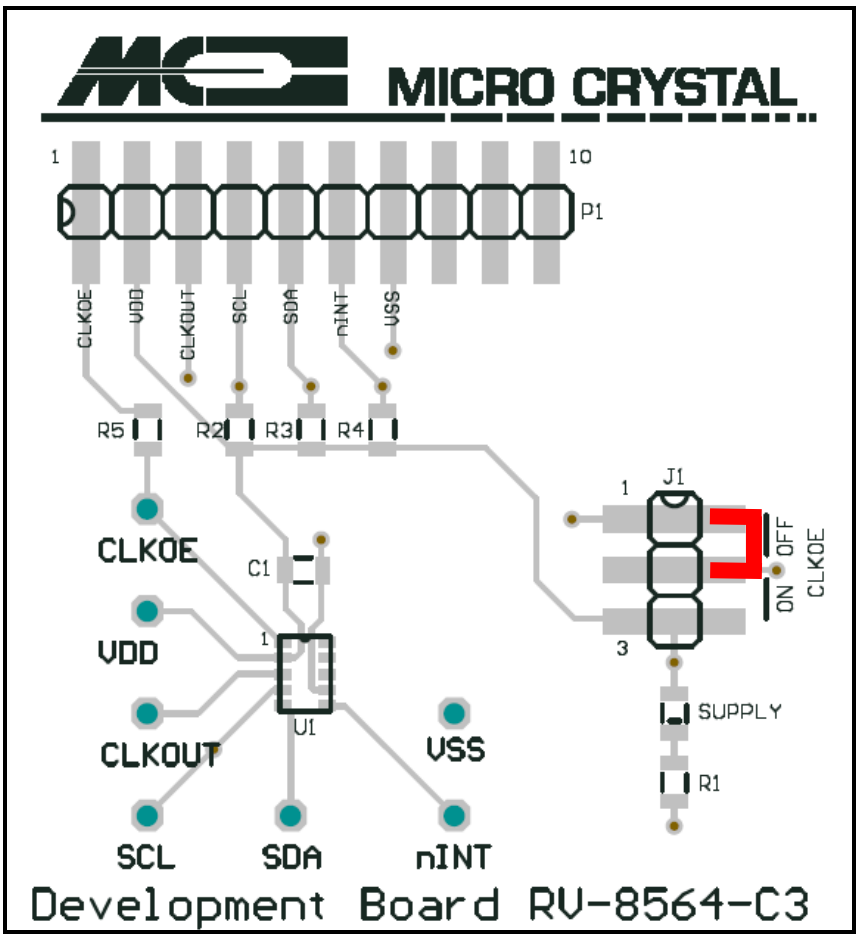
RV-8564-C3

The RV-8564-C3 is soldered onto the Development Board.
Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between V_{SS} and V_{DD}
R1	330 Ω	current limiting resistor for LED
LED	green	Supply, current consumption of the LED has to be considered
R2	10 kΩ	Pull-up resistor SCL to V_{DD}
R3	10 kΩ	Pull-up resistor SDA to V_{DD}
R4	10 kΩ	Pull-up resistor INT to V_{DD}
R5	10 kΩ	Protection resistor to prevent short-circuit between external CLKOE signal and Jumper.

DEVELOPMENT BOARD



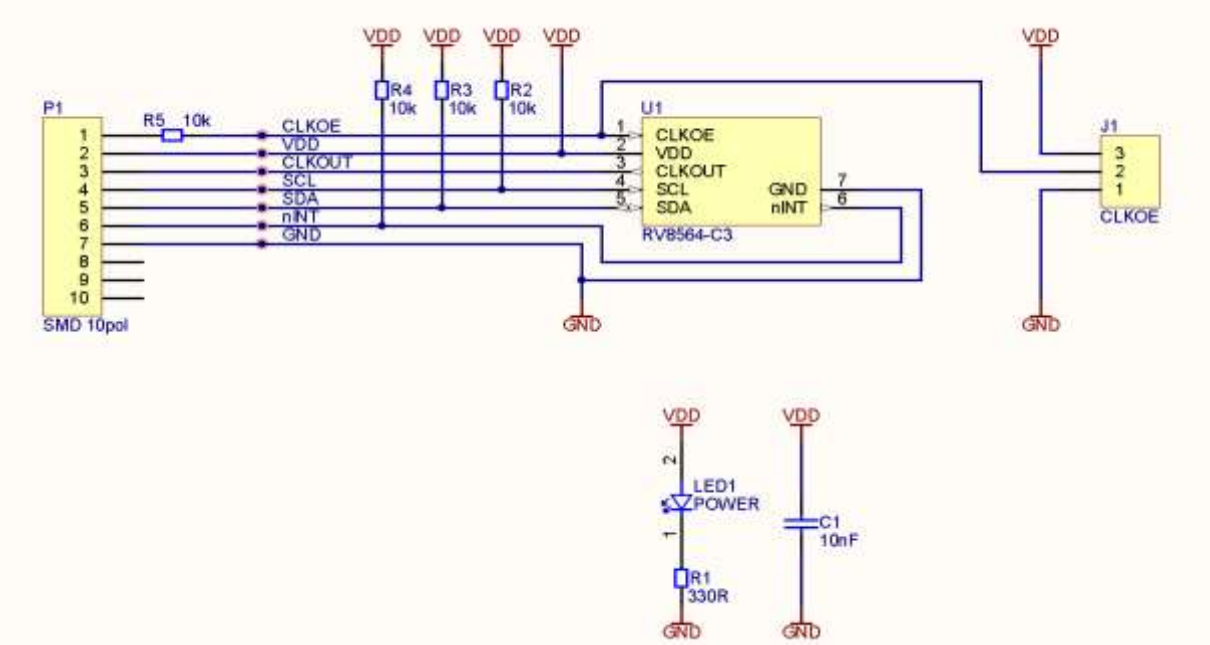
JUMPER

CLKOE = HIGH

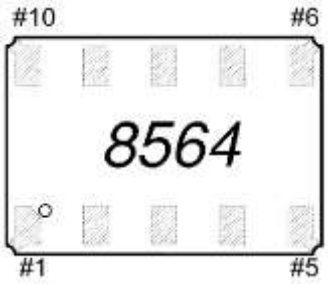
CLKOE = LOW

Development Board RV-8564-C3

SCHEMATICS



PINOUT RV-8564-C3



# 1 CLKOE	#10 N.C.
# 2 V_{DD}	# 9 N.C.
# 3 CLKOUT	# 8 N.C.
# 4 SCL	# 7 V_{SS}
# 5 SDA	# 6 $\overline{INT}$

PIN DESCRIPTION

Symbol	Pin #	Description
CLKOE	1	CLKOUT enable/disable pin; enable is active HIGH
V_{DD}	2	Positive supply voltage; recommended 10 nF decoupling capacitor close to device
CLKOUT	3	Clock Output pin; push-pull output; at power-up by default 32.768kHz
SCL	4	Serial Clock Input pin; requires pull-up resistor
SDA	5	Serial Data Input-Output pin; open-drain; requires pull-up resistor.
$\overline{INT}$	6	Interrupt Output pin; open-drain; active LOW
V_{SS}	7	Ground
NC	8	Not Connected
NC	9	Not Connected
NC	10	Not Connected

Datasheet and Application-Manual are available for download under: www.microcrystal.com