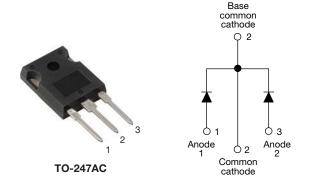


Vishay Semiconductors

## Ultrafast Rectifier, 2 x 30 A FRED Pt®



PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 30 A				
$V_{R}$	400 V				
V <sub>F</sub> at I <sub>F</sub>	1.30 V				
t <sub>rr</sub> typ.	37 ns				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				

#### **FEATURES**

- Low forward voltage drop
- 175 °C operating junction temperature
- · Ultrafast recovery time
- Low leakage current
- Designed and qualified according to JEDEC-JESD47
- Material categorization:
   For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>





ROHS
COMPLIANT
HALOGEN
FREE

#### **DESCRIPTION/APPLICATIONS**

VS-60CPU04... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, welding, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters, and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Peak repetitive reverse voltage	$V_{RRM}$		400	V				
Average rectified forward current per le		Rated V <sub>R</sub> , T <sub>C</sub> = 134 °C	30	Α				
per device	e I <sub>F(AV)</sub>		60					
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	300	^				
Peak repetitive forward current per leg	I <sub>FM</sub>	Rated $V_R$ , square wave, 20 kHz, $T_C$ = 134 °C	60					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C				

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	400	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.10	1.30	.,	
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	0.92	1.10	V	
		I <sub>F</sub> = 60 A	-	1.25	1.6		
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	1.10	1.4		
Reverse leakage current		$V_R = V_R$ rated	-	-	10		
neverse leakage current	I <sub>R</sub>	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	-	100	μA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 400 V	-	40	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	ı	12	-	nΗ	



# VS-60CPU04-F3, VS-60CPU04-N3

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, dI_F/dt = 10$	$00 \text{ A/}\mu\text{s}, \text{ V}_{\text{R}} = 30 \text{ V}$	-	37	40		
Boyoroo roooyory timo		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	46	-		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	65	-	ns	
		T <sub>J</sub> = 125 °C		-	119	-		
Dools woody on a commont	,	T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$	-	6.4	-	Α	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	14.7	-	_ ^	
Reverse recovery charge		T <sub>J</sub> = 25 °C		-	206	-	nC	
	$Q_{rr}$	T <sub>J</sub> = 125 °C		-	874	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.6	1.0		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	40	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	6	-	g	
vveigni			-	0.21	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-247AC		60CI	PU04		

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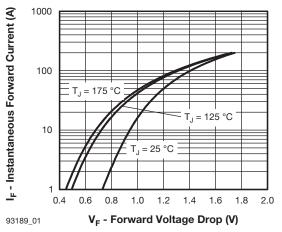


Fig. 1 - Typical Forward Voltage Drop Characteristics

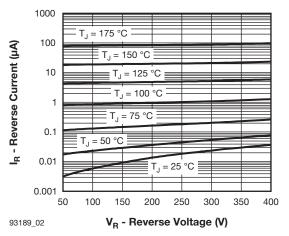


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

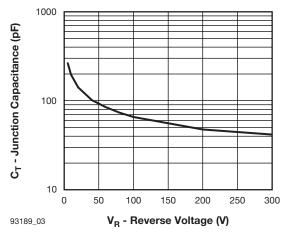


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

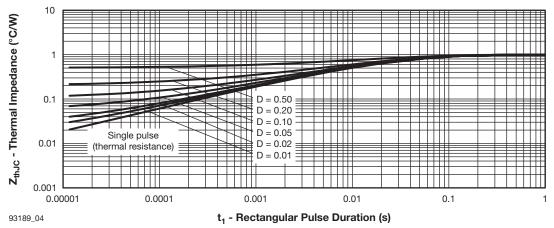


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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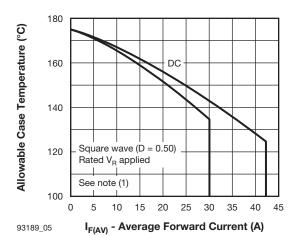


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

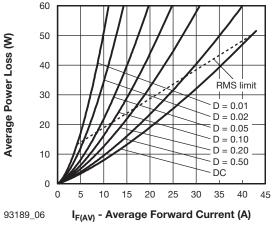


Fig. 6 - Forward Power Loss Characteristics

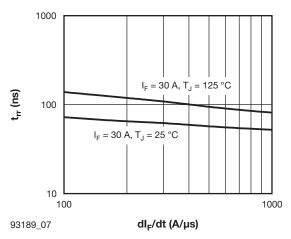


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

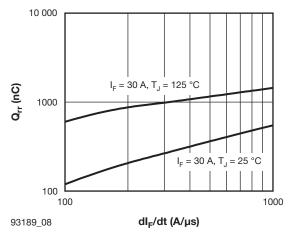


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ (1 - D); \ I_R \ \text{at} \ V_{R1} = \text{Rated} \ V_R \\ \end{array}$ 

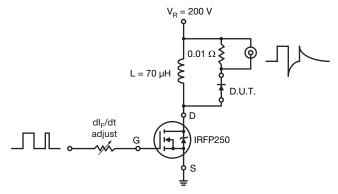
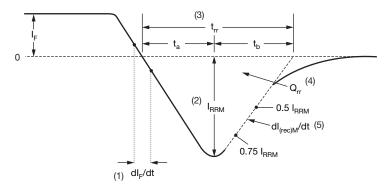


Fig. 9 - Reverse Recovery Parameter Test Circuit

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- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75  $I_{RRM}$  and 0.50  $I_{RRM}$  extrapolated to zero current.
- (4)  ${\rm Q}_{\rm rr}$  area under curve defined by  ${\rm t}_{\rm rr}$  and  ${\rm I}_{\rm BBM}$

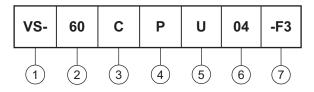
$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) dI<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

### **Device code**



- Vishay Semiconductors product
- 2 Current rating (60 = 60 A)
- Circuit configuration: C = Common cathode
- **4** P = TO-247AC
- 5 U = Ultrafast rectifier
- 6 Voltage rating (04 = 400 V)
- 7 Environmental digit:
  - -F3 = RoHS compliant and totally lead (Pb)-free
  - -N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

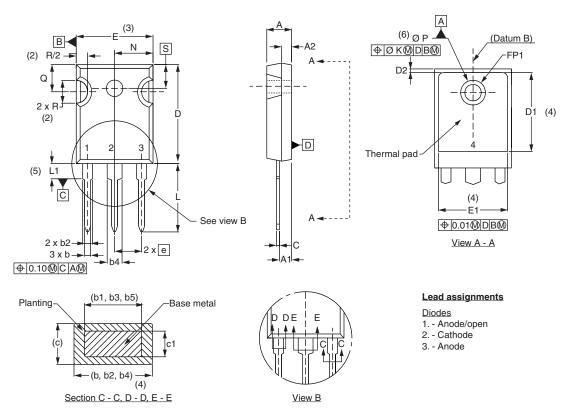
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-60CPU04-F3	25	500	Antistatic plastic tube				
VS-60CPU04-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95542				
Part marking information	www.vishay.com/doc?95007				
SPICE model	www.vishay.com/doc?95398				



### Vishay Semiconductors

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	INCHES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.54		0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



### **Legal Disclaimer Notice**

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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