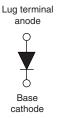
High Performance Schottky Rectifier, 240 A



www.vishay.com



240 A

15 V

HALF-PAK (D-67)

Single diode

HALF-PAK (D-67)

PRODUCT SUMMARY

I_{F(AV)}

 V_R

Package

Circuit

F	EATURES
•	125 °C T _{.1} operation

- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-245NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	240	А		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	20 000	А		
V _F	240 A _{pk} , T _J = 75 °C	0.37	V		
Т _Ј	Range	-55 to 125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-245NQ015PbF	UNITS		
Maximum DC reverse voltage	V _R	15	V		
Maximum working peak reverse voltage	V _{RWM}	25	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 73 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated FSM 10 ms sine or 6 ms rect. pulse V _{RRM} applied	20 000	A	
See fig. 7	'FSM			3000	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 5 A, L = 1 mH		12	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	А

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COMPLIANT





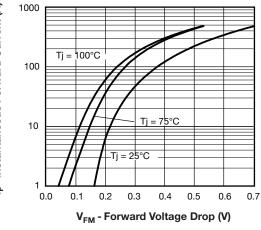
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		240 A	T 05 %C	0.52	V mA pF nH
Maximum forward voltage drop	V _{EM} ⁽¹⁾	480 A	T _J = 25 °C	0.61	
See fig. 1	VFM (**	240 A	T. = 125 °C	0.37 045	v
		480 A	1j = 125 C		
Maximum reverse leakage current	1 (1)	T _J = 25 °C	V - Reted V	80	mA
See fig. 2	I _{RM} (1)	T _J = 125 °C	V _R = Rated V _R	4000	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		15 800	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

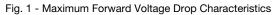
Note

⁽¹⁾ Pulse width < 500 μ s

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range)	T _J , T _{Stg}		-55 to 125	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	0.19 0.05	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05		
A				30	g	
Approximate weight	Approximate weight			1.06	oz.	
Mounting torque	minimum		Non-lubricated threads	3 (26.5)	N · m (lbf · in)	
Mounting torque	maximum			4 (35.4)		
— · · · ·	minimum			3.4 (30)		
Terminal torque	maximum			5 (44.2)		
Case style				HALF-PA	K module	







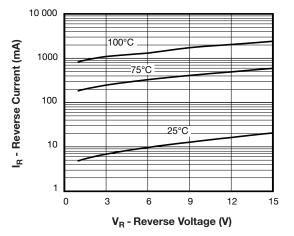


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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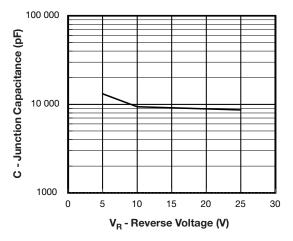
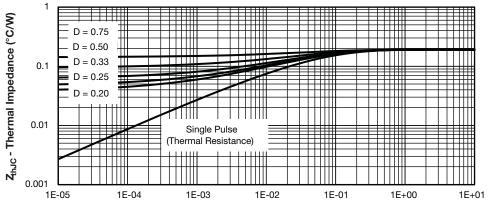
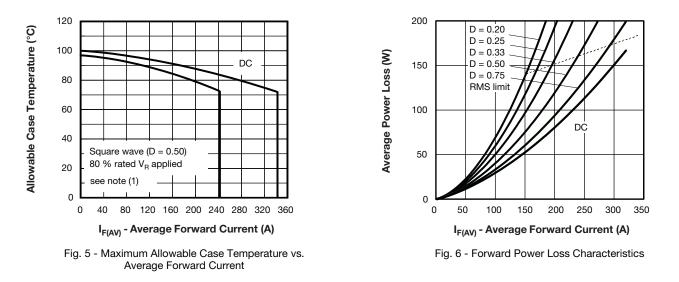


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



t₁ - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



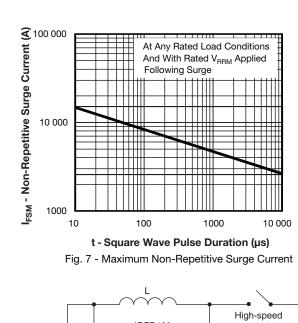
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VS-245NQ015PbF

Vishay Semiconductors



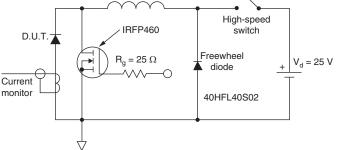


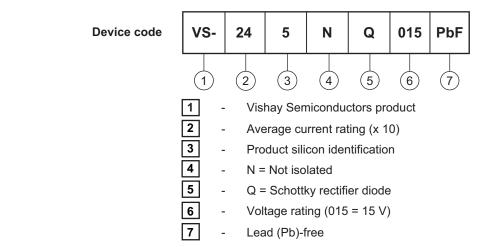
Fig. 8 - Unclamped Inductive Test Circuit

Note

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 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

17.5 (0.69) 16.5 (0.65)



DIMENSIONS in millimeters (inches)

SHAY





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