

PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR
Features

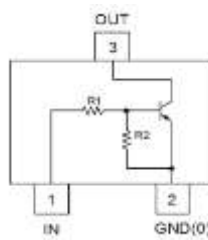
- Epitaxial Planar Die Construction
- Built-In Biasing Resistors, R1 = R2
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

R1, R2 (NOM)
47kΩ

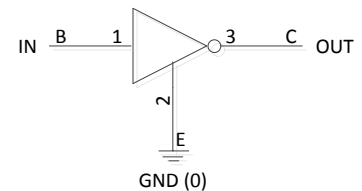
SOT23



Top View



Device Schematic



Equivalent Inverter Circuit

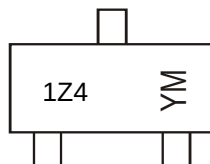
Ordering Information (Notes 4 & 5)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ADTA144ECAQ-7	Automotive	1Z4	7	8	3,000
ADTA144ECAQ-13	Automotive	1Z4	13	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

SOT23



1Z4 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec				
Code	1	2	3	4	5	6	7	8	9	O	N	D				

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

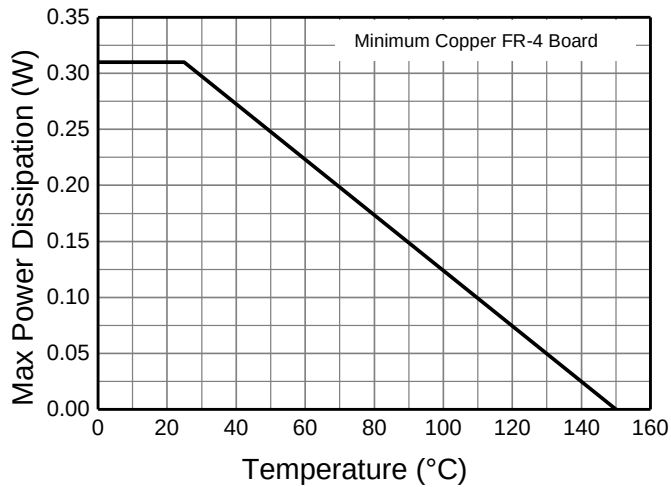
Characteristic	Symbol	Value	Unit
Supply Voltage <Pin: (3) to (2)>	V _{CC}	-50	V
Input Voltage <Pin: (1) to (2)>	V _{IN}	+10 to -40	V
Output Current	I _O	-30	mA
Output Current	I _C (Max)	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

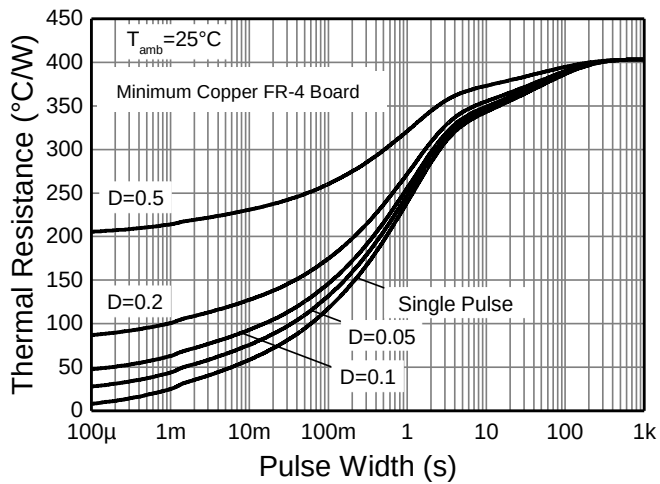
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	310	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{θJA}	403	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Note: 6. Mounted on FR-4 PC Board with minimum recommended pad layout.

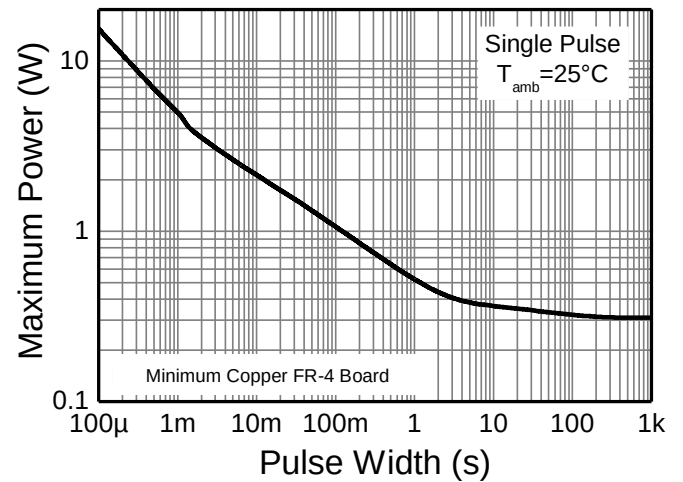
Thermal Characteristics and Derating Information



Derating Curve



Transient Thermal Impedance



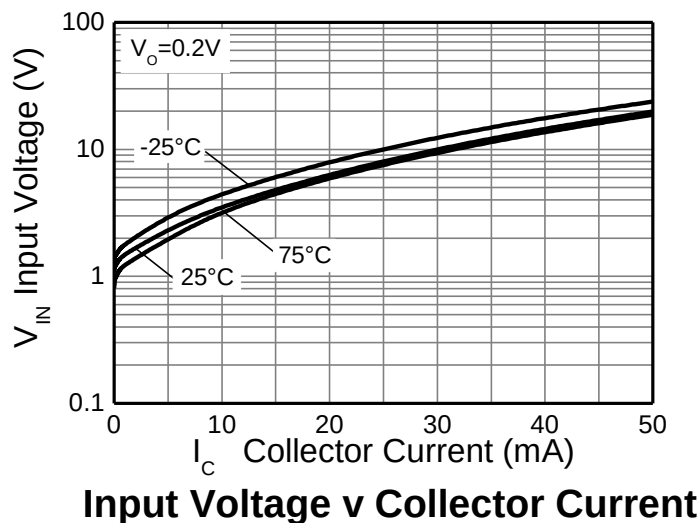
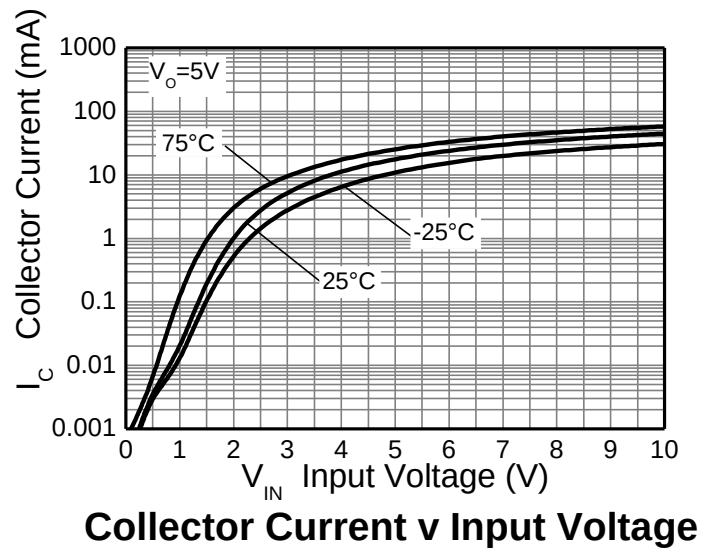
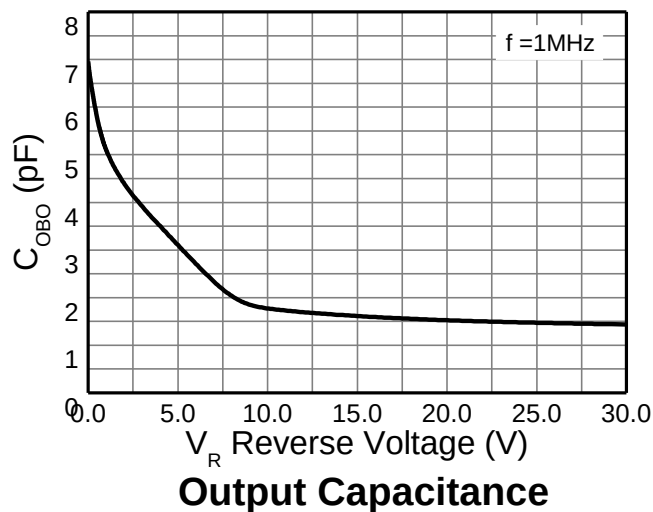
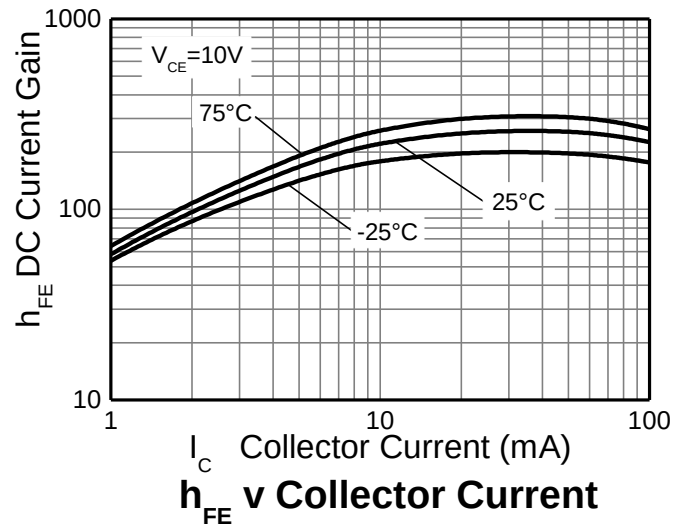
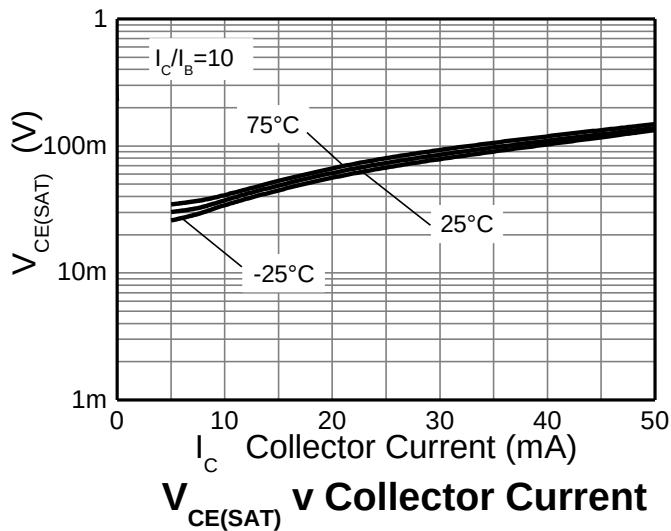
Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage	V _{I(OFF)}	-0.5	-1.1	—	V	V _{CC} = -5V, I _O = -100μA
	V _{I(ON)}	—	-1.9	-3		V _O = -0.3V, I _O = -2mA
Output Voltage	V _{O(ON)}	—	-0.1	-0.3	V	I _O /I _I = -10mA/-0.5mA
Input Current	I _I	—	—	-0.18	mA	V _I = -5V
Output Current	I _{O(OFF)}	—	—	-0.5	μA	V _{CC} = -50V, V _I = 0V
DC Current Gain	G _I	68	—	—	—	V _O = -5V, I _O = -5mA
Input Resistor Tolerance	ΔR ₁	-30	—	+30	%	—
Resistance Ratio Tolerance	ΔR ₂ /R ₁	-20	—	+20	%	—
Gain-Bandwidth Product (Note 7)	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz

Note: 7. Transistor - For Reference Only.

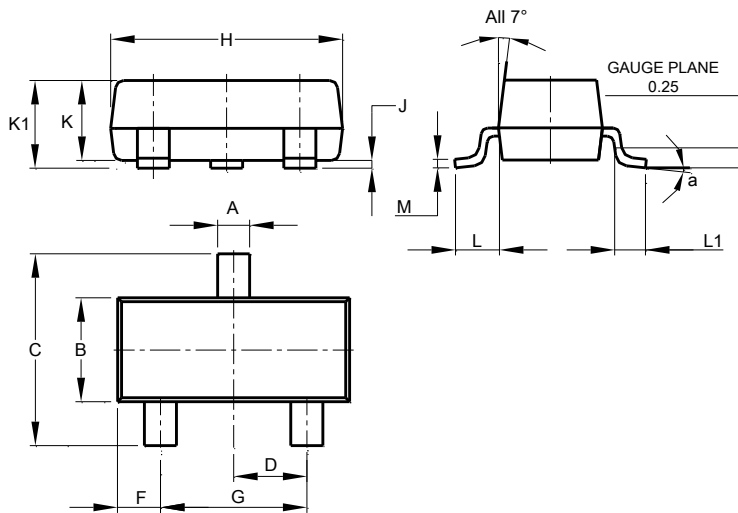
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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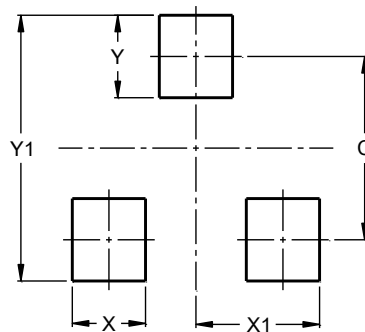


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Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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