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Part Number: [0780658101](#)
Status: **Active**
Description: 1.00mm (.039") Pitch DDR2 DIMM Socket, Very Low Profile, Vertical, Press-fit, 1.8V Center Keys, 3.30mm (.130") Tail Length, Black Latches, 240 Circuits, 0.76µm (30µm) Gold(Au) Plating, Solder-tail: Tin/Lead (Pb/Sn) over Nickel Underplate version

Documents:

[3D Model](#) [Product Specification PS-78065-001 \(PDF\)](#)
[Drawing \(PDF\)](#) [RoHS Certificate of Compliance \(PDF\)](#)

Agency Certification

CSA LR19980
UL E29179

General

Product Family Memory Module Sockets
Series [78065](#)
Comments Latches in Black Color
Component Type Socket
JEDEC Outline MO-237
Product Name DDR2 DIMM

Physical

Circuits (Loaded) 240
Circuits (maximum) 240
Color - Resin Black
Durability (mating cycles max) 25
Entry Angle Vertical
Flammability 94V-0
Function Key None
Keying to Mating Part Yes
Material - Metal Phosphor Bronze
Material - Plating Mating Gold
Material - Plating Termination Tin-Lead
Material - Resin High Temperature Thermoplastic
PC Tail Length (in) 0.130 In
PC Tail Length (mm) 3.30 mm
PCB Locator No
PCB Retention Yes
PCB Thickness Recommended (in) 0.142 In
PCB Thickness Recommended (mm) 3.60 mm
Packaging Type Tray
Pitch - Mating Interface (in) 0.039 In
Pitch - Mating Interface (mm) 1.00 mm
Pitch - Term. Interface (in) 0.039 In, 0.197 In
Pitch - Term. Interface (mm) 1.00 mm, 5.00 mm
Plating min: Mating (µin) 30.4
Plating min: Mating (µm) 0.76
Plating min: Termination (µin) 15.2
Plating min: Termination (µm) 0.38
Temperature Range - Operating -55°C to +85°C
Termination Interface: Style Through Hole - Compliant Pin

Electrical

Current - Maximum per Contact 0.5A
Voltage - Maximum 30V AC (RMS)/DC

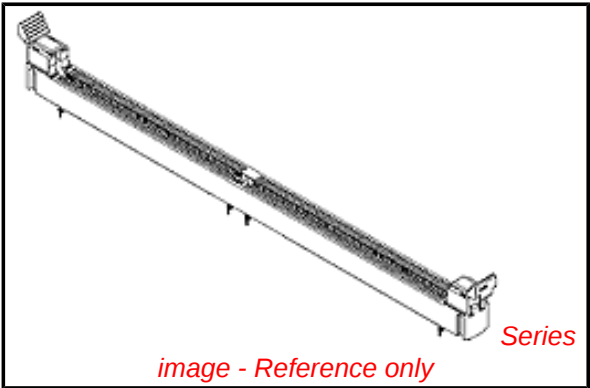


image - Reference only

EU RoHS

RoHS Compliant by Exemption

REACH SVHC

Not Reviewed

Halogen-Free

Status

Not Halogen-Free

China RoHS



Need more information on product environmental compliance?

Email productcompliance@molex.com
For a multiple part number RoHS Certificate of Compliance, [click here](#)

Please visit the [Contact Us](#) section for any non-product compliance questions.

Search Parts in this Series

[78065Series](#)

Mates With

JEDEC MO-237 modules

Voltage Key

Center

Solder Process Data

Replacement RoHS Compliant Part Number

0780658001

Material Info

Reference - Drawing Numbers

Product Specification

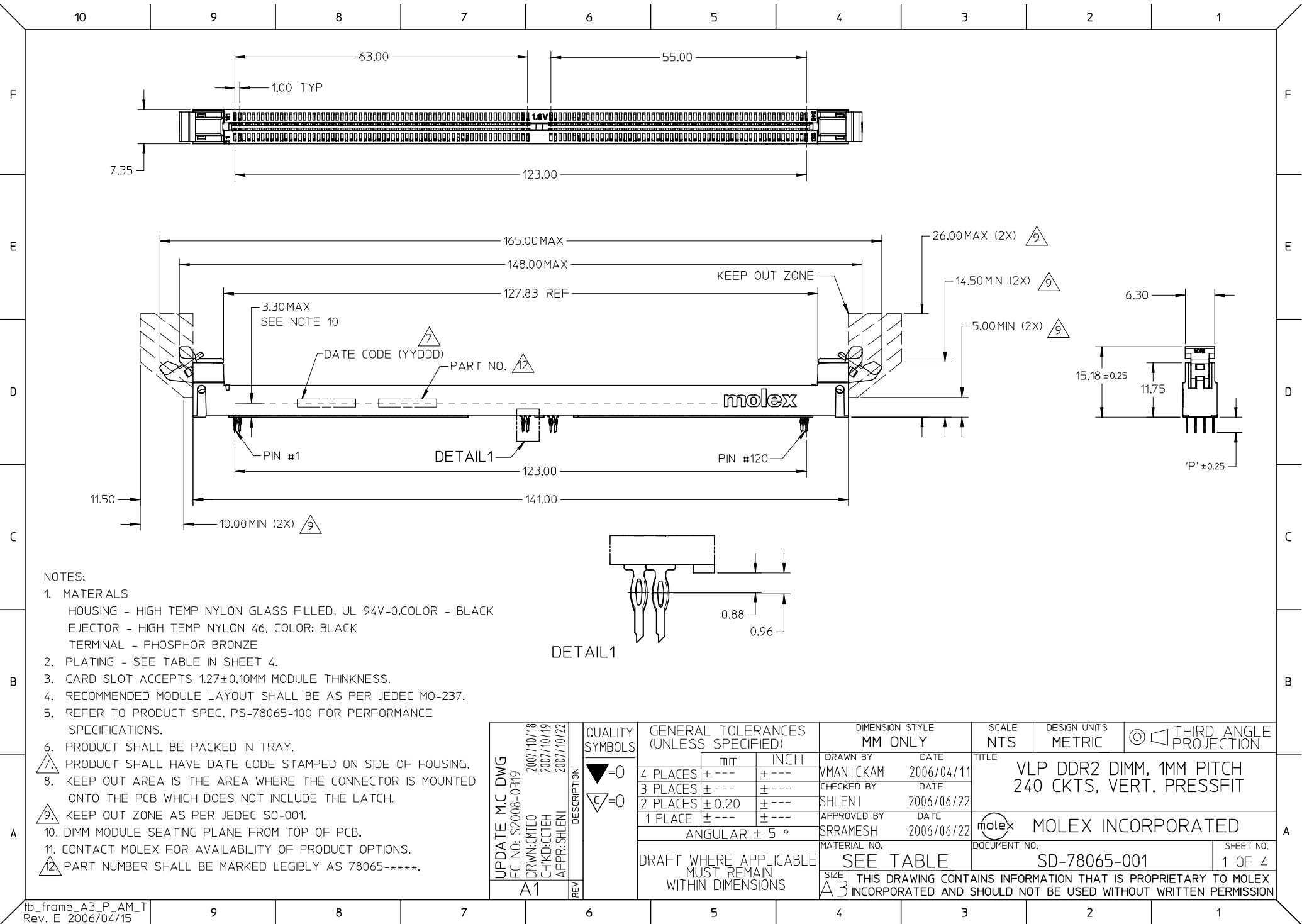
PS-78065-001, RPS-78065-002

Sales Drawing

SD-78065-001

This document was generated on 03/17/2010

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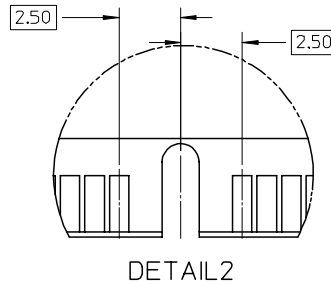
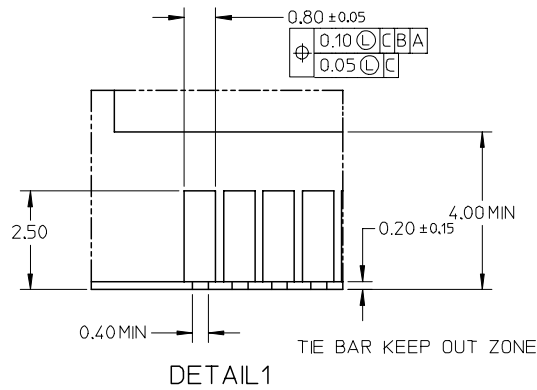
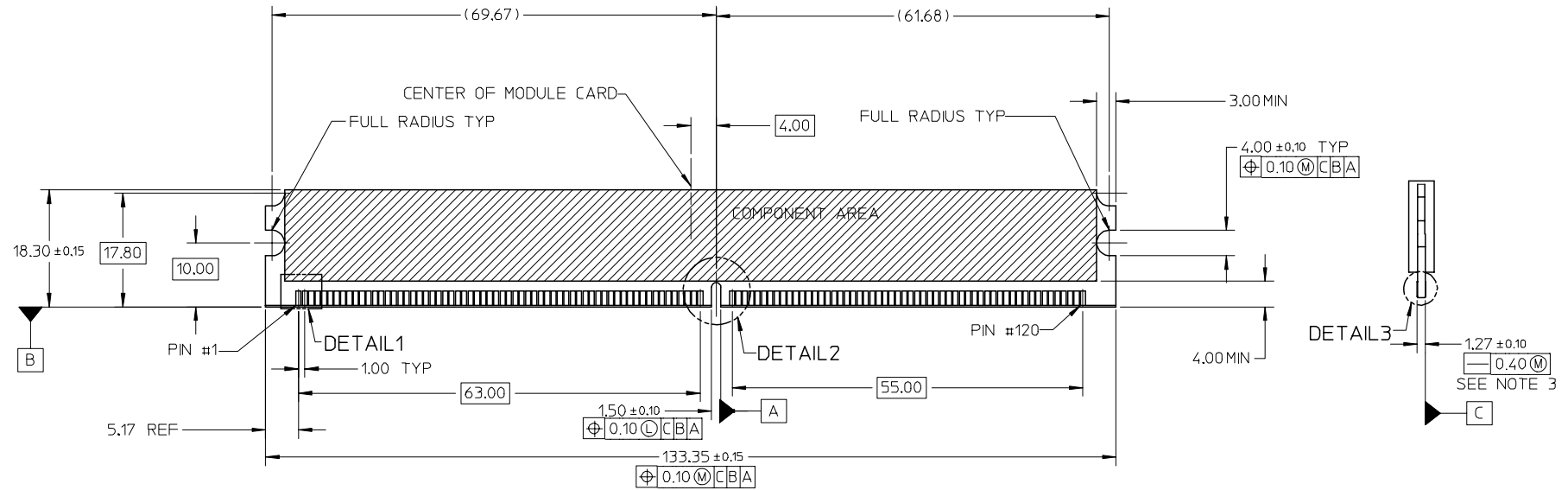
NOTES:

1. MATERIALS
HOUSING - HIGH TEMP NYLON GLASS FILLED, UL 94V-0,COLOR - BLACK
EJECTOR - HIGH TEMP NYLON 46, COLOR: BLACK
TERMINAL - PHOSPHOR BRONZE
2. PLATING - SEE TABLE IN SHEET 4.
3. CARD SLOT ACCEPTS 1.27±0.10MM MODULE THICKNESS.
4. RECOMMENDED MODULE LAYOUT SHALL BE AS PER JEDEC MO-237.
5. REFER TO PRODUCT SPEC, PS-78065-100 FOR PERFORMANCE SPECIFICATIONS.
6. PRODUCT SHALL BE PACKED IN TRAY.
7. PRODUCT SHALL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.
8. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR IS MOUNTED ONTO THE PCB WHICH DOES NOT INCLUDE THE LATCH.
9. KEEP OUT ZONE AS PER JEDEC SO-001.
10. DIMM MODULE SEATING PLANE FROM TOP OF PCB.
11. CONTACT MOLEX FOR AVAILABILITY OF PRODUCT OPTIONS.
12. PART NUMBER SHALL BE MARKED LEGIBLY AS 78065-****.

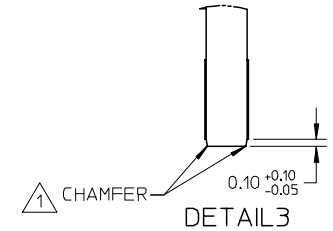
UPDATE M.C DWG EC NO: S2008-0319 DRWN:CMTEO CHKD:CCTH APPR:SHLENI A1		2007/10/18 2007/10/19 2007/10/22	DESCRIPTION REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
				▽=0										
				▽C=0										
					mm	INCH	DRAWN BY	DATE	TITLE VLP DDR2 DIMM, 1MM PITCH 240 CKTS, VERT. PRESSFIT					
				4 PLACES	± ---	± ---	VMANICKAM	2006/04/11						
						3 PLACES	± ---	± ---	CHECKED BY	DATE 2006/06/22				
						2 PLACES	± 0.20	± ---	SHLENI					
						1 PLACE	± ---	± ---	APPROVED BY	DATE 2006/06/22				
						ANGULAR ± 5 °		SRRAMESH						
						DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO.	DOCUMENT NO. SD-78065-001					
								SEE TABLE					SHEET NO. 1 OF 4	
								SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

10 9 8 7 6 5 4 3 2 1

RECOMMENDED MODULE LAYOUT



KEY LOCATION	VOLTAGE
CENTER	1.8V



NOTES:
 1. MODULE CARD USED FOR TESTS ARE WITH 0.10MM CHAMFER.

UPDATE M.C DWG EC NO: S2008-0319 DRWN:CMTEO CHKD:CTEH APPR:SHLENI 2007/10/18 2007/10/19 2007/10/22	DESCRIPTION	QUALITY SYMBOLS ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY VMANICKAM	DATE 2006/04/11	TITLE VLP DDR2 DIMM, 1MM PITCH 240 CKTS, VERT. PRESSFIT			
			4 PLACES	± ---	± ---	CHECKED BY SHLENI	DATE 2006/06/22				
			3 PLACES	± ---	± ---	APPROVED BY SRRAMESH	DATE 2006/06/22	MOLEX INCORPORATED			
			2 PLACES	± 0.20	± ---	MATERIAL NO.	DOCUMENT NO.				
			1 PLACE	± ---	± ---	SEE TABLE		SHEET NO. 2 OF 4			
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

10 9 8 7 6 5 4 3 2 1

F

E

D

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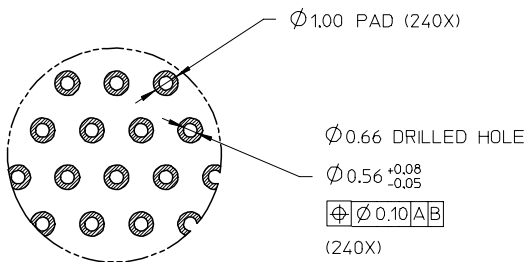
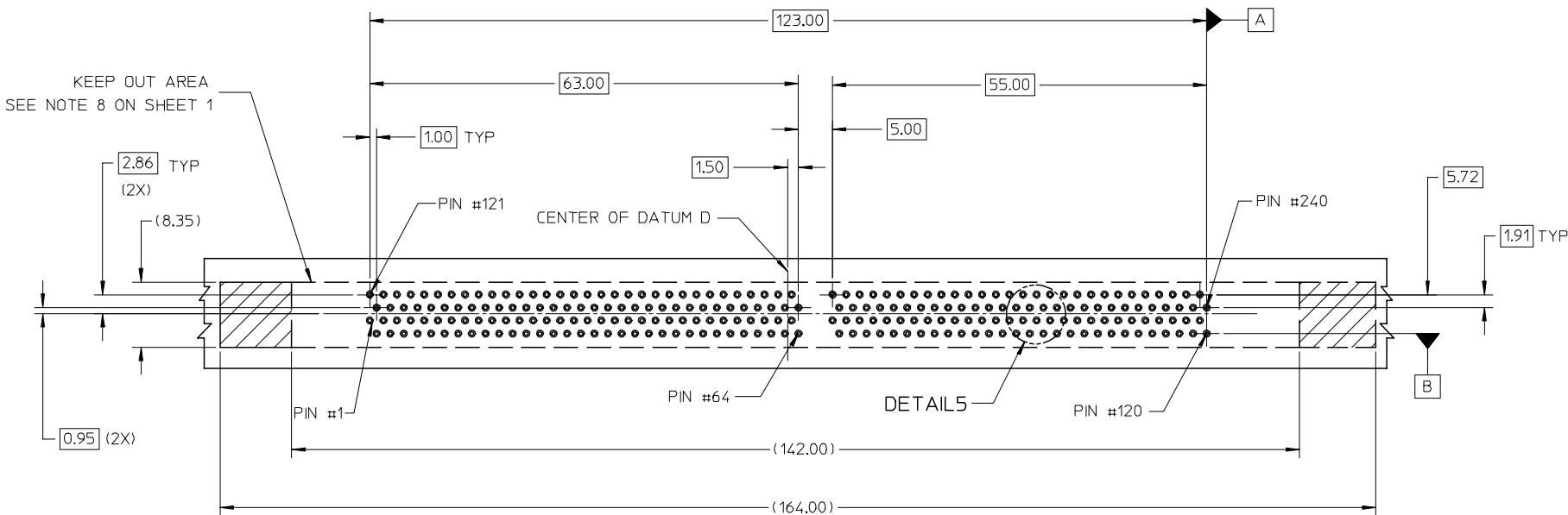
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C

B

A

(SEE RECOMMENDED THICKNESS IN TABLE)
RECOMMEND PCB LAYOUT
CONNECTOR SIDE



DETAIL5

UPDATE M.C DWG EC NO: S2008-0319 DRWN:CMTEO 2007/10/18 CHKD:CTEH 2007/10/19 APPR:SHLENI 2007/10/22 A1			DESCRIPTION REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION				
				 =0  =0		mm	INCH	DRAWN BY	DATE	TITLE VLP DDR2 DIMM, 1MM PITCH 240 CKTS, VERT. PRESSFIT					
					4 PLACES	± ---	± ---	VMANICKAM	2006/04/11						
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					1 PLACE	± ---	± ---	APPROVED BY	DATE						
				ANGULAR ± 5 °		SRRAMESH	2006/06/22	 MOLEX INCORPORATED		DOCUMENT NO. SD-78065-001		SHEET NO. 3 OF 4			
				DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO. SEE TABLE									
		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION											

9 8 7 6 5 4 3 2 1

	10	9	8	7	6	5	4	3	2	1	
F											F
	TIN OPTION										
E											E
D											D
C											C
B											B
A											A
ib_frame_A3_P_AM_T Rev. E 2006/04/15											
	9	8	7	6	5	4	3	2	1		

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DRWN:CMTEO		CHKD:CCTEH		APPR:SHLENI		REV		DESCRIPTION		QUALITY SYMBOLS	
A1										▽=0 ▽=0	

GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE NTS		DESIGN UNITS METRIC		THIRD ANGLE PROJECTION	
			mm INCH		DRAWN BY VMANICKAM		DATE 2006/04/11		TITLE VLP DDR2 DIMM, 1MM PITCH 240 CKTS, VERT. PRESSFIT	
4 PLACES ± ---			± ---		CHECKED BY SHLENI		DATE 2006/06/22			
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2 PLACES ± 0.20			± ---		MATERIAL NO.		DOCUMENT NO.		SHEET NO.	
1 PLACE ± ---			± ---		SEE TABLE		SD-78065-001		4 OF 4	
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