

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 870A-B

HIGH EFFICIENCY MONOLITHIC SYNCHRONOUS BUCK REGULATOR

LTC3410B

DESCRIPTION

Demonstration circuit 870A-B is a high efficiency, high frequency buck converter, incorporating the LTC3410B monolithic synchronous regulator. The DC870 has an input voltage range of 2.25V to 5.5V, and is capable of delivering up to 300 mA of output current. It has an output voltage range from 0.8V to 5V, and an operating frequency range of 2.25 MHz (allowing the exclusive use of low profile surface mount components). In low load current operation, the LTC3410B operates in pulse-skip mode. The

DC870 is a very efficient circuit: up to 96%. These features, plus the LTC3410B coming in a tiny, low-profile 6-Pin SC70 package, make the DC870 demo board an ideal circuit for use in battery-powered, hand-held applications.

Design files for this circuit board are available. Call the LTC factory.

TM - Burst Mode is a trademark of Linear Technology Corporation

Table 1.

Performance Summary ($T_A = 25^\circ\text{C}$)

PARAMETER	CONDITION	VALUE
Minimum Input Voltage		2.25V
Maximum Input Voltage		5.5V
Output Voltage V_{OUT}	$V_{IN} = 2.25\text{V to } 5.5\text{V}, I_{OUT} = 0\text{A to } 0.3\text{A}$	$1.8\text{V} \pm 4\%$
Typical Output Ripple V_{OUT}	$V_{IN} = 5\text{V}, I_{OUT} = 0.3\text{A}$ (20 MHz BW)	20mV _{p-p}
Output Regulation	Line	$\pm 1\%$
	Load	$\pm 1\%$
Nominal Switching Frequency		2.25 MHz

QUICK START PROCEDURE

The DC870A-B is easy to set up to evaluate the performance of the LTC3410B. For proper measurement equipment configuration, set up the circuit according to the diagram in **Figure 1**. Before proceeding to test, insert jumper JP1 shunt into the off position, connecting the RUN pin to ground (GND), which shuts down the circuit.

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or out-

put voltage ripple by touching the probe tip directly across the V_{in} or V_{out} and GND terminals. See Figure 2 for proper scope probe technique.

1. Connect the input power supply and the load to the board. Do not hot-plug V_{in} or increase V_{in} over the rated maximum supply voltage of 5.5V, or the part may be damaged. Refer to figure 1 for the proper measurement equipment setup.

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2. Apply 3.3V at V_{in} . Measure V_{out} ; it should read 0V. If desired, one can measure the shutdown supply current at this point. The supply current will be approximately 1 μ A in shutdown.
 3. Turn on the circuit by inserting the shunt in jumper JP1 into the ON position. The output voltage should be regulating. Measure V_{out} - it should measure 1.8V \pm 2%.
 4. Vary the input voltage from 2.25V to 5.5V and adjust the load current from 0 to 0.3A. V_{out} should read between 1.8V \pm 3%.
 5. Measure the output ripple voltage at any output current level; it usually will measure less than 20 mVAC.
 6. Observe the voltage waveform at the switch node (one pin of the inductor). Verify the switching frequency is between 1.8 MHz and 2.7 MHz ($T = 0.56 \mu$ s and 0.37μ s), and that the switch node waveform is rectangular in shape.
- When finished, turn off the circuit by inserting the shunt in jumper JP1 into the OFF position.

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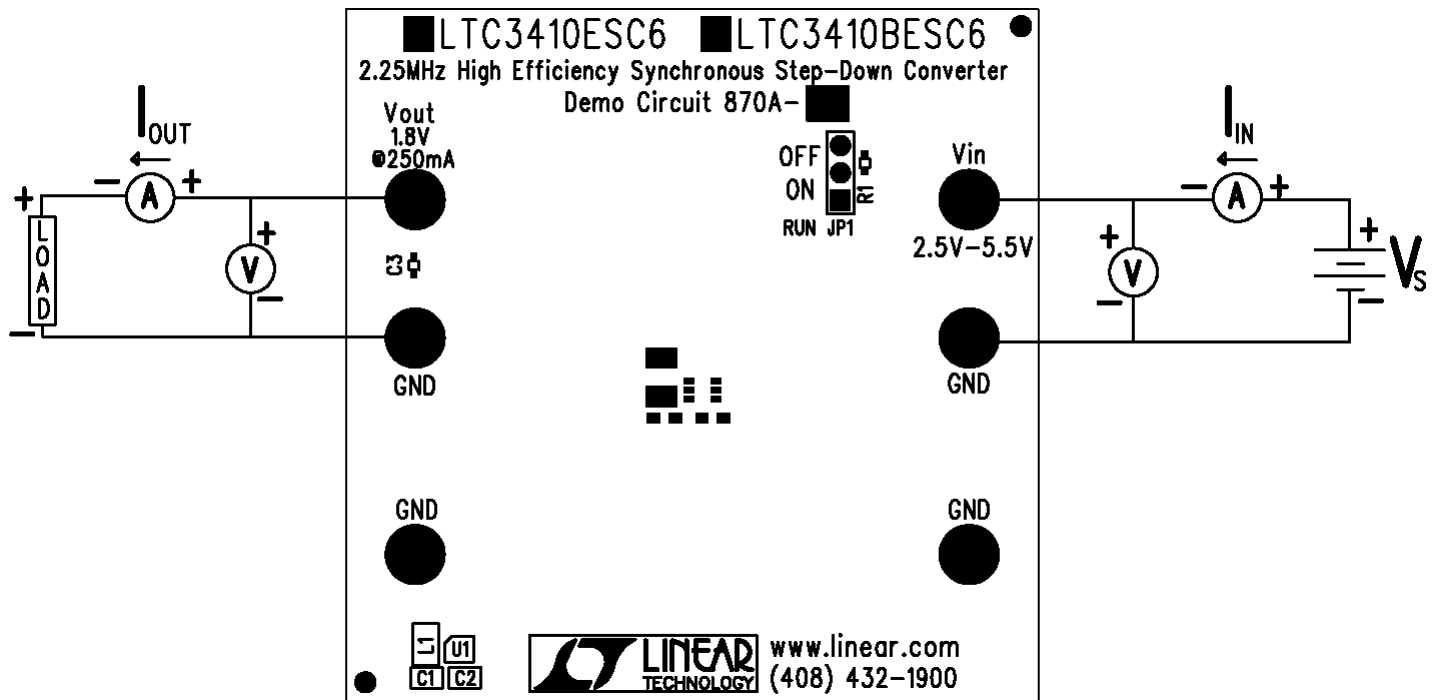


Figure 1. Proper Measurement Equipment Setup

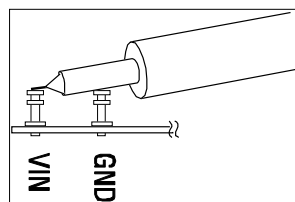


Figure 2. Measuring Input or Output Ripple

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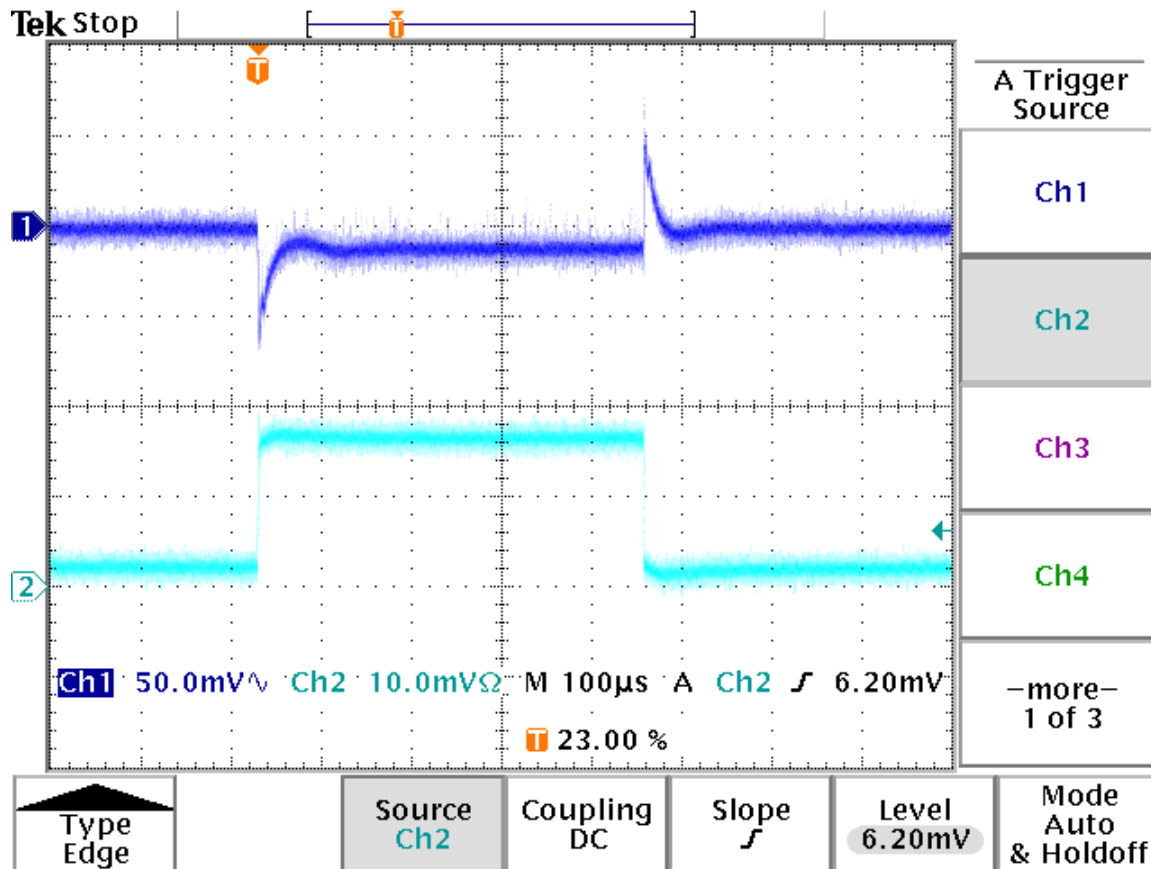


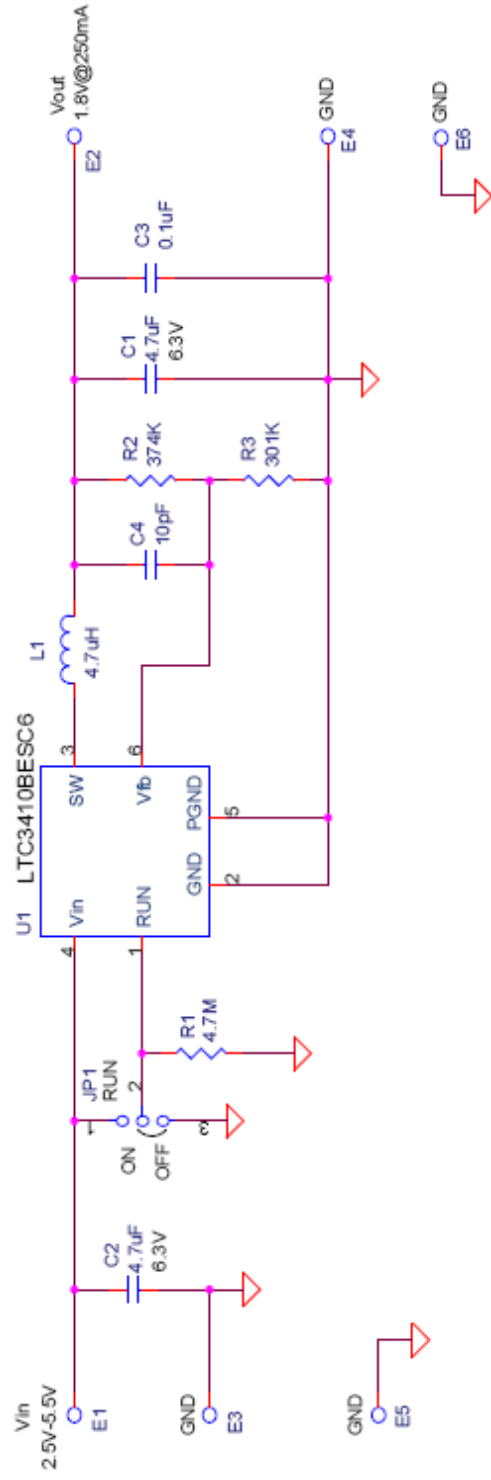
Figure 3. Load Step Response

$V_{in} = 3.3V$, $V_{out} = 1.8V$, I_{out} Step = 0.2A

Trace 1: Output Voltage (50 mV/div AC)

Trace 2: Output Current (0.1A/div)

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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

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CHECKED	
APPROVED	
ENGINEER Tom Gross	1/11/06
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TITLE

LTC3410ESC6 2.25MHz High Efficiency Synchronous
Step-Down Converter

SIZE	CAGE CODE	DWG NO	REV
		DC870A-B	A
SCALE:			SHEET 1 OF 1
FILENAME:			